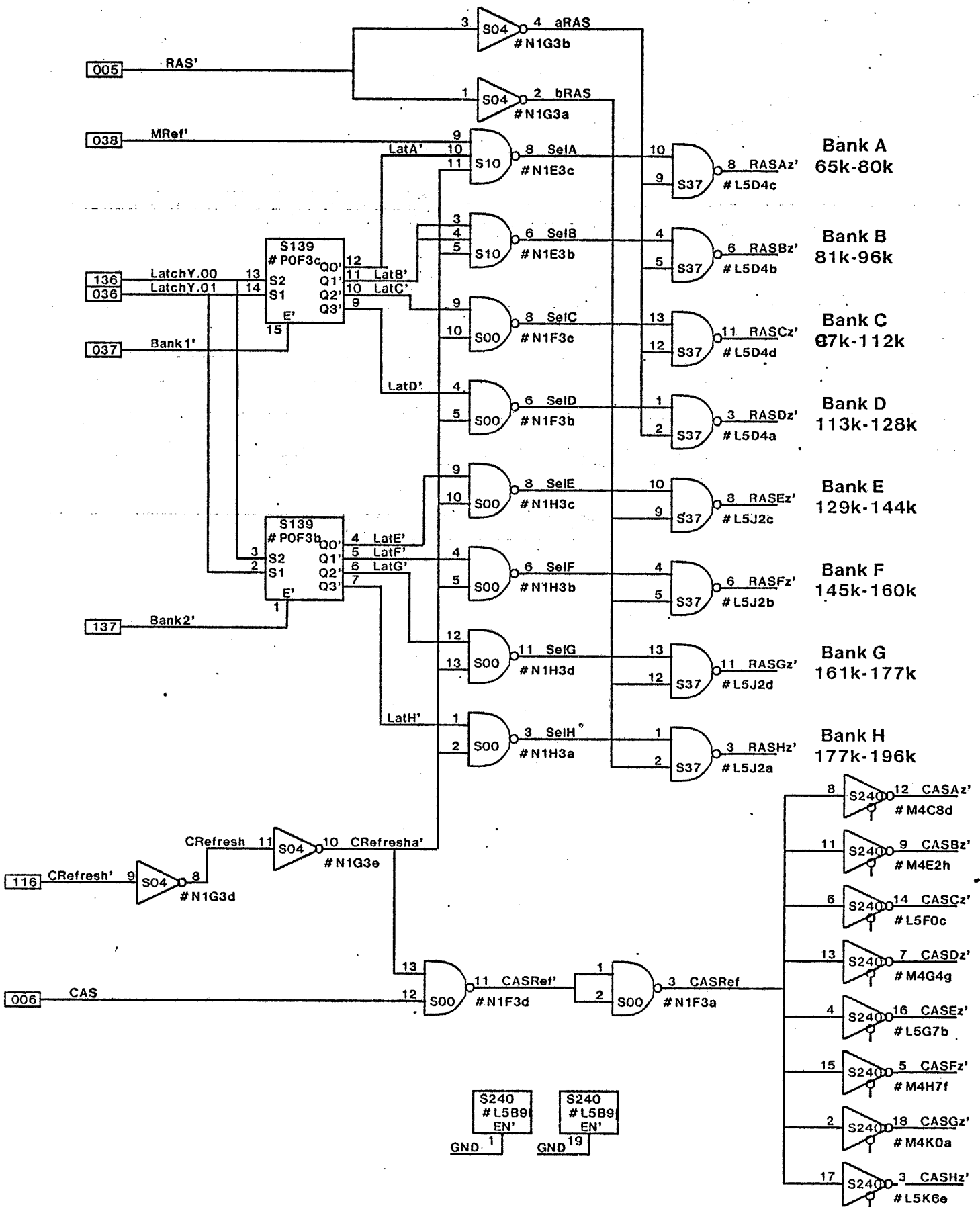
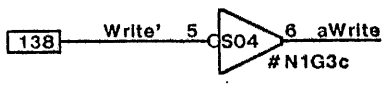
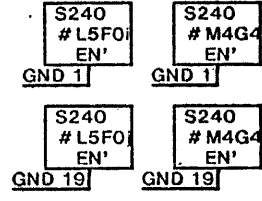
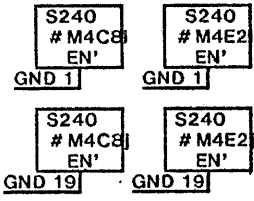
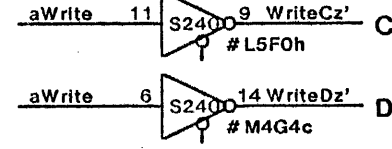
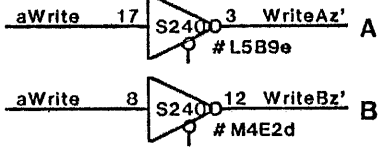
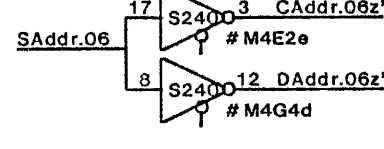
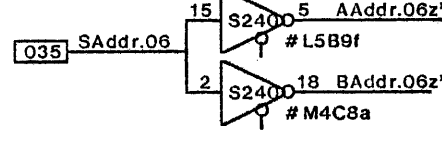
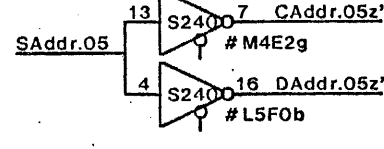
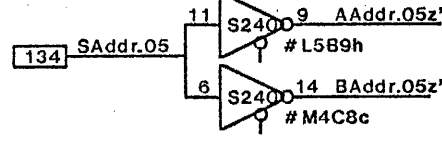
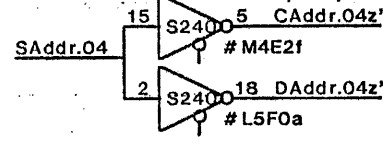
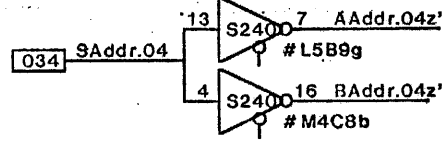
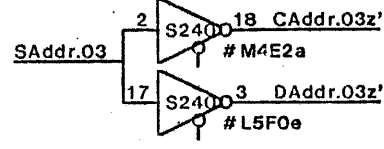
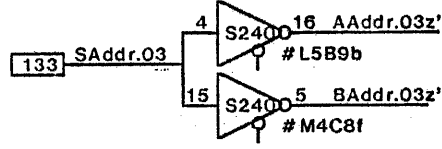
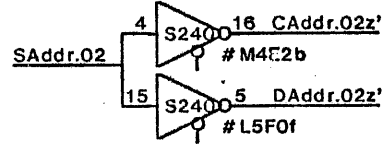
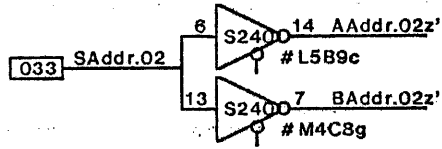
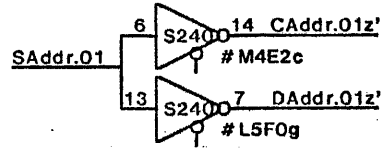
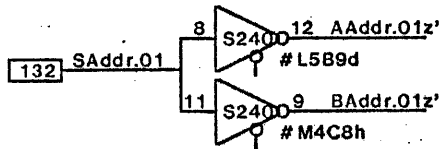
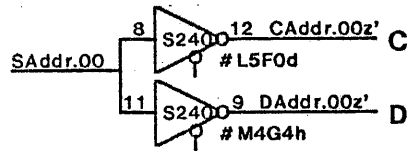
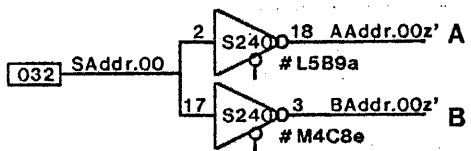


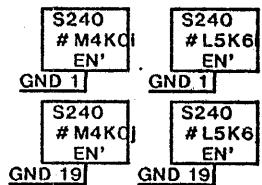
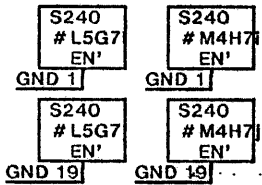
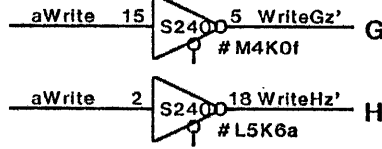
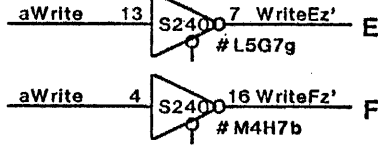
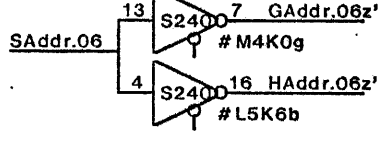
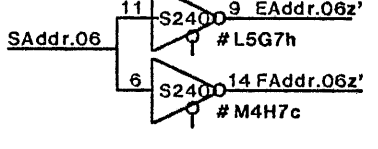
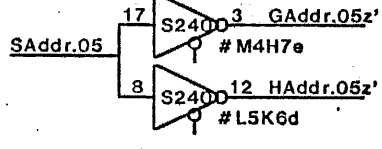
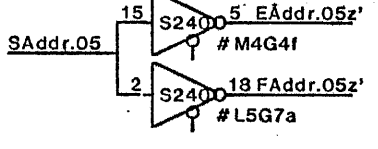
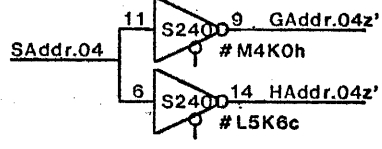
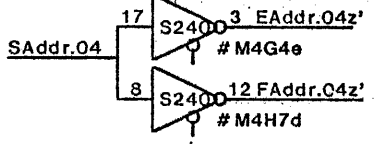
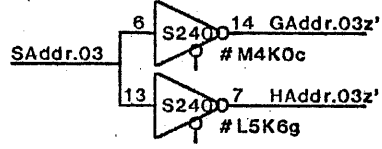
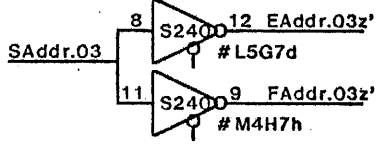
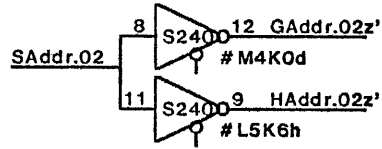
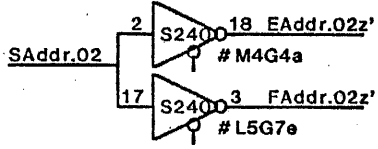
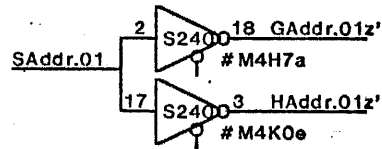
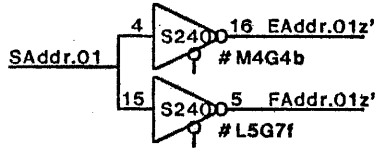
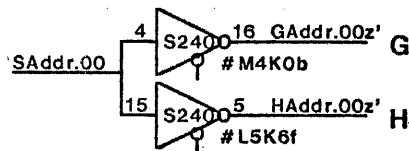
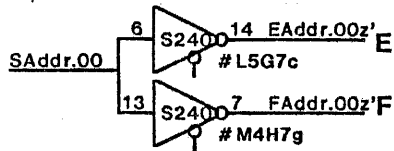
## Storage Card 128k Printed Circuit Version

1. Bank Selection, CAS' drivers, RAS' drivers
2. Address drivers Bank A, B, C & D
3. Address drivers Bank E, F, G & H
4. Bank A, AAddr.00' - 06', CASA', RASA', WriteA'
5. Bank B, BAddr.00 - 06, CASB', RASB', WriteB'
6. Bank C, CAddr.00' - 06', CASC', RASC', WriteC'
7. Bank D, DAddr.00, - 06, CasD', RASD', WriteD'
8. Bank E, EAddr.00' - 06', RASE', CASE', WriteE'
9. Bank F, FAddr.00 - 06, CASF', RASF', WriteF'
10. Bank G, GAddr.00' - 06', CASG', RASG', WriteG'
11. Bank H, HAddr.00 - 06, CASH', RASH', WriteH'
12. Data section, Bank A, B, C & D
13. Data section, Bank E, F, G & H
14. Data Input Buffers
15. Data Output Buffers
16. Resistors
17. Resistors and R-Dips
18. Caps, Diodes and Fuses
19. Test Points
20. Material List

XEROX SDD	Project WS	128k Printed Circuit Storage Card Drawings	Reference	File ESTG00.sil	Designer JR Cucinitti	Rev D	Date 3/18/80	Page 00
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SDI.00'	2	# A6B8b14 MemData.00'	2	# A6C9b14 MemData.00'	2	# A6E0b14 MemData.00'	2	# A6F1b14 MemData.00'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.01'	2	# B0B8b14 MemData.01'	2	# B0C9b14 MemData.01'	2	# B0E0b14 MemData.01'	2	# B0F1b14 MemData.01'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.02'	2	# B4B8b14 MemData.02'	2	# B4C9b14 MemData.02'	2	# B4E0b14 MemData.02'	2	# B4F1b14 MemData.02'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.03'	2	# B8B8b14 MemData.03'	2	# B8C9b14 MemData.03'	2	# B8E0b14 MemData.03'	2	# B8F1b14 MemData.03'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.04'	2	# C2B8b14 MemData.04'	2	# C2C9b14 MemData.04'	2	# C2E0b14 MemData.04'	2	# C2F1b14 MemData.04'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.05'	2	# C6B8b14 MemData.05'	2	# C6C9b14 MemData.05'	2	# C6E0b14 MemData.05'	2	# C6F1b14 MemData.05'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.06'	2	# D0B8b14 MemData.06'	2	# D0C9b14 MemData.06'	2	# D0E0b14 MemData.06'	2	# D0F1b14 MemData.06'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.07'	2	# D4B8b14 MemData.07'	2	# D4C9b14 MemData.07'	2	# D4E0b14 MemData.07'	2	# D4F1b14 MemData.07'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.08'	2	# D8B8b14 MemData.08'	2	# D8C9b14 MemData.08'	2	# D8E0b14 MemData.08'	2	# D8F1b14 MemData.08'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.09'	2	# E2B8b14 MemData.09'	2	# E2C9b14 MemData.09'	2	# E2E0b14 MemData.09'	2	# E2F1b14 MemData.09'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.10'	2	# E6B8b14 MemData.10'	2	# E6C9b14 MemData.10'	2	# E6E0b14 MemData.10'	2	# E6F1b14 MemData.10'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.11'	2	# F0B8b14 MemData.11'	2	# F0C9b14 MemData.11'	2	# F0E0b14 MemData.11'	2	# F0F1b14 MemData.11'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.12'	2	# F4B8b14 MemData.12'	2	# F4C9b14 MemData.12'	2	# F4E0b14 MemData.12'	2	# F4F1b14 MemData.12'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.13'	2	# F8B8b14 MemData.13'	2	# F8C9b14 MemData.13'	2	# F8E0b14 MemData.13'	2	# F8F1b14 MemData.13'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.14'	2	# G2B8b14 MemData.14'	2	# G2C9b14 MemData.14'	2	# G2E0b14 MemData.14'	2	# G2F1b14 MemData.14'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.15'	2	# G6B8b14 MemData.15'	2	# G6C9b14 MemData.15'	2	# G6E0b14 MemData.15'	2	# G6F1b14 MemData.15'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.16'	2	# H0B8b14 MemData.16'	2	# H0C9b14 MemData.16'	2	# H0G0b14 MemData.16'	2	# H0F1b14 MemData.16'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.17'	2	# H4B8b14 MemData.17'	2	# H4C9b14 MemData.17'	2	# H4E0b14 MemData.17'	2	# H4F1b14 MemData.17'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.18'	2	# H8B8b14 MemData.18'	2	# H8C9b14 MemData.18'	2	# H8E0b14 MemData.18'	2	# H8F1b14 MemData.18'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.19'	2	# J2B8b14 MemData.19'	2	# J2C9b14 MemData.19'	2	# J2E0b14 MemData.19'	2	# J2F1b14 MemData.19'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.20'	2	# J6B8b14 MemData.20'	2	# J6C9b14 MemData.20'	2	# J6E0b14 MemData.20'	2	# J6F1b14 MemData.20'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.21'	2	# K0B8b14 MemData.21'	2	# K0C9b14 MemData.21'	2	# K0E0b14 MemData.21'	2	# K0F1b14 MemData.21'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2

Bank A

Bank B

Bank C

Bank D

XEROX SDD	Project WS	A-B-C-D Data Section Work Station Storage Card	File EStg12.sil	Designer JR Cucinitti	Rev D	Date 2/20/80	Page 12
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SDI.00'	2	# A6G2b14 MemData.00'	2	# A6H3b14 MemData.00'	2	# A6D4b14 MemData.00'	2	# A6K5b14 MemData.00'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.01'	2	# B0G2b14 MemData.01'	2	# B0H3b14 MemData.01'	2	# B0J4b14 MemData.01'	2	# B0K5b14 MemData.01'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.02'	2	# B4G2b14 MemData.02'	2	# B4H3b14 MemData.02'	2	# B4J4b14 MemData.02'	2	# B4K5b14 MemData.02'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.03'	2	# B8G2b14 MemData.03'	2	# B8H3b14 MemData.03'	2	# B8J4b14 MemData.03'	2	# B8K5b14 MemData.03'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.04'	2	# C2G2b14 MemData.04'	2	# C2H3b14 MemData.04'	2	# C2J4b14 MemData.04'	2	# C2K5b14 MemData.04'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.05'	2	# C6G2b14 MemData.05'	2	# C6H3b14 MemData.05'	2	# C6J4b14 MemData.05'	2	# C6K5b14 MemData.05'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.06'	2	# D0G2b14 MemData.06'	2	# D0H3b14 MemData.06'	2	# D0J4b14 MemData.06'	2	# D0K5b14 MemData.06'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.07'	2	# D4G2b14 MemData.07'	2	# D4H3b14 MemData.07'	2	# D4J4b14 MemData.07'	2	# D4K5b14 MemData.07'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.08'	2	# D8G2b14 MemData.08'	2	# D8H3b14 MemData.08'	2	# D8J4b14 MemData.08'	2	# D8K5b14 MemData.08'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.09'	2	# E2G2b14 MemData.09'	2	# E2H3b14 MemData.09'	2	# E2J4b14 MemData.09'	2	# E2K5b14 MemData.09'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.10'	2	# E6G2b14 MemData.10'	2	# E6H3b14 MemData.10'	2	# E6J4b14 MemData.10'	2	# E6K5b14 MemData.10'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.11'	2	# F0G2b14 MemData.11'	2	# F0H3b14 MemData.11'	2	# F0J4b14 MemData.11'	2	# F0K5b14 MemData.11'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.12'	2	# F4G2b14 MemData.12'	2	# F4H3b14 MemData.12'	2	# F4J4b14 MemData.12'	2	# F4K5b14 MemData.12'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.13'	2	# F8G2b14 MemData.13'	2	# F8H3b14 MemData.13'	2	# F8J4b14 MemData.13'	2	# F8K5b14 MemData.13'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.14'	2	# G2G2b14 MemData.14'	2	# G2H3b14 MemData.14'	2	# G2J4b14 MemData.14'	2	# G2K5b14 MemData.14'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.15'	2	# G6G2b14 MemData.15'	2	# G6H3b14 MemData.15'	2	# G6J4b14 MemData.15'	2	# G6K5b14 MemData.15'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.16'	2	# H0G2b14 MemData.16'	2	# H0H3b14 MemData.16'	2	# H0J4b14 MemData.16'	2	# H0K5b14 MemData.16'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.17'	2	# H4G2b14 MemData.17'	2	# H4H3b14 MemData.17'	2	# H4J4b14 MemData.17'	2	# H4K5b14 MemData.17'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.18'	2	# H8G2b14 MemData.18'	2	# H8H3b14 MemData.18'	2	# H8J4b14 MemData.18'	2	# H8K5b14 MemData.18'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.19'	2	# J2G2b14 MemData.19'	2	# J2H3b14 MemData.19'	2	# J2J4b14 MemData.19'	2	# J2K5b14 MemData.19'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.20'	2	# J6G2b14 MemData.20'	2	# J6H3b14 MemData.20'	2	# J6J4b14 MemData.20'	2	# J6K5b14 MemData.20'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2
SDI.21'	2	# K0G2b14 MemData.21'	2	# K0H3b14 MemData.21'	2	# K0J4b14 MemData.21'	2	# K0K5b14 MemData.21'
		MK 16-2		MK 16-2		MK 16-2		MK 16-2

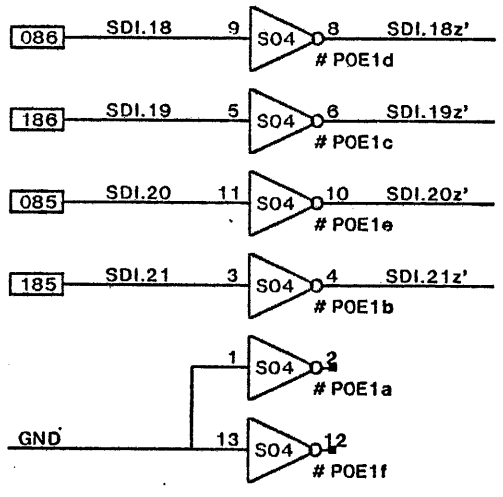
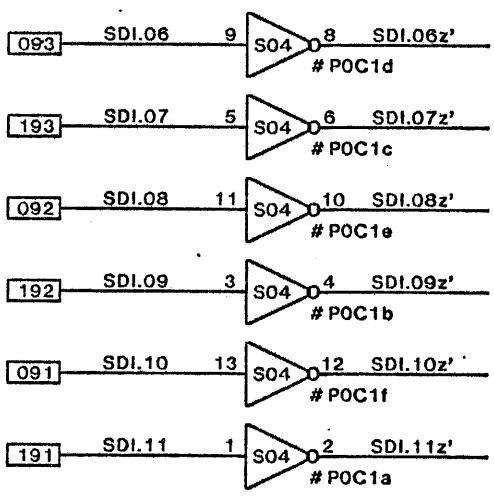
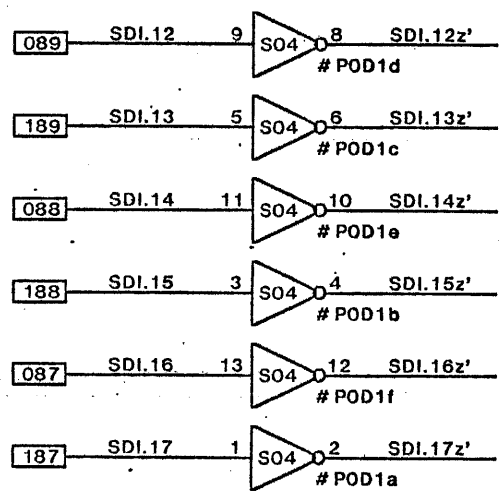
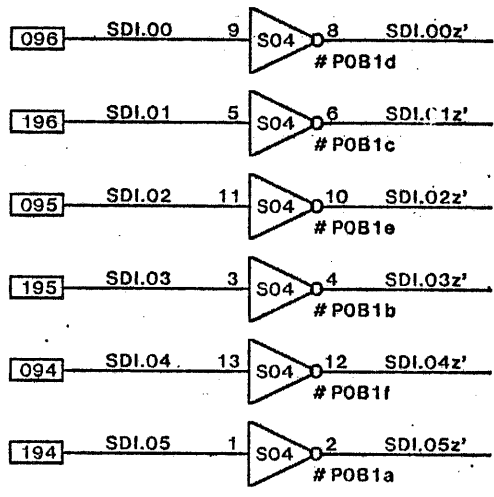
Bank E

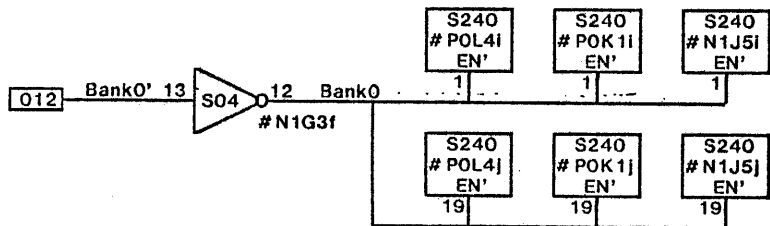
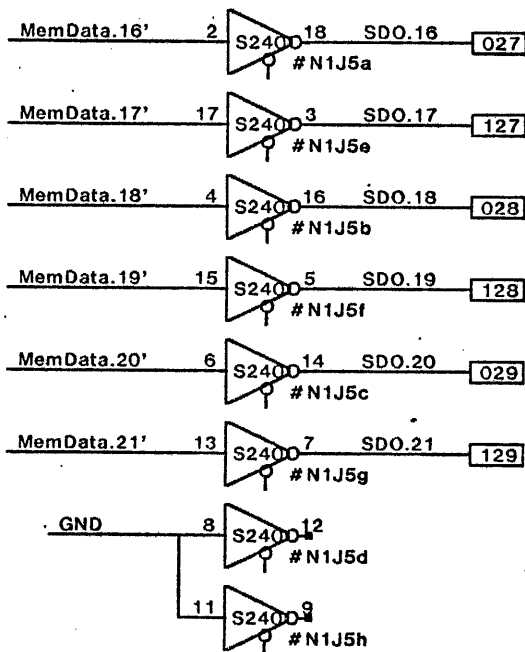
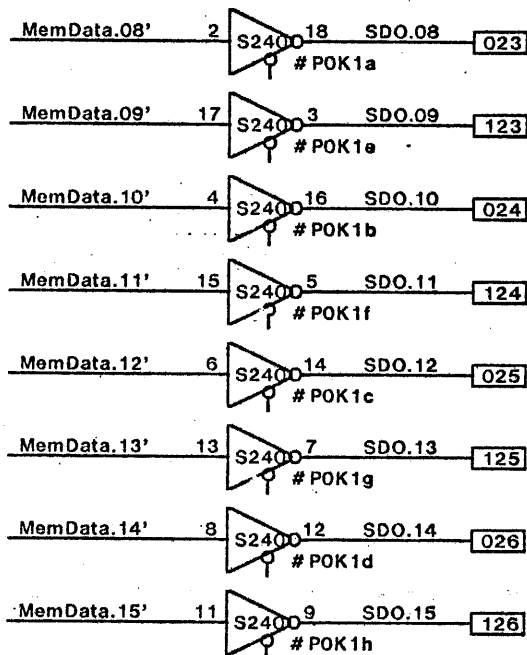
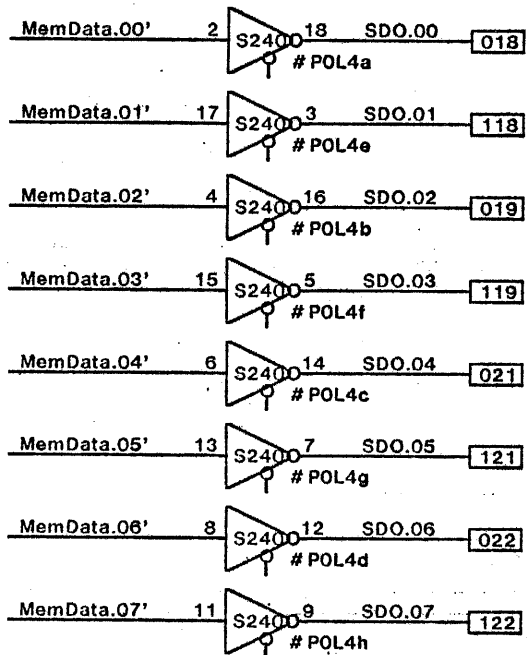
Bank F

Bank G

Bank H

XEROX SDD	Project WS	Memory Data Bank E-F-G-H Work Station Storage Card	File ESTg13.sil	Designer JR Cucinitti	Rev D	Date 2/20/80	Page 13
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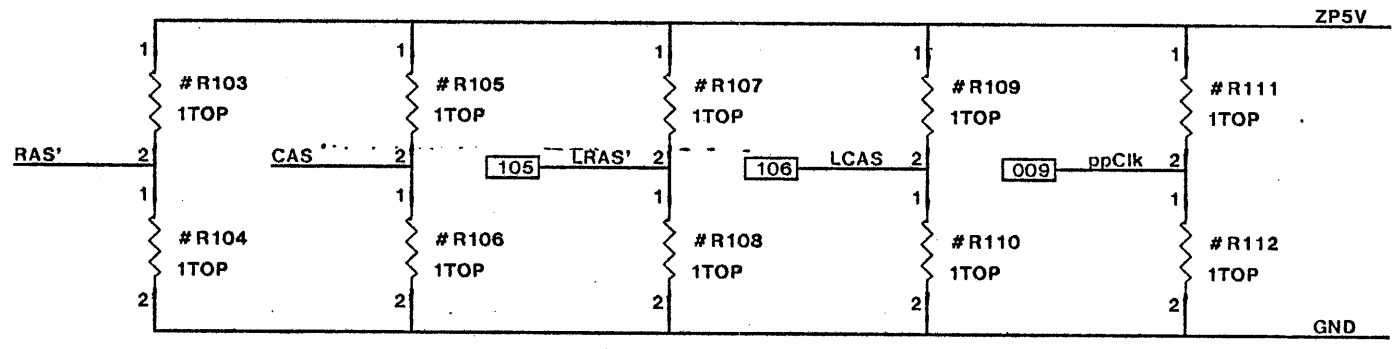
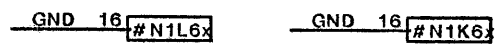
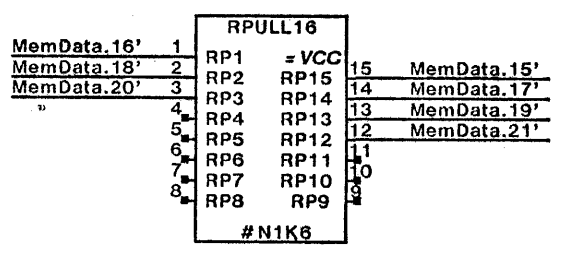
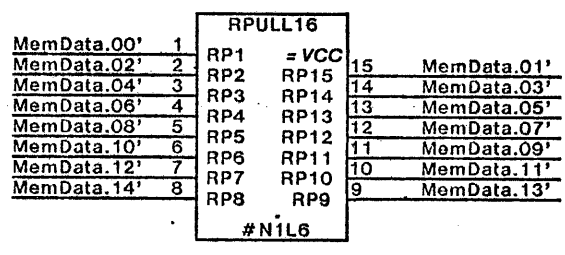
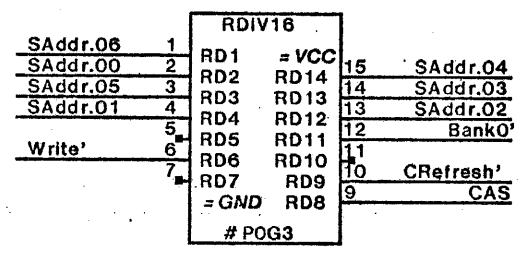
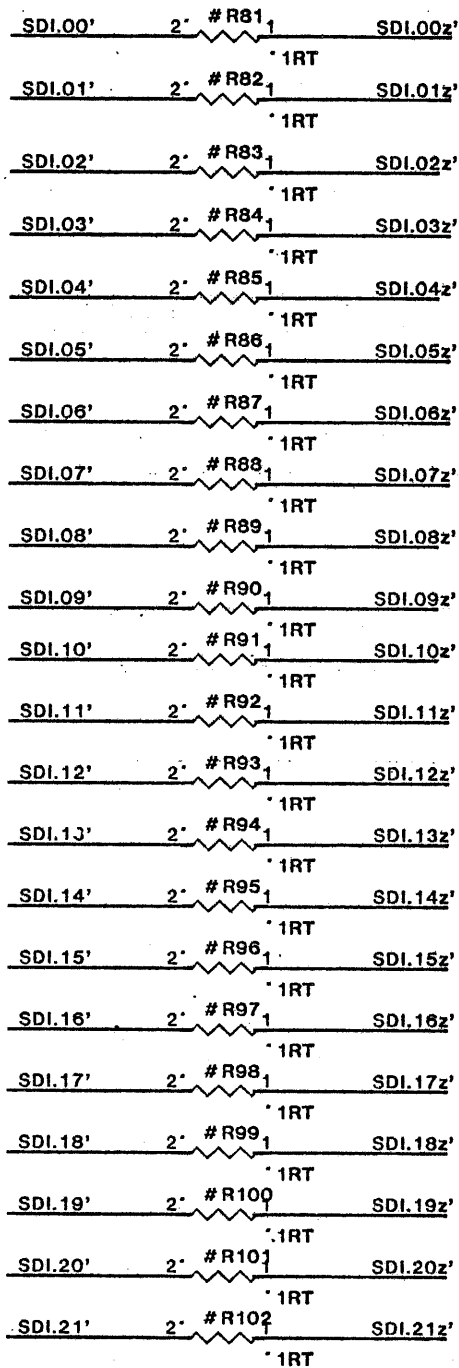


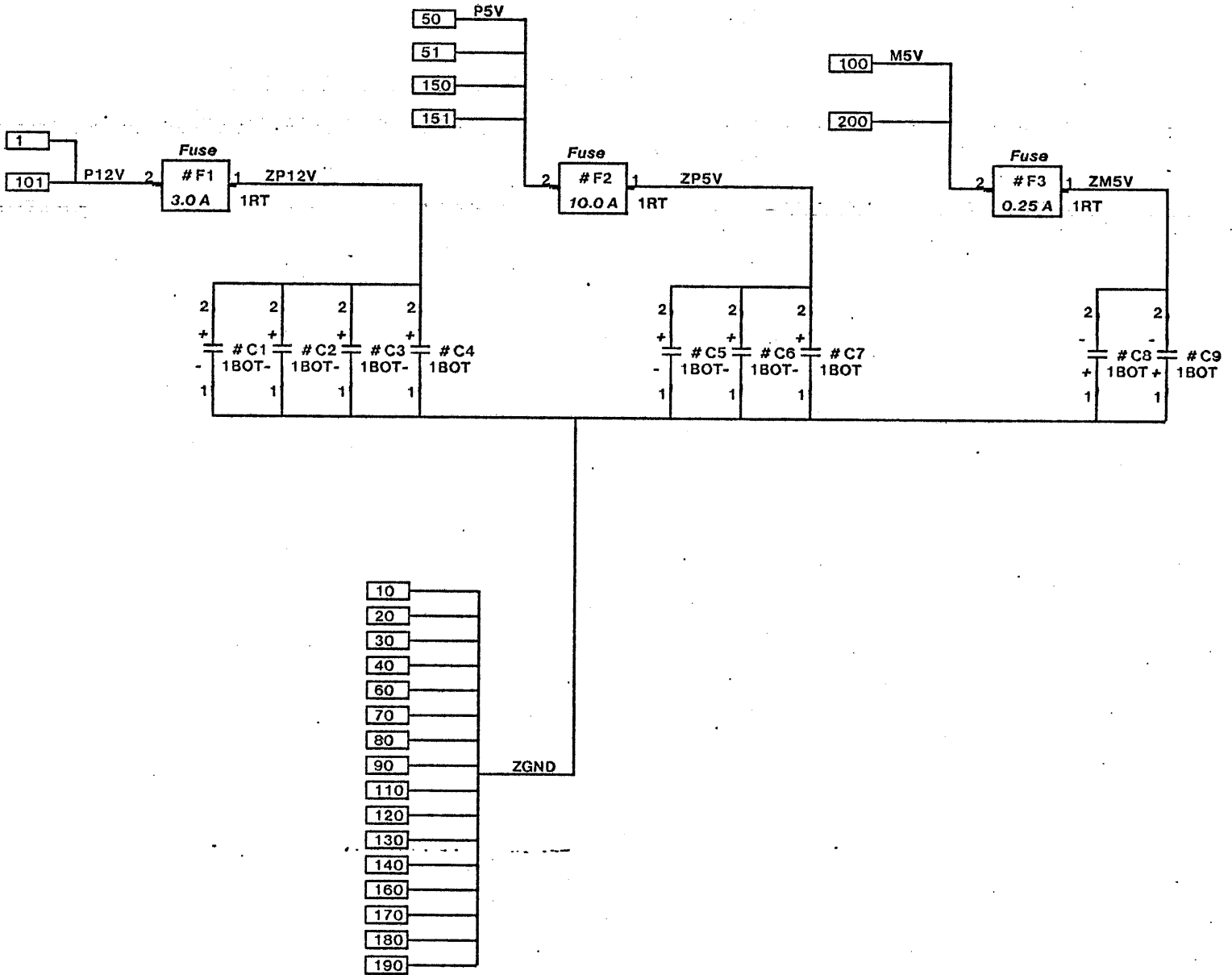
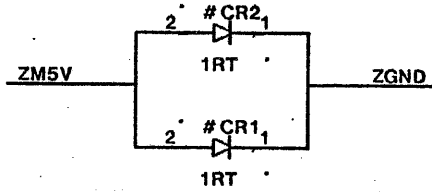


AAddr.05' 2' #R1 1 AAddr.05z' 1RT  
 AAddr.01' 2' #R2 1 AAddr.01z' 1RT  
 AAddr.04' 2' #R3 1 AAddr.04z' 1RT  
 AAddr.02' 2' #R4 1 AAddr.02z' 1RT  
 AAddr.06' 2' #R5 1 AAddr.06z' 1RT  
 AAddr.03' 2' #R6 1 AAddr.03z' 1RT  
 RASA' 2' #R7 1 RASAz' 1RT  
 AAddr.00' 2' #R8 1 AAddr.00z' 1RT  
 WriteA' 2' #R9 1 WriteAz' 1RT  
 CASA' 2' #R10 1 CASAz' 1RT  
 BAddr.05' 2' #R11 1 BAddr.05z' 1RT  
 BAddr.01' 2' #R12 1 BAddr.01z' 1RT  
 BAddr.04' 2' #R13 1 BAddr.04z' 1RT  
 BAddr.02' 2' #R14 1 BAddr.02z' 1RT  
 BAddr.06' 2' #R15 1 BAddr.06z' 1RT  
 BAddr.03' 2' #R16 1 BAddr.03z' 1RT  
 RASB' 2' #R17 1 RASBz' 1RT  
 BAddr.00' 2' #R18 1 BAddr.00z' 1RT  
 WriteB' 2' #R19 1 WriteBz' 1RT  
 CASB' 2' #R20 1 CASBz' 1RT  
 CAddr.05' 2' #R21 1 CAddr.05z' 1RT  
 CAddr.01' 2' #R22 1 CAddr.01z' 1RT  
 CAddr.04' 2' #R23 1 CAddr.04z' 1RT  
 CAddr.02' 2' #R24 1 CAddr.02z' 1RT  
 CAddr.06' 2' #R25 1 CAddr.06z' 1RT  
 CAddr.03' 2' #R26 1 CAddr.03z' 1RT  
 RASC' 2' #R27 1 RASCz' 1RT

CAddr.00' 2' #R28 1 CAddr.00z' 1RT  
 WriteC' 2' #R29 1 WriteCz' 1RT  
 CASC' 2' #R30 1 CASCz' 1RT  
 DAddr.05' 2' #R31 1 DAddr.05z' 1RT  
 DAddr.01' 2' #R32 1 DAddr.01z' 1RT  
 DAddr.04' 2' #R33 1 DAddr.04z' 1RT  
 DAddr.02' 2' #R34 1 DAddr.02z' 1RT  
 DAddr.06' 2' #R35 1 DAddr.06z' 1RT  
 DAddr.03' 2' #R36 1 DAddr.03z' 1RT  
 RASD' 2' #R37 1 RASDz' 1RT  
 DAddr.00' 2' #R38 1 DAddr.00z' 1RT  
 WriteD' 2' #R39 1 WriteDz' 1RT  
 CASD' 2' #R40 1 CASDz' 1RT  
 EAddr.05' 2' #R41 1 EAddr.05z' 1RT  
 EAddr.01' 2' #R42 1 EAddr.01z' 1RT  
 EAddr.04' 2' #R43 1 EAddr.04z' 1RT  
 EAddr.02' 2' #R44 1 EAddr.02z' 1RT  
 EAddr.06' 2' #R45 1 EAddr.06z' 1RT  
 EAddr.03' 2' #R46 1 EAddr.03z' 1RT  
 RASE' 2' #R47 1 RASEz' 1RT  
 EAddr.00' 2' #R48 1 EAddr.00z' 1RT  
 WriteE' 2' #R49 1 WriteEz' 1RT  
 CASE' 2' #R50 1 CASEz' 1RT  
 FAddr.05' 2' #R51 1 FAddr.05z' 1RT  
 FAddr.01' 2' #R52 1 FAddr.01z' 1RT  
 FAddr.04' 2' #R53 1 FAddr.04z' 1RT  
 FAddr.02' 2' #R54 1 FAddr.02z' 1RT

FAddr.06' 2' #R55 1 FAddr.06z' 1RT  
 FAddr.03' 2' #R56 1 FAddr.03z' 1RT  
 RASF' 2' #R57 1 RASFz' 1RT  
 FAddr.00' 2' #R58 1 FAddr.00z' 1RT  
 WriteF' 2' #R59 1 WriteFz' 1RT  
 CASF' 2' #R60 1 CASFz' 1RT  
 GAddr.05' 2' #R61 1 GAddr.05z' 1RT  
 GAddr.01' 2' #R62 1 GAddr.01z' 1RT  
 GAddr.04' 2' #R63 1 GAddr.04z' 1RT  
 GAddr.02' 2' #R64 1 GAddr.02z' 1RT  
 GAddr.06' 2' #R65 1 GAddr.06z' 1RT  
 GAddr.03' 2' #R66 1 GAddr.03z' 1RT  
 RASG' 2' #R67 1 RASGz' 1RT  
 GAddr.00' 2' #R68 1 GAddr.00z' 1RT  
 WriteG' 2' #R69 1 WriteGz' 1RT  
 CASG' 2' #R70 1 CASGz' 1RT  
 HAddr.05' 2' #R71 1 HAddr.05z' 1RT  
 HAddr.01' 2' #R72 1 HAddr.01z' 1RT  
 HAddr.04' 2' #R73 1 HAddr.04z' 1RT  
 HAddr.02' 2' #R74 1 HAddr.02z' 1RT  
 HAddr.06' 2' #R75 1 HAddr.06z' 1RT  
 HAddr.03' 2' #R76 1 HAddr.03z' 1RT  
 RASH' 2' #R77 1 RASHz' 1RT  
 HAddr.00' 2' #R78 1 HAddr.00z' 1RT  
 WriteH' 2' #R79 1 WriteHz' 1RT  
 CASH' 2' #R80 1 CASHz' 1RT





AAddr.00' 1  ti # TP001  
 RASA' 1  ti # TP002  
 AAddr.03' 1  ti # TP003  
 AAddr.06' 1  ti # TP004  
 AAddr.02' 1  ti # TP005  
 AAddr.04' 1  ti # TP006  
 AAddr.01' 1  ti # TP007  
 AAddr.05' 1  ti # TP008

EAddr.00' 1  ti # TP041  
 RASE' 1  ti # TP042  
 EAddr.03' 1  ti # TP043  
 EAddr.06' 1  ti # TP044  
 EAddr.02' 1  ti # TP045  
 EAddr.04' 1  ti # TP046  
 EAddr.01' 1  ti # TP047  
 EAddr.05' 1  ti # TP048

SDI.07' 1  ti # TP081  
 SDI.06' 1  ti # TP082  
 SDI.05' 1  ti # TP083  
 SDI.04' 1  ti # TP084  
 SDI.03' 1  ti # TP085  
 SDI.02' 1  ti # TP086  
 SDI.01' 1  ti # TP087  
 SDI.00' 1  ti # TP088

BAddr.03' 1  ti # TP009  
 BAddr.06' 1  ti # TP010  
 BAddr.02' 1  ti # TP011  
 BAddr.04' 1  ti # TP012  
 BAddr.01' 1  ti # TP013  
 BAddr.05' 1  ti # TP014  
 CASA' 1  ti # TP015  
 WriteA' 1  ti # TP016

FAddr.03' 1  ti # TP049  
 FAddr.06' 1  ti # TP050  
 FAddr.02' 1  ti # TP051  
 FAddr.04' 1  ti # TP052  
 FAddr.01' 1  ti # TP053  
 FAddr.05' 1  ti # TP054  
 CASE' 1  ti # TP055  
 WriteE' 1  ti # TP056

SDI.15' 1  ti # TP089  
 SDI.14' 1  ti # TP090  
 SDI.13' 1  ti # TP091  
 SDI.12' 1  ti # TP092  
 SDI.11' 1  ti # TP093  
 SDI.10' 1  ti # TP094  
 SDI.09' 1  ti # TP095  
 SDI.08' 1  ti # TP096

CAddr.02' 1  ti # TP017  
 CAddr.04' 1  ti # TP018  
 CAddr.01' 1  ti # TP019  
 CAddr.05' 1  ti # TP020  
 CASB' 1  ti # TP021  
 WriteB' 1  ti # TP022  
 BAddr.00' 1  ti # TP023  
 RASB' 1  ti # TP024

GAddr.02' 1  ti # TP057  
 GAddr.04' 1  ti # TP058  
 GAddr.01' 1  ti # TP059  
 GAddr.05' 1  ti # TP060  
 CASF' 1  ti # TP061  
 WriteF' 1  ti # TP062  
 FAddr.00' 1  ti # TP063  
 RASF' 1  ti # TP064

MemData.00' 1  ti # TP103  
 MemData.01' 1  ti # TP104  
 MemData.02' 1  ti # TP105  
 MemData.03' 1  ti # TP106  
 MemData.04' 1  ti # TP107  
 MemData.05' 1  ti # TP108  
 MemData.06' 1  ti # TP109  
 MemData.07' 1  ti # TP110

DAddr.01' 1  ti # TP025  
 DAddr.05' 1  ti # TP026  
 CASC' 1  ti # TP027  
 WriteC' 1  ti # TP028  
 CAddr.00' 1  ti # TP029  
 RASC' 1  ti # TP030  
 CAddr.03' 1  ti # TP031  
 CAddr.06' 1  ti # TP032

HAddr.01' 1  ti # TP065  
 HAddr.05' 1  ti # TP066  
 CASG' 1  ti # TP067  
 WriteG' 1  ti # TP068  
 GAddr.00' 1  ti # TP069  
 RASG' 1  ti # TP070  
 GAddr.03' 1  ti # TP071  
 GAddr.06' 1  ti # TP072

MemData.08' 1  ti # TP111  
 MemData.09' 1  ti # TP112  
 MemData.10' 1  ti # TP113  
 MemData.11' 1  ti # TP114  
 MemData.12' 1  ti # TP115  
 MemData.13' 1  ti # TP116  
 MemData.14' 1  ti # TP117  
 MemData.15' 1  ti # TP118

CASD' 1  ti # TP033  
 WriteD' 1  ti # TP034  
 DAddr.00' 1  ti # TP035  
 RASD' 1  ti # TP036  
 DAddr.03' 1  ti # TP037  
 DAddr.06' 1  ti # TP038  
 DAddr.02' 1  ti # TP039  
 DAddr.04' 1  ti # TP040

CASH' 1  ti # TP073  
 WriteH' 1  ti # TP074  
 HAddr.00' 1  ti # TP075  
 RASH' 1  ti # TP076  
 HAddr.03' 1  ti # TP077  
 HAddr.06' 1  ti # TP078  
 HAddr.02' 1  ti # TP079  
 HAddr.04' 1  ti # TP080

MemData.16' 1  ti # TP119  
 MemData.17' 1  ti # TP120  
 MemData.18' 1  ti # TP121  
 MemData.19' 1  ti # TP122  
 MemData.20' 1  ti # TP123  
 MemData.21' 1  ti # TP124

ITEM	DESCRIPTION	DWG #	QTY	REMARKS
1	INTEGRATED CIRCUIT SN74S00	733w00318	2	
2	INTEGRATED CIRCUIT SN74S04	733w00319	5	
3	INTEGRATED CIRCUIT SN74S10	733w01606	1	
4	INTEGRATED CIRCUIT SN74S37	733w02136	2	
5	INTEGRATED CIRCUIT SN74S139	733w01669	1	
6	INTEGRATED CIRCUIT SN74S240	733w01633	12	
7	MEMORY CHIP 16K MK4116-2	733w01512	176	
8	CAPACITOR 0.1uf 50v	102P20600	195	
9	CAPACITOR 20uf 15v	702W07301	5	C5 --> C9 (alt. 25uf 12v 702W05601)
10	CAPACITOR 10uf 25v	702W08901	4	C1 --> C4
11	DIODE 1N5820	107P10105	2	CR1 + CR2
12	FUSE 0.25 A	708W10302	1	F3
13	FUSE 3.0 A	708W11002	1	F1
14	FUSE 10.0 A	708W11402	1	F2
15	R-DIP 1.5K ohm	703W13691	2	#N1K6 = #N1L6
16	R-DIP 1.5K/3.3K ohm	703W30591	1	#P0G3
17	RESISTOR 18 ohm 1/4 watt 5 %	703W28088	24	R7,R9,R10,R17,R19,R20,R27,R29,R30, R37,R39,R40,R47,R49,R50,R57,R59,R60, R67,R69,R70,R77,R79 + R80
18	RESISTOR 20 ohm 1/4 watt 5 %	703W28188	56	R1 --> R6,R8,R11 --> R16,R18 R21 --> R26,R28,R31 --> R36,R38, R41 --> R46,R48,R51 --> R56,R58, R61 --> R66,R68,R71, --> R76 + R78
19	RESISTOR 27 ohm 1/4 watt 5 %	703W28488	22	R81 --> R102
20	RESISTOR 220 ohm 1/4 watt 5 %	703W30688	10	R103 --> R112
21	PWB	140P11229	1	
22	BOARD EXTRACTOR	003P80513	2	
23	STIFFENER (front)	596P54167	1	
24	STIFFENER (back)	030P83244	1	
25	RIVETS	320W13201	7	