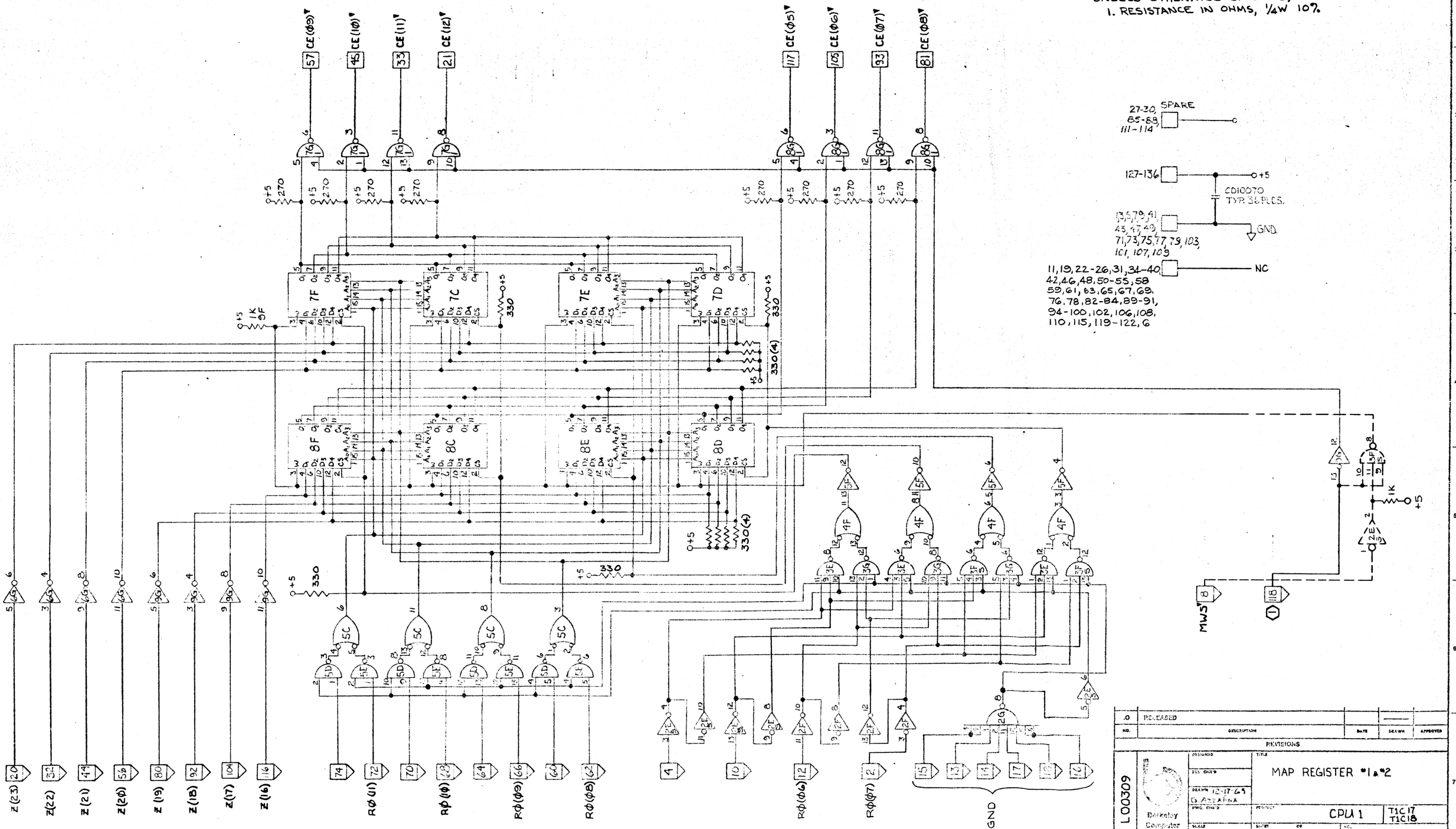
NOTE:

(1) MAP REGISTER #1 (C17) - RØ (12)
MAP REGISTER #2 (C18) - MRØ (12)

UNLESS OTHERWISE SPECIFIED;
1. RESISTANCE IN OHMS, 1/4W 10%



NO.	DESCRIPTION	DATE	DRAWN	APPROVED
.0	RELEASED			

REVISIONS

L00309

Berkeley Computer Corporation

DESIGNED
DEL. CHK'D
DRAWN 12-17-69 G. AZZARINA
DWG. CHK'D
SCALE NONE

TITLE MAP REGISTER #1 & #2

PROJECT CPU 1 T1C17 T1C18

SHEET 1 OF 1

NO. L00309 .1