
* CP/M vers 2.2 Cold Start Loader.

* The following routines will boot CP/M from the Disk Jockey 2D Rev. B, or from the Disk Jockey Hard disk controller.

* Floppy boot:

* The cold boot loader (track 0, sector 1) is loaded into RAM on the controller by the cold boot routine in the firmware. This cold boot loader will start loading the CCP from track 0, sector 5 and will finish up with the last part of the CBIOS on track 1 sector 7.

* During a warm boot sectors 1, 2, and part of 3 will be loaded from track 1. Track 0 loading is unaffected.

track	sector	sysgen	load order	Name	
0	1	900	ff00	1	Boot loader
0	2	980			Unused
0	3	a00			
0	4	a80			
0	5	b00	9500	2	CCP
0	6	b80	9580	13	
0	7	c00	9600	3	
0	8	c80	9680	14	
0	9	d00	9700	4	
0	10	d80	9780	15	
0	11	e00	9800	5	
0	12	e80	9880	16	
0	13	f00	9900	6	
0	14	f80	9980	17	
0	15	1000	9a00	7	
0	16	1080	9a80	18	
0	17	1100	9b00	8	
0	18	1180	9b80	19	
0	19	1200	9c00	9	
0	20	1280	9c80	20	
0	21	1300	9d00	10	BDOS
0	22	1380	9d80	21	
0	23	1400	9e00	11	
0	24	1480	9e80	22	
0	25	1500	9f00	12	
0	26	1580	9f80	23	

* Track 1 is recorded in double density format. There are 1024 bytes per sector.

1	1	1600	a000	4	
1	2	1a00	a400	1	
1	3	1e00	a800	5	CBIOS (@ ab00h)
1	4	2200	ac00	2	
1	5	2600	b000	6	
1	6	2a00	b400	3	
1	7	2e00	b800	7	
1	8	3200	bc00		Unused

* Note that the interleave sequences for loading tracks 0 and 1 are different. This difference was designed so that the boot sequence could be done in 4 disk revolutions since the 2D Mod. B can not load consecutive sectors off of the disk.

* Three spare sectors (track 0, sectors 2 to 4) have been

*BOOT PROVIDED WITH
CBIOS 2.9 8/22/82
(ABOOT & ASM)*

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* provided for a more advanced boot loader at a later date.
*
*
* Hard boot (M10, M20, M26):
* The cold boot loader (track 0, sector 1) is loaded into
* RAM at either 0100h or the 2DB's RAM depending on whether
* this loader is assembled with a 2DB or not. This cold
* boot loader will start loading the CCP from track 0,
* sector 2 and will finish up with the last part of the
* CBIOS on track 0 sector 21.

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track	sector	sysgen	load	order	Name
0	1	900	fc00	1	Cold boot
0	2	b00	9500	3	CCP
0	3	d00	9700	4	
0	4	f00	9900	5	
0	5	1100	9b00	6	
0	6	1300	9d00	7	BDOS
0	7	1500	9f00	8	
0	8	1700	a100	9	
0	9	1900	a300	10	
0	10	1b00	a500	11	
0	11	1d00	a700	12	
0	12	1f00	a900	13	
0	13	2100	ab00	14	CBIOS
0	14	2300	ad00	15	
0	15	2500	af00	16	
0	16	2700	b100	17	
0	17	2900	b300	18	
0	18	2b00	b500	19	
0	19	2d00	b700	20	
0	20	2f00	b900	2	Partial load
0	21	3000			Unused

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* The warm boot load sequence starts at track 0, sector 2
* and goes straight through to sector 12. There is still
* plenty of room left in this loader for more advanced
* things like sector interleaving although this is hardly
* necessary on a hard disk.

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msize equ 48 ;Memory size of target CP/M
bias equ (msize-20)*1024 ;Memory offset from 20k system
ccp equ 2500h+bias ;Console command processor
bios equ ccp+1600h ;CBIOS address
cboot equ bios ;Cold boot address for CP/M
loadr equ ccp ;Load address for floppy
retries equ 10 ;Maximum # of disk retries

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* The following equates set up the relationship between the
* 2D floppies and the Hard Disk Controllers.

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first equ 1 ;0 = Floppies are A-D drives and
; Hard Disk are E-P
;1 = Hard Disks are A-L drives and
; Floppies are M-P
maxhd equ 1 ;Set to number of hard disks
maxflop equ 4 ;Set to number of floppies

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* The following equates are for the Diskus Hard disk if wanted. *
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if (maxhd ne 0) and first ;Want Hard Disk included ?
hdorg equ 50h ;Hard Disk Controller
hdstat equ hdorg ;Hard Disk Status
hdcntl equ hdorg ;Hard Disk Control
hddata equ hdorg+3 ;Hard Disk Data
hdfunc equ hdorg+2 ;Hard Disk Function
hdcmd equ hdorg+1 ;Hard Disk Command
hdreslt equ hdorg+1 ;Hard Disk Result
retry equ 2 ;Retry bit of result
tkz equ 1 ;Track zero bit of status
opdone equ 2 ;Operation done bit of status
complt equ 4 ;Complete bit of status
tmout equ 8 ;Time out bit of status
wfault equ 10h ;Write fault bit of status
drvrdy equ 20h ;Drive ready bit of status
indx equ 40h ;Index bit of status
pstep equ 4 ;Step bit of function
nstep equ 0fbh ;Step bit mask of function
hdrlen equ 4 ;Sector header length
secln equ 512 ;Sector data length
wenabl equ 0fh ;Write enable
wreset equ 0bh ;Write reset of function
scenbl equ 5 ;Controller control
dskclk equ 7 ;Disk clock for control
mdir equ 0f7h ;Direction mask for function
null equ 0fch ;Null command
idbuff equ 0 ;Initialize data command
isbuff equ 8 ;Initialize header command
rsect equ 1 ;Read sector command
wsect equ 5 ;Write sector command
endif

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*****
*
* The following equates are for the Disk Jockey 2D/B if wanted. *
*
*****

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if maxflop ne 0
origin equ 0f800h ;Origin of DJ 2D Mod B PROM
djram equ origin+400h ;Disk Jockey 2D Mod B routines
tkzero equ origin+9h ;Track 0 seek
trkset equ origin+0ch ;Set track
setsec equ origin+0fh ;Set sector
setdma equ origin+12h ;Set DMA address
dread equ origin+15h ;Read sector
dmast equ origin+24h ;Get DMA address
status equ origin+27h ;Disk status
dskerr equ origin+2ah ;Flash error light
setden equ origin+2dh ;Set density
endif

if first ;Define start address if hard disk
if maxflop ne 0
boot equ djram ;If floppy is there then use its RAM
else
boot equ 0100h ;Otherwise start at 0100h
endif
else ;Define start address if floppy
boot equ djram+0300h ;Upper quarter of floppy RAM
endif

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offset equ 900h-boot ;DDT offset

*****
*
* Cold Boot loader for Discus M10, M20, or M26.
*
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        if      first      ;first = 1 is hard disk

        org      boot

boothd  lxi      sp,cstkhd   ;Set up stack at end of this sector
        lxi      b,1*100h+20 ;B = sector count, C = sector #
        call     clodhd     ;Load sector 20 into CCP
        lxi      h,ccp+1e00h ;Destination of move
        lxi      d,ccp      ;Source of move
        mvi      c,0

cmovhd  ldax     d           ;Get a byte of source
        mov      m,a       ;Move it
        inc      h         ;Bump destination
        inc      d         ;Bump source
        dcr      c         ;All done with this page ?
        jnz     cmovhd
        lxi      h,ccp-200h ;Initial DMA address
        shld    cdmahd
        lxi      b,18*100h+2 ;B = sector count, C = sector #
        call     clodhd
        jmp     cboot      ;Go to CP/M

clodhd  push     b           ;Save sector and count
        mov      a,c
        sta     hdsec
        lxi      h,ccp-200h ;Get DMA address (self modifying)
cdmahd  equ      $-2         ;Storage for previous DMA address
        lxi      d,200h     ;Offset to new DMA address
        dad     d           ;Add in offset, HL = new DMA address
        shld    cdmahd     ;Save new DMA address
        call     crhd      ;Attempt a read
        pop     b           ;Recover sector number and count
        ;       B = count, C = number
        dcr      b         ;Update sector count
        rz      ;All done ?
        inc      c
        jmp     clodhd     ;Continue reading

*****
*
* Rdhd does the actual read from the controller, the DMA
* address and sector # have already been set up.
*
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crhd    lxi      b,retries*100h+1 ;Maximum # of attempts
crnd    push     b           ;Save error count
        call     hhread     ;Attempt the read
        pop     b           ;Restore the error count
        rnc     ;Return if no error
        dcr      b         ;Update error count
        jnz     crhd      ;Try again if not to many errors
        jmp     $         ;Dynamic error halt

hread   call     hdprep     ;Prepare the sector header image
        rc      ;Error exit
        mvi      a,rsect   ;Read sector command

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out      hdcmd
call    process      ;Process the read
rc      ;Error exit
xra     a            ;Pointer to data buffer
out     hdcmd
mvi     b,secln/4    ;Number of bytes to read
lhld   cdmahd       ;Get destination of data
in      hddata      ;Two dummy data bytes
in      hddata
rtloop  in      hddata      ;Move four bytes
mov     m,a         ;Byte one
inx    h
in      hddata      ;Byte two
mov     m,a
inx    h
in      hddata      ;Byte three
mov     m,a
inx    h
in      hddata      ;Byte four
mov     m,a
inx    h
dcr     b           ;Update byte count
jnz    rtloop
ret

process in      hdstat      ;Wait for command to finish
mov     b,a
ani     opdone
jz      process
mvi     a,dskclk     ;Turn on Disk Clock
out     hdcntl
in      hdstat
ani     tmout        ;Timed out ?
stc
rnz
in      hreslt
ani     retry        ;Any retries ?
stc
rnz
xra     a            ;No error exit
ret

hdprep  in      hdstat      ;Is Drive ready ?
ani     drvrdy
stc
rnz
mvi     a,isbuff     ;Initialize pointer to header buffer
out     hdcmd
mvi     a,null
out     hdfunc       ;Select drive A
xra     a
out     hddata      ;Form head byte
out     hddata      ;Form track byte
mvi     a,0          ;Form sector byte
hdsec   equ     $-1
out     hddata
mvi     a,80h        ;Form Key
out     hddata
mvi     a,dskclk     ;Turn on Disk clock
out     hdcntl
mvi     a,wenabl     ;Write enable on
out     hdcntl
ret

org     boothd+200h-2

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cstkhd equ $
dw boothd

else ;first = 0 is floppy disk

*****
*
* Cold boot loader for the Disk Jockey 2D Revision B controller *
*
*****

org boot

t0boot mvi a,5-2 ;First sector - 2
newsec equ $-1
inr a ;Update sector #
inr a
cpi 27 ;Size of track in sectors + 1
trksiz equ $-1
jc nowrap ;Skip if not at end of track
jnz tlboot ;Done with this track
exit equ $-2
sui 27-6 ;Back up to sector 6
backup equ $-1
lxi h,loadaddr-80h ;Memory address of sector - 100h
nxtdma equ $-2
shld newdma
nowrap sta newsec ;Save the updated sector #
mov c,a
call setsec ;Set up the sector
lxi h,loadaddr-100h ;Memory address of sector - 100h
newdma equ $-2
lxi d,100h ;Update DMA address
secsiz equ $-2
dad d
nowrp shld newdma ;Save the updated DMA address
mov b,h
mov c,l
call setdma ;Set up the new DMA address
lxi b,retries*100h+0;Maximum # of errors, track #
nxtrty equ $-2
fread push b
call trkset ;Set up the proper track
call dread ;Read the sector
pop b
jnc t0boot ;Continue if no error
dcr b
jnz fread ;Keep trying if error
jmp dskerr ;Too many errors, flash the light

tlboot lxi h,cboot ;We jump to cboot next time
shld exit
mvi c,l ;Select double density
call setden
xra a ;First sector - 2
sta newsec
mvi a,8 ;Size of (logical) track + 1
sta trksiz
dcr a ;Number of sectors to back up
sta backup
lxi h,loadaddr+0700h ;DMA start address for first revolution - 2048
shld newdma
lxi h,loadaddr+0300h ;DMA start address for second revolution - 2048
shld nxtdma
lxi h,2048 ;Difference between DMA addresses
shld secsiz

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```
lxi h,retries*100h+1;Maximum # of errors, track #
shld nxtrty
jmp t0boot ;Go load in track 1
endif
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end
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