

MAGNETIC TAPE PROGRAMMING MANUAL

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MAGNETIC TAPE PROGRAMMING MANUAL

1. INTRODUCTION

This manual describes the functional characteristics and programming aspects of the INTERDATA 45 IPS 1600 character per inch (CPI), 45 IPS 800 CPI, 75 IPS 800 CPI (vacuum column), and 75 IPS Dual Density (vacuum column) Read-After-Write Magnetic Tape Systems.

For information about INTERDATA programming, refer to the following:

Model 7/32 Processor User's Manual, Publication Number 29-405
Model 8/32 Processor User's Manual, Publication Number 29-428
16-Bit Processor User's Manual, Publication Number 29-509
SELCH Programming Manual, Publication Number 29-567
ESELCH Programming Manual, Publication Number 29-529.

The appendices of this manual contain various sample programs.

2. CONFIGURATION

These Magnetic Tape systems can be used with most INTERDATA Series 16, Series 32 or compatible processors.

These Magnetic Tape systems are normally operated in conjunction with a Selector Channel, although the Selector Channel is not a mandatory prerequisite for operation of the 45 IPS 800 CPI NRZI and 45 IPS 1600 CPI Magnetic Tape systems. This manual deals primarily with the programming considerations of the Selector Channel.

The following is the relationship between Product Numbers and Part Numbers for different configurations of Magnetic Tape systems.

Mag Tape Interface	Mag Tape	Transport
M46-475, M46-512	1600 CPI PE (45 IPS)	M46-465 M46-466
M46-470, M46-500	800 CPI NRZI (45 IPS)	M46-460 M46-461
M46-490	800 CPI NRZI (75 IPS Vac. Col.)	M46-492
M46-494	800/1600 NRZI/PE (75 IPS Vac. Col.)	M46-496

Table 1 gives various characteristics of each Magnetic Tape system.

TABLE 1. CHARACTERISTICS OF MAGNETIC TAPE SYSTEMS

	PE 1600 CPI	NRZI 800 CPI	NRZI 800 CPI (VAC. COL.)	PE/NRZI DUAL DENSITY (V.C.)
INTERFACE PRODUCT NO.	M46-475 M46-512	M46-470 M46-500	M46-490	M46-494
TAPE SPEED	45 IPS	45 IPS	75 IPS	75 IPS
RECORDING FORMAT	9 TRACK P.E. ANSI AND IBM COMPATIBLE	9 TRACK NRZI IBM COMPAT- IBLE	9 TRACK P.E. ANSI AND IBM COMPATIBLE	9 TRACK PE/NRZI ANSI AND IBM COMPATIBLE
TAPE CAPACITY	2400 FEET	2400 FEET	2400 FEET	2400 FEET
INTER-RECORD GAP	.6 INCH	.75 INCH	.6 INCH	.6 INCH
TRANSFER RATE	72K BYTES/ SEC	36K BYTES/ SEC	60K BYTES/ SEC	120K BYTES/SEC (READ), 240K BYTES/SEC (WRITE)
ERROR CHECKS	1. VERTICAL PARITY CHECKS (READ-AFTER- WRITE) 2. FALSE PRE- AMBLE AND POSTAMBLE DETECTION	1. VERTICAL PAR- ITY CHECKS (READ AND READ- AFTER WRITE) 2. LRC CHECK (READ AND READ- AFTER-WRITE) 3. CRC CHECKS (READ ONLY)	1. VERTICAL PARITY CHECKS (READ-AFTER WRITE) 2. LCR CHECK (READ AND READ-AFTER WRITE) 3. CRC EHCKS (READ ONLY)	1. VERTICAL PAR- ITY CHECKS (READ AND READ-AFTER WRITE) 2. LRC CHECK (READ AND READ- AFTER-WRITE) 3. CRC CHECKS (READ ONLY) 4. FALSE PRE- AMBLE AND POST- AMBLE DETECTION
MIN. NO. CHAR.	4	4	4	4

3. OPERATING CONTROLS AND INDICATORS

Figure 1 illustrates the operating controls and indicators for the 1600 CPI, 800 CPI, and Dual Density Magnetic Tape drives.

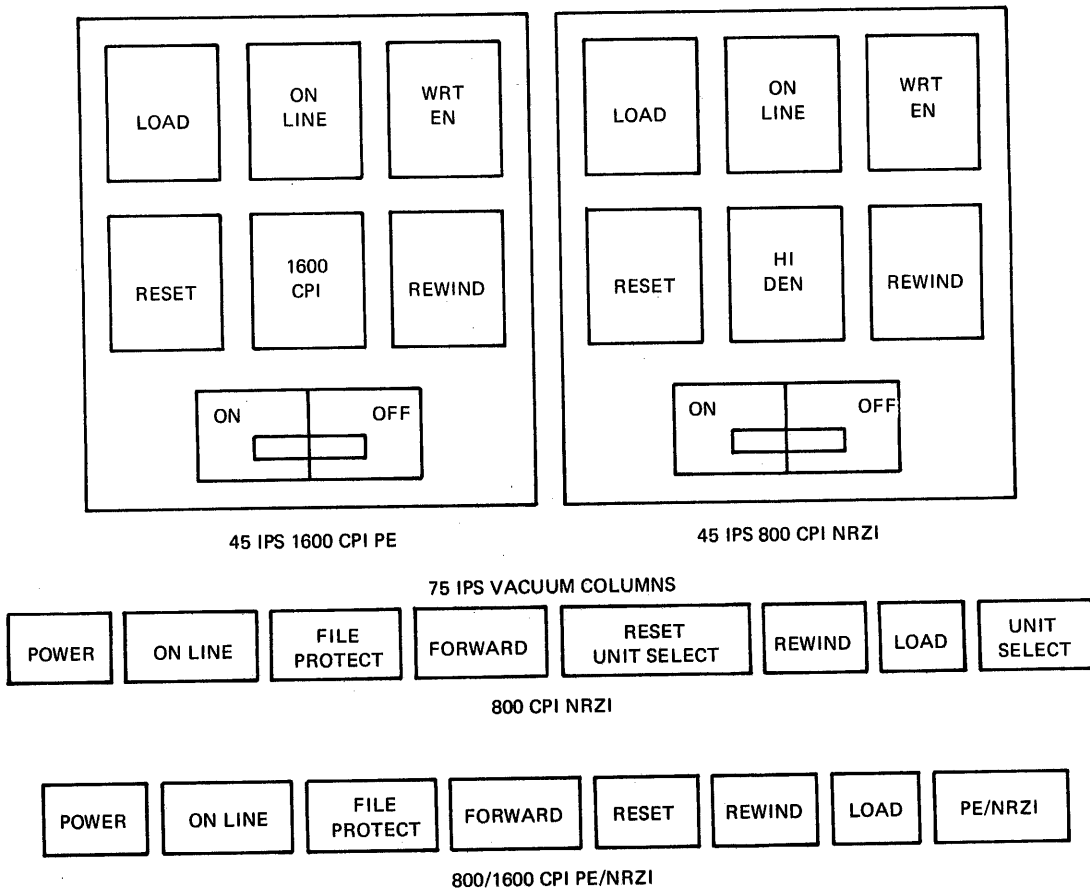


Figure 1. Controls and Indicators of Tape Drivers

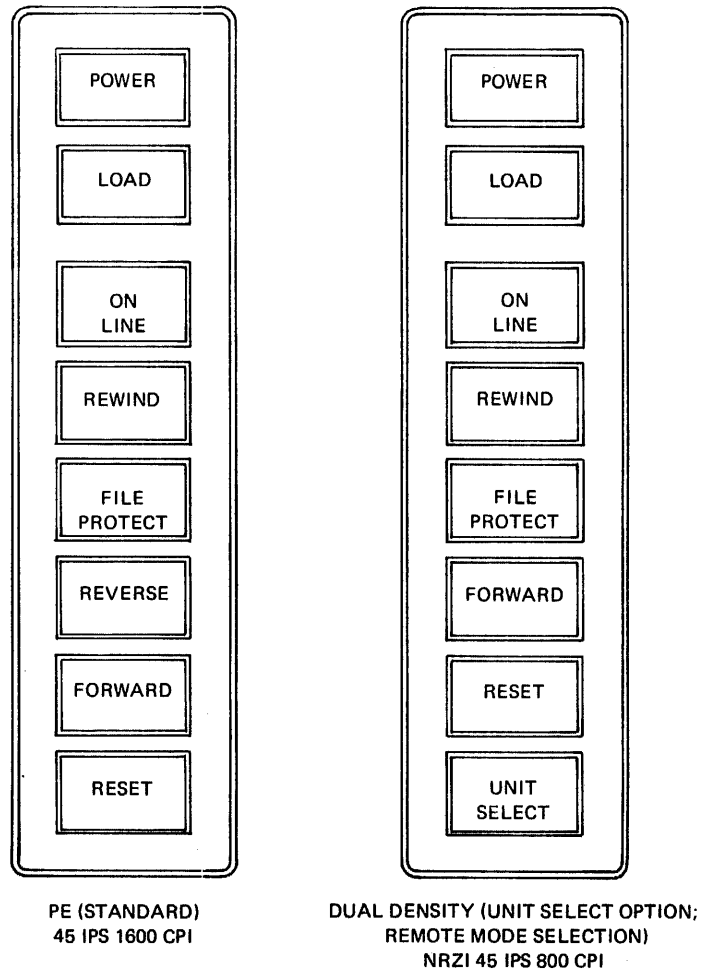


Figure 1. (Continued)

3.1 Switches and Indicators

The switches and indicators for the 45 IPS 800 CPI NRZI and 1600 CPI PE are:

- | | |
|-----------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| ON/OFF | The ON/OFF control is an alternate action switch/indicator which connects line voltage to the unit. |
| LOAD | The LOAD control is a momentary action switch/indicator. When LOAD is momentarily depressed for the first time after power is applied, and a tape is mounted, the tension arms move to the load position. When the LOAD control is depressed for the second time, after power is applied, the tape is positioned at the load point which is indicated by a reflector on the tape. The LOAD switch is disabled once the first LOAD or manual Rewind command has been given following the power-on and can only be re-enabled by loss of tape tension or restoration of power after power has been off. |
| 1600 CPI | The 1600 CPI control is an alternate action switch/indicator. It indicates that power is applied to the transport. As a switch, it is not operational. |
| HI DEN (High Density) | The HIGH DEN control is an alternate action switch/indicator. It indicates that power is applied to the transport. As a switch, it is not operational. |

REWIND	The REWIND control is a momentary action switch/indicator which is enabled only in the Off-Line Mode. Depressing and releasing the control causes the tape to rewind at the specified speed. On reaching the BOT reflector, the rewind drive ceases and the Load sequence is automatically entered. The BOT tab overshoots the photo-tab sensor, moves forward and stops at the Load Point. If the REWIND control is depressed and released while the tape is positioned at BOT, the tape rewinds until it is unloaded from the take-up reel and tension in the tape is lost.
ON-LINE	The ON-LINE control is a momentary switch/indicator which is enabled after a Load or Rewind sequence has been initiated. Depressing and releasing the switch puts the transport On-Line and turns on the indicator lamp. In this condition, the transport can accept external commands, provided it is also Ready and Selected.
WRT EN (Write Enable)	WRT EN is an indicator lamp which is turned on whenever the Power switch is in the ON position and a reel of tape, with a Write Enable ring installed, is mounted on the transport (tape tension established).
RESET	RESET is a momentary action switch/indicator which stops all manual commands when the switch is depressed. Depressing the RESET switch when the transport is ON-LINE removes the unit from On-Line operation.
FORMATTER POWER SWITCH	The formatter power control is an alternate action switch/indicator which applies AC power to the formatter. It is located on the front panel of the formatter (on 1600 CPI Drives only).

3.2 Switches and Indicators for the Vacuum Column Tape Systems

POWER	This is a combination pushbutton switch and indicator. The indicator lamp is on when power has been supplied to the tape system. For the convenience of the maintenance or customer engineer, a power switch is provided on the power supply chassis at the inside rear of the machine. It is accessible only when the tape unit is swung open for service.
LOAD	After threading of the tape, this button is pressed to complete the tape loading operation. Tape is automatically advanced to the load point and then the tape system goes ON-LINE. The LOAD and ON-LINE lamps are on when the action is completed. After this sequence of operations is completed, the switch becomes functionally disabled and can only be re-enabled by loss of vacuum in the chambers. The LOAD lamp goes out when the tape is advanced from load point or rewound. The LOAD lamp is on anytime that the tape is positioned at load point.
FORWARD	This button functions only when the machine is off-line. If the ON-LINE indicator is on, pressing the FORWARD button has no effect. If the ON-LINE indicator is off and the FORWARD button is pressed, the indicator is turned on and the transport moves the tape in the forward direction at the normal tape speed. To stop the machine when it is running in this mode, the RESET button should be pressed.
RESET/UNIT SELECT	All tape motion (except UNLOAD), regardless of the command that established it, stops when RESET is pressed. Pressing RESET clears all read, write, and control functions and also removes the tape unit from on-line operation. The ON-LINE indicator is turned off. The RESET switch also houses the Unit Select indicator on NRZI only units. This indicator is turned on if the unit has been selected. Dual Density units do not have this indicator.
ON-LINE	This is a combination switch and indicator. If this indicator is on the system is under processor control. If the system is off-line and control is to be turned over to the processor, ON-LINE must be pressed. The system is returned to the OFF-LINE mode if the system interlocks are lost, or the RESET pushbutton is depressed.
FILE PROTECT	This indicator is on when a write enable ring is <i>not</i> installed on the file reel. When a file or supply reel is put on the machine with the write enable ring in place in the slot at the back of the reel, the FILE PROTECT light is off. This indicates that data may be written on the tape. Without the ring, protective circuits in the tape system prevent data from being recorded or erased.

REWIND	Pressing this button results in rewind of the tape, with high speed reverse operation. The operator can stop this reverse motion by pressing the RESET switch. If Reset is not pressed, the tape goes beyond the beginning-of-tape marker, stops, and then automatically returns to the load point. If the REWIND button is then again pressed, the tape rewinds until tension is lost and vacuum shuts off. Since tape still remains in the chambers, it is drawn out of the tape path and the unload sequence completes. If the REWIND button is again pressed, the file reel moves slowly in the rewind direction to take up the remaining tape, as long as the button is held in. This switch is disabled if the system is ON-LINE.
UNIT SELECT SWITCH	This switch is not functional.
PE/NRZI	This switch is used only on PE/NRZI Dual Density units. It is a combination alternate action switch and indicator. This switch conditions the system to operate in PE mode when the indicator is on. To operate in NRZI mode, the switch must be in the alternate state corresponding to the indicator being turned off. This switch is not installed if the PE SELECT [†] input line is used. See the <i>Vendor's Operating and Service Manual</i> Publication Number under Section 3.4.

3.3 Preventive Maintenance

It is recommended that the heads and capstan be cleaned after every eight hours of operation. Refer to the vendor's Operating and Service Manual (Publication Number under Section 3.4).

3.4 Magnetic Tape Loading and Unloading

Refer to the *Vendor's Operating and Service Manual*, Publication Number 29-321 (1600 BPI), 29-296 (800 BPI), and 29-557 (vac. col.).

4. DATA FORMAT

4.1 1600 CPI

Data is written on the 1600 CPI Read-After-Write Magnetic Tape system in the standard 1600 CPI phase-encoded format. This consists of a preamble, a variable length binary record with eight bits per character and one vertical parity bit, followed by a postamble. The minimum record size for data transfer is four bytes. See Figures 2, 3, and 4.

4.2 800 CPI

Data is written on the 800 CPI Read-After-Write Magnetic Tape in the standard nine track NRZI format. This consists of variable length binary records with eight bits per character and one bit of vertical parity check (VPC). A Cyclic Redundance Check Character (CRCC) and Longitudinal Record Check Character (LRCC) are inserted at the end of each record. An Inter-Record Gap separates one record from another. Note that the CRCC checking is optional to the interface. The minimum record size for data transfer is four bytes. See Figures 2, 3, and 4.

4.3 System Tape Format

Figure 2 shows the INTERDATA system tape format. Records are separated by Inter-Record Gaps (IRG), and a number of records are separated by a File Mark (EOF). When generating a tape (writing) from the beginning of tape (Load point), a File Mark (EOF) must be written on tape for compatibility purposes.

Figure 3 shows an example of the appearance of data on magnetic tape.

Figure 4 shows the spacing of track locations on magnetic tape.

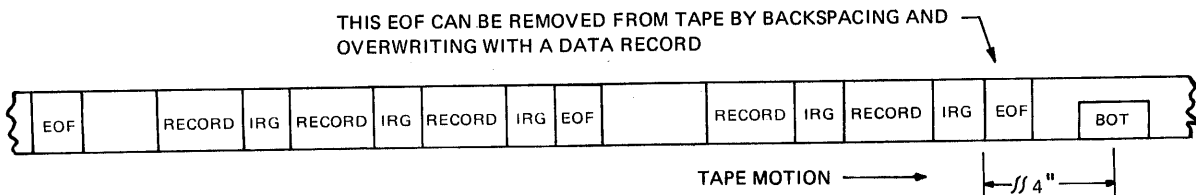


Figure 2. Tape Format

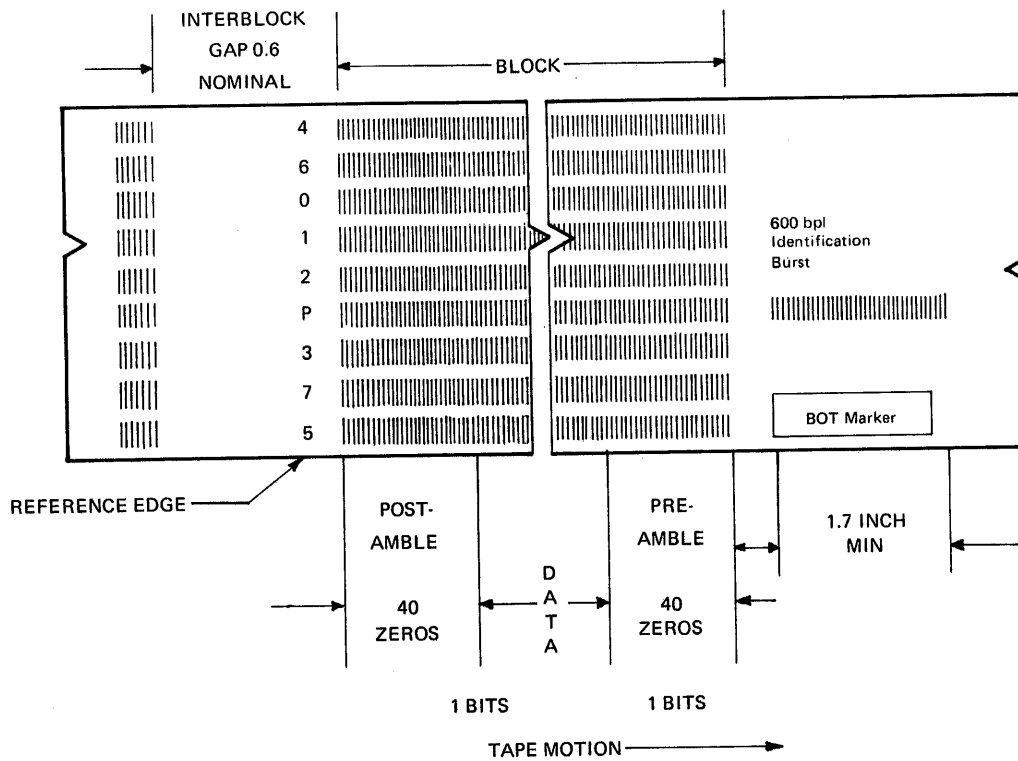


Figure 3. Data Format

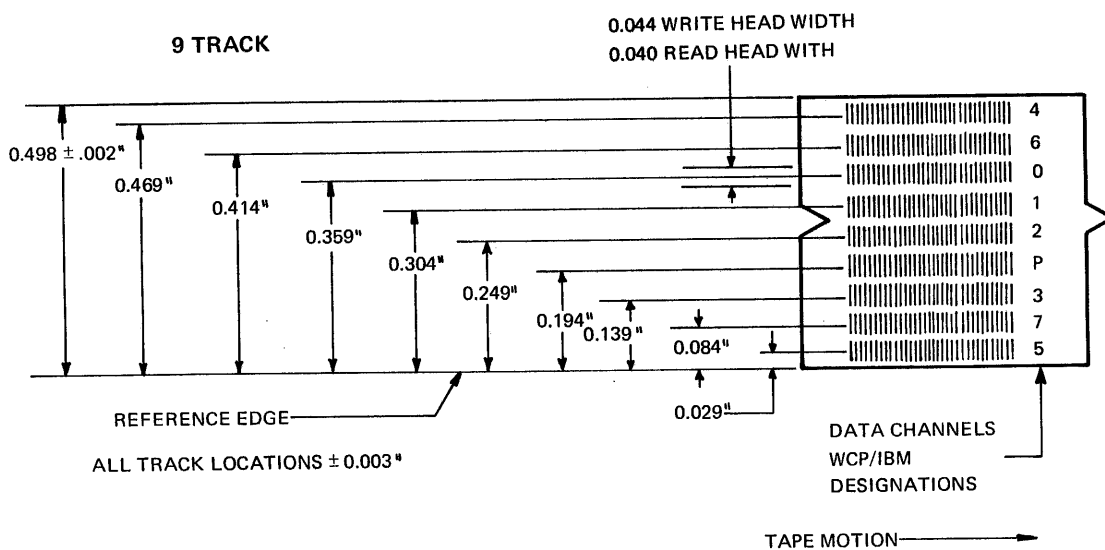


Figure 4. Track Locations and Spacing

5. PROGRAMMING INSTRUCTIONS

There are several methods of programming the Read-After-Write Magnetic Tape System. The choice of the method depends upon the application and the system configuration; that is, through the Selector Channel or directly on the Multiplexor Bus. The information on Status and Command bytes applies regardless of the programming method used. Later paragraphs discuss the Selector Channel programming.

5.1 Instructions

Output Command (OC or OCR)	This instruction is used to send a command byte to the Magnetic Tape Interface.
Sense Status (SS or SSR)	This instruction reads the status byte of the Magnetic Tape Interface.
Read Data (RD or RDR) * Write Data (WD or WDR)	This instruction is used to input/output a data byte between the Magnetic Tape Interface and the processor, if operating on the multiplexor bus.
Acknowledge Interrupt (AI or AIR)	This instruction allows the user to examine the device address and status byte when the Interface generates an interrupt. It is valid on 16-bit Processors only.
Write Block, Read Block ** (WB, RB)	This instruction is used to input/output a block of data between the Magnetic Tape Interface and the Processor if operating on the multiplexor channel.
Autoload **	The Autoload instruction can be used initially to input data into the processor. However, when used in this manner, there should be no leading filemark on the tape, as Autoload terminates. See Section 6.1.

5.2 Status and Command Byte Data

The INTERDATA System Controller returns a status byte to the processor as a result of a Sense Status Instruction. Table 2 shows the bit assignments for the Magnetic Tape System Status Byte and Command Byte.

TABLE 2. STATUS AND COMMAND BYTE CODING

BIT NUMBER	0	1	2	3	4	5	6	7
STATUS	ERR	EOF	ET	NMTN	BSY	EX	EOM	DU
COMMAND	DISABLE	ENABLE	SEE TABLE 3 FOR LEGAL COMMANDS					

NOTES

The ERR bit is deferred until EOM sets (EOM=1).

The status byte reflects the current status of the selected tape transport (last transport addressed).

Each transport has its own individual device address. If an operation on the transport is in progress, any instruction which addresses the other transports causes the operation in progress to be aborted. An exception to this rule is the Rewind operation. See Section 6.9 for details.

*Because of the speed of the Magnetic Tape and the possible loss of data with this instruction, it is advisable to use the Write Block or Read Block instruction when transferring data without the SELCH.

**This instruction is not useable on 75 IPS drives.

Status

ERR. The Error bit (Bit 0) is set on the following conditions:

1. Write Overflow – if a data byte is not supplied in time, the controller assumes end of record and any attempt to write to the device while generating the postamble, and the Inter-Record GAP (IRG) (generated after postamble), sets this bit.
2. Read Overflow – Processor fails to read characters in time.
3. Read Error – (1600 BPI) – vertical parity failure, false preamble and postamble detection.
4. Read Error on Read or Write – (800 BPI) – Longitudinal Record Check (LRC) incorrect on transfer or vertical parity (VPC) on a Write.
5. Write Error – vertical parity failure, false preamble and postamble, and error corrections made on data read (1600 BPI only).
6. VPC, CRCC, or LRC error on a Read (800 BPI only) – The error bit may be set on either a Read or Write operation because of the Read-After-Write feature of the magnetic tape transport.

EOF The end of File bit (Bit 1) is set when a file mark has been detected during Write, Read, Backspace, or Skip operation.

ET The End of Tape (Bit 2) is set on:

1. BOT (Beginning of Tape) – Physical reflector marker on the tape.
2. EOT (End of Tape) – Physical reflector marker at the end of the tape. It remains set until Rewind, Clear, or Initialize.

NMTN The No Motion bit (Bit 3) is set when the tape motion has stopped and the Controller is in the idle state (ready to accept any valid command). All output commands given when NMTN is reset are ignored.

BSY The Busy bit (Bit 4) is set when data is not available in the Read or Write Mode. It is reset in the Idle state or when EOM is set.

EX The Examine bit (Bit 5) is set to indicate that one or more of the high order bits (ERR, EOF, NMTN) have set.

EOM The End of Medium bit (Bit 6) is set upon the termination of any of the following operations: Read, Write, Backspace, WEOF, or Skip (operations).

DU The Device Unavailable bit (Bit 7) is set if the tape drive power switch is in the OFF position, the ON-LINE switch is reset, or the Magnetic Tape is not properly readied for Processor control (and the power switch on the formatter is in the OFF position).

5.3 Command Byte

The Processor controls the operation of the Magnetic Tape System by issuing commands encoded into a Command byte. The command Byte Enables, Disables, and Disarms interrupts, sets the mode to Read or Write, and controls the Rewind, Backspace, Skip, and Write EOF operations. Table 3 shows the bit assignments for the Command Byte.

TABLE 3. COMMAND BYTE

COMMAND	HEX COMMAND EQUIVALENT	BIT 0	BIT 1	BIT 2	BIT 3	BIT 4	BIT 5	BIT 6	BIT 7
** DISABLE INTERRUPTS	X'80'	1	0	0	0	0	0	0	0
** ENABLE INTERRUPTS	X'40'	0	1	0	0	0	0	0	0
** DISARM INTERRUPTS	X'C0'	1	1	0	0	0	0	0	0
READ	X'21'	0	0	1	0	0	0	0	1
WRITE	X'22'	0	0	1	0	0	0	1	0
WRITE FILE MARK (EOF)	X'30'	0	0	1	1	0	0	0	0
REWIND	X'38'	0	0	1	1	1	0	0	0
SKIP FILE FORWARD	X'23'	0	0	1	0	0	0	1	1
SKIP FILE REVERSE	X'13'	0	0	0	1	0	0	1	1
* CLEAR	X'20'	0	0	1	0	0	0	0	0
BACKSPACE	X'11'	0	0	0	1	0	0	0	1

* BITS 0 AND 1 MUST BE ZEROS ON A CLEAR COMMAND.

** THESE COMMANDS CAN BE COMBINED WITH THE FUNCTION COMMANDS TO PROVIDE BOTH SETS OF INFORMATION WITH A SINGLE COMMAND.

NOTE

Any bit configurations that do not conform to the above table are invalid and are ignored to the interface.

5.4 Commands

- DISABLE** When this command bit is set and the ENABLE bit is reset, the Controller generates interrupt signals to the Processor. If an interrupt condition occurs, the interrupt signal is queued and remains queued until Disarmed or Enabled by the Processor.
- ENABLE** When this command bit is set and the DISABLE bit is reset, the controller sends interrupt signals to the Processor as they occur.
- DISARM** When both the DISABLE and ENABLE bits are set in any output command, the controller inhibits the generation and queuing of interrupts, and clears any previously queued interrupts.
- NO CHANGE** If DISABLE and ENABLE are both reset in an output command, the controller maintains its existing Interrupt state, initiated by a previous command, Power Up, or an Initialize sequence.
- READ** The Read command sets the Controller to the Read Mode and starts the tape moving forward.
- WRITE** The Write command sets the Controller to Write Mode and starts the tape moving forward.
- WRITE FILE MARK (EOF)** The Write File Mark command causes the Controller to write a hardware generated file mark (EOF).

BACKSPACE	This command moves the tape backward from the current position to the previous Inter-Record Gap.
SKIP FILE	This command causes the tape to move backwards until a file mark is encountered and then to stop in the next gap.
REWIND	This command moves the tape backward from the current position to beginning of the tape (BOT).
CLEAR	This command initializes the Controller and resets the interface, leaving the interface addressed (same as System Clear except the Address flip-flop is not reset). This command may be issued at any time. However, if the tape is rewinding at the time of the command, tape motion does not stop until the Rewind operation is complete. (This command places the Interrupt circuit of the Controller in the disarm state, 1600 CPI only).

NOTE

Interrupts are generated only by the selected tape transport.

6. PROGRAMMING SEQUENCES

The following section gives a number of conventions and comments concerning the Magnetic Tape System programming. Some, or all, of the comments may apply, depending on how the Magnetic Tape System is to be used.

6.1 File Mark at BOT

To conform to IBM as well as INTERDATA tape formats, it is necessary to write a file mark when the tape is positioned at the Beginning of Tape (BOT). This is accomplished with a Write EOF command. Once the Write EOF operation terminates (NMTN set), the normal write record processing may be executed. Also, the EOF mark may be overwritten if desired by a "Backspace, Write" sequence. (See Appendix for special uses of Filemarks in an operating system.)

6.2 Skip File Forward from BOT

Since a Skip Forward command can be issued while the tape is positioned at the beginning of Tape (BOT status set), a general procedure for Skipping File Marks from BOT is as follows:

1. Command Skip File Forward, which moves the tape past the first EOF mark, if not overwritten.
2. A Read or Write command may be issued to process the remaining records of the file.

6.3 Error Recovery

Like any magnetic tape device, the Read-After-Write Magnetic Tape is subject to errors from dirt on the tape or from oxide worn thin. When writing or reading from a magnetic tape, proper error tests and error recovery routines should be used. In general, during the reading operations, if EOM occurs before four characters have been transferred, the EOM indication may be due to dirt or noise on the tape. In this case, the "short record" should be ignored, and re-read as outlined below. If the ERR status occurs after reading a record of normal length, a Read error may have resulted. In this case, it is proper to Backspace and re-read the record five times before considering the error unrecoverable. In the case of a Write error, it is proper to Backspace and attempt to re-write the record five times before considering it an unrecoverable error.

NOTE

Following an unrecoverable error, either the program should backspace and overwrite the bad record with record gaps and resume, or the write process on that magnetic tape should be aborted.

The procedure for filling a bad record with record gaps is as follows:

1. Write a File Mark.
2. Backspace.
3. Write a File Mark.
4. Backspace.
5. Write.

6.4 Skip File Forward

The Skip File Forward operation is achieved by the use of the Skip command. That is, once an Output command to skip is issued, the tape advances to the next Inter-Record Gap (IRG) beyond the EOF mark.

6.5 Long Records

For normal use, there should be sufficient buffer space in memory to allow reading the longest record on the magnetic tape. In case the program waits for EOM status and ignores the trailing extra data characters, then ERR status results due to data input "overflow." The program cannot then distinguish between "long records" and an actual read error condition. To avoid this ambiguity, it is mandatory that the program read all data characters from the tape, and disregard those characters in the program that do not fit into the buffer space available.

6.6 Short Records

If short records are written, a large amount of space may be wasted on the tape by the many Inter-Record Gaps that occur.

6.7 End of File Marks

The Write End of File Mark (WEOF) command causes the tape unit to generate a special hardware character. When reading in the EOF mark, the special character is recognized by the hardware and the EOF bit is set in the status.

File marks are used by the Magnetic Tape Controller for Skip operations. For this reason, extraneous EOFs on a tape should be avoided. If a tape terminates its last file with a file mark, care should be taken in extending that file. The procedure to extend the last file, terminated by a file mark, is:

1. Skip Forward the last file mark at end of last file.
2. Backspace one record.
3. Write new record as desired. (Overwriting the File Mark)
4. Write a new file mark at the end.

6.8 End of Tape (EOT) Status

The EOT status bit is set whenever the magnetic tape sensors detect the reflective markers at the beginning or end of the tape. Once the End of Tape (EOT) is encountered, the EOT bit remains set until a Clear command is given, system initialization occurs, or a Rewind operation is performed. The Examine (EX) bit does not set when EOT sets, unlike the other status bits (EOF, ERR and NMTN). Thus, it is necessary that the programmer test the upper half of the status byte to ascertain the EOT or BOT condition. It is permissible to write beyond the End of Tape (EOT). (Note that care must be taken to insure the tape length is not exceeded.) The EOT bit remains set until one of the previously mentioned operations in this paragraph is performed.

If the Processor issued a Write or Write File Mark command to a magnetic tape that does not have a Write ring present, (1) the command is ignored, (2) the No Motion (NMTN) status bit remains set, and (3) the BUSY (BSY) status bit remains reset.

6.9 Read Operation at BOT

The Read block and Autoload instructions can be used to read from the BOT (except on 75 IPS tape drives). If the first record on the tape is an EOF mark, then it is necessary that two Read operations must be performed to read the tape. The first Read operation moves the tape past the EOF and the second Read loads the first data record.

6.10 Multi-Transport Operations

Each transport on a controller can be issued a Rewind command and all transports rewound. If Transport 0 is Busy (NMTN=0) and Transport 1 is addressed, the operation (if other than Rewind) is terminated on Transport 0. The following is a recommended sequence for switching tape transport operations:

1. Wait for NMTN=1 on Transport 0.
2. Issue Rewind command to Transport 0.
3. Wait for NMTN=1 on Transport 1.
4. Perform operations on Transport 1.

It is not mandatory that a program adhere to this sequence, but as a general rule, only the Rewind command to all transports can allow motion of all transports at the same time.

As a general rule, no transport accepts a command unless NMTN=1. This means that No Motion (NMTN=1) must be set on one transport before any other can be used. It is not necessary to write until NMTN is set before ending an operation, but it is necessary to wait for NMTN to set before starting an operation on the same transport or to any other transport.

6.11 Data Transfer

Due to the high data transfer rate of the Magnetic Tape System the Selector Channel is the preferred method of implementation, however, Multiplexer Bus programming can be used in certain situations. The 75 IPS 800 CPI and Dual Density 75 IPS tape drives can only transfer data with the Selector Channel due to their extremely high transfer rate. Multiplexer Bus programming can be used for the 45 IPS transports, but must be done with knowledge of data transfer rates and the throughput rate of the controlling software.

6.12 Programming Modes

The Magnetic Tape System may be programmed using Sense Status loops, Immediate Interrupts, or Auto Driver Channel. The choice of mode depends on the application and the drives involved. (See Appendix 2 for programming examples of various modes.)

7. INTERRUPTS

The Tape System produces interrupts when:

1. DU sets or resets
2. BUSY resets (interface is ready to receive or transmit data).
3. EX sets.
4. NMTN sets.
5. EOM sets.

The program must respond to these interrupts (i.e., read or write data) to avoid loss of data (or overflow), which set the ERR bit, within the specified times as shown below:

13 micro-seconds for 800 CPI (75 IPS)

6.5 Micro-seconds for 1600 CPI (75 IPS)

25 micro-seconds for 800 CPI (45 IPS)

If, because of extraordinary circumstances, the program attempts to write to the magnetic tape after the time period that the particular drive allows for response to an interrupt, the data byte is not written. However, the status byte on the next interrupt indicates ERR, EX, and EOM.

NOTE

The End of Medium interrupt occurs as described above, and at the end of all normal operations. Another interrupt occurs after End of Medium (NMTN is set) after the following time periods:

10 milliseconds for 45 IPS
5.3 milliseconds for 75 IPS

Only the addressed transport may generate interrupts.

8. INITIALIZATION

Manual initialization of the Processor puts the Magnetic Tape Controller in the idle state, with interrupts DISARMED, ready to accept any valid command. The configuration of the status byte is: ERR bit reset, ET bit set if at BOT or EOT, NMTN is set, BSY bit is reset, EX bit set, EOM bit reset, and DU bit set or reset, depending on the state of the device. See Figure 5.

	ERR	EOF	ET	NMTN	BSY	EX	EOM	DU
STATUS	0	0	X	1	0	1	0	Y

WHERE: X DEPENDS ON POSITION OF TAPE

Y DEPENDS ON STATE OF THE DEVICE

Figure 5. Status of Magnetic Tape Controller after Initialization.

9. DEVICE NUMBERS

The preferred Device Numbers assigned to the Magnetic Tape System are:

	1600 CPI 45 IPS PE	1600/800 CPI 75 IPS PE/NRZI	800 CPI 45 IPS also 75 IPS NRZI
1st Transport	X'C5'	X'C5'	X'85'
2nd Transport	X'D5'	X'D5'	X'95'
3rd Transport	X'E5'	X'E5'	X'A5'
4th Transport	X'F5'	X'F5'	X'B5'

1600 CPI and 800 CPI Programming Differences

The differences between the INTERDATA 1600 CPI Magnetic Tape System (M46-475) and the INTERDATA 800 CPI NRZI (M46-470) are as follows:

1. When reading a file mark on the 1600 CPI Magnetic Tape System, no data is transferred to memory. On an 800 CPI drive, a file mark transfers two bytes of X'13' to memory.
2. If a Write command is given to the Controller and no data is presented to the Controller by the Processor before the end of the preamble, a one character record is written onto tape and the Error bit is set.

These differences in no way affect the Software compatibility with existing INTERDATA Magnetic Tape Software.

NOTE

Compatibility is not possible between the 800 CPI NRZI and 1600 CPI Phase Encoded Tapes.

10. SAMPLE PROGRAMS

Appendix 2 contains 3 programs and flowcharts for the 16-bit Processors (Model 7/16 or equivalent).

The first program uses Sense Status loops to write a pre-defined buffer of data onto magnetic tape and then reads it back into memory. Read Block/Write Block instructions are used.

The second program uses Sense Status loops and a SELCH to write a predefined buffer of data onto magnetic tape and then reads it back into memory.

The third program uses Immediate Interrupts and a SELCH to write a predefined buffer of data onto magnetic tape and reads it back into memory.

Appendix 3 contains three programs and flowcharts for the 32-Bit Processors (Model 7/32, 8/32, or equivalent). The first program uses Immediate Interrupts and an ESELCH to write a predefined buffer of data onto magnetic tape and reads it back into memory. The second program uses the Auto Driver Channel and an ESELCH to write a buffer of data onto magnetic tape and reads it back into memory. The third program uses Immediate Interrupts and an ESELCH to write a buffer of data onto magnetic tape and then copies it onto another magnetic tape.

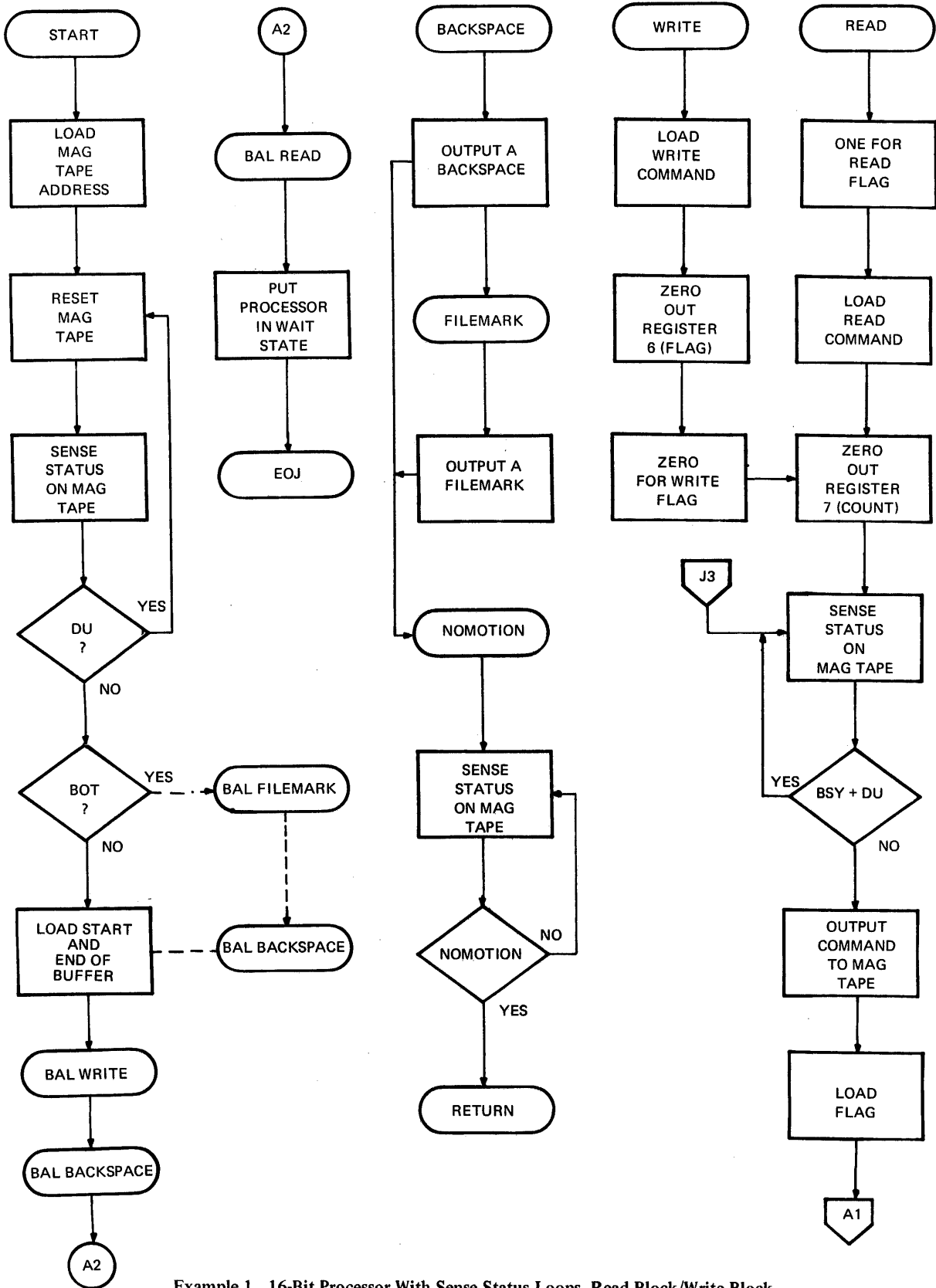
**APPENDIX 1
SPECIAL CONDITIONS FOR FILEMARKS UNDER
AN INTERDATA OPERATING SYSTEM**

Certain utility programs use "filemarks" for one purpose or another. The use of filemarks, however, can make a program unnecessarily device dependent. If filemarks are to be used in a device-independent program, the following rules should be observed:

1. Never use more than one filemark on the same medium. This immediately restricts the medium to be magnetic tape, cassette, and some types of disc files. That is:
 - no double filemark to indicate EOM
 - no "subfiles" separated by filemarks
2. Chained and Indexed files do not support filemarks. To append to a chained or indexed file, the file should be positioned at the end of file by issuing a Forward File (FF) Command.
3. It is always permissible to write a filemark when it is used to indicate EOF, or more precisely, LEOM (Logical End of Medium). This implies that no data should be written following the filemark.
4. Programs taking special action upon detection of error status should treat EOF and EOM status identically, when sensed on a Read operation.

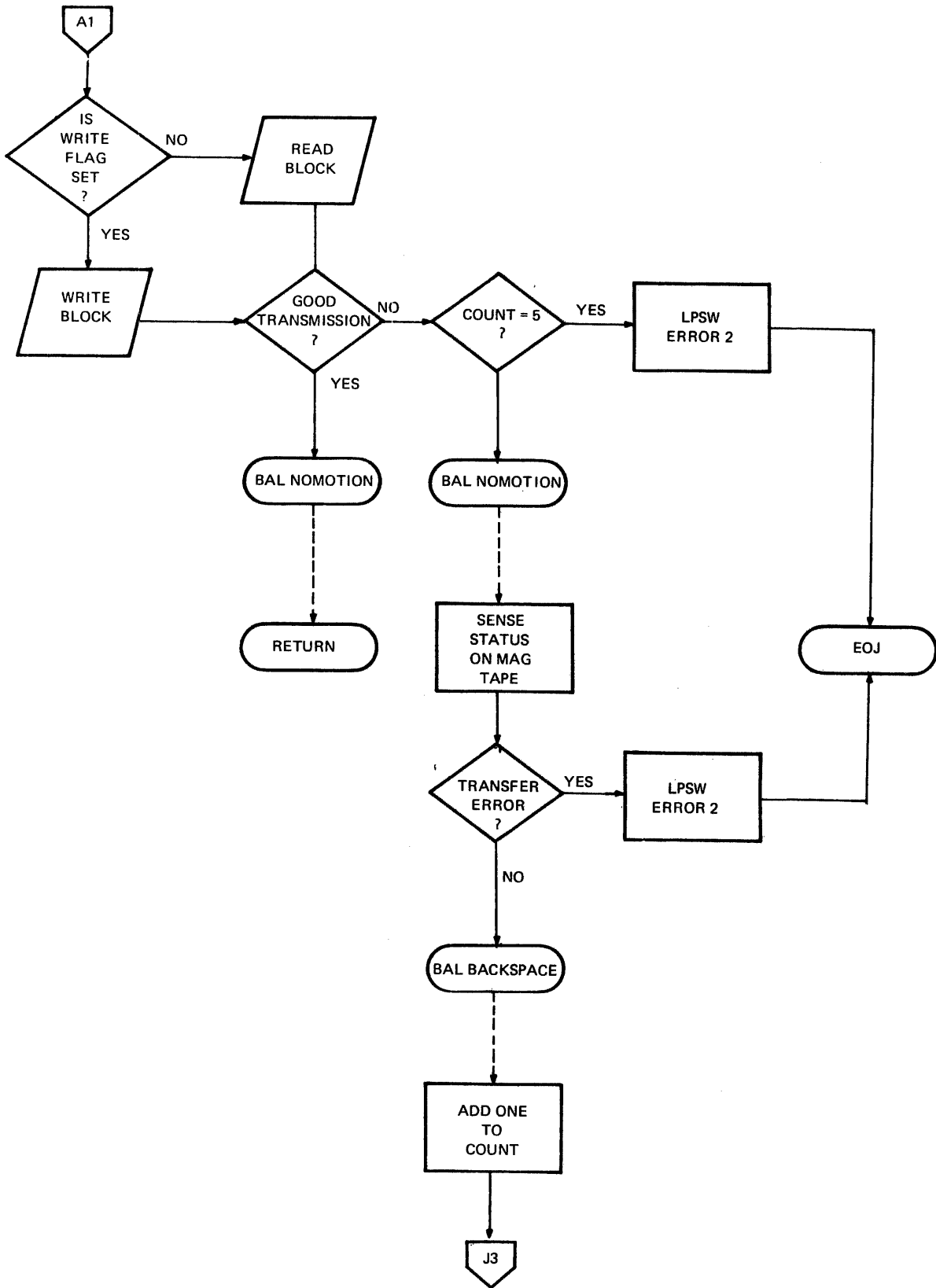
Any program strictly follows the above rules will be device-independent under all presently-existing operating systems.

APPENDIX 2
PROGRAMS AND FLOW CHARTS FOR 16-BIT PROCESSORS



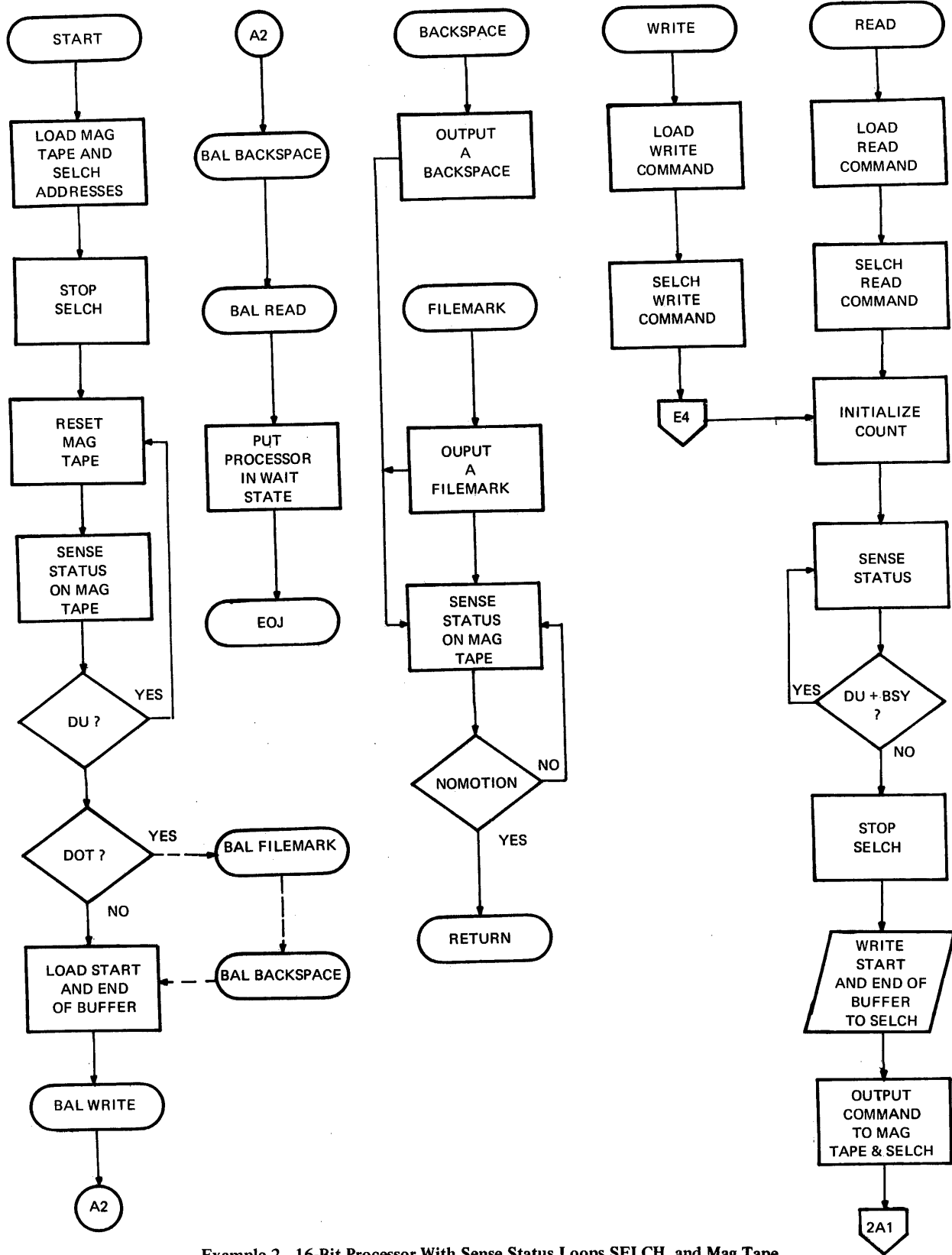
Example 1. 16-Bit Processor With Sense Status Loops, Read Block/Write Block Instructions, and Mag Tape

APPENDIX 2 (Continued)



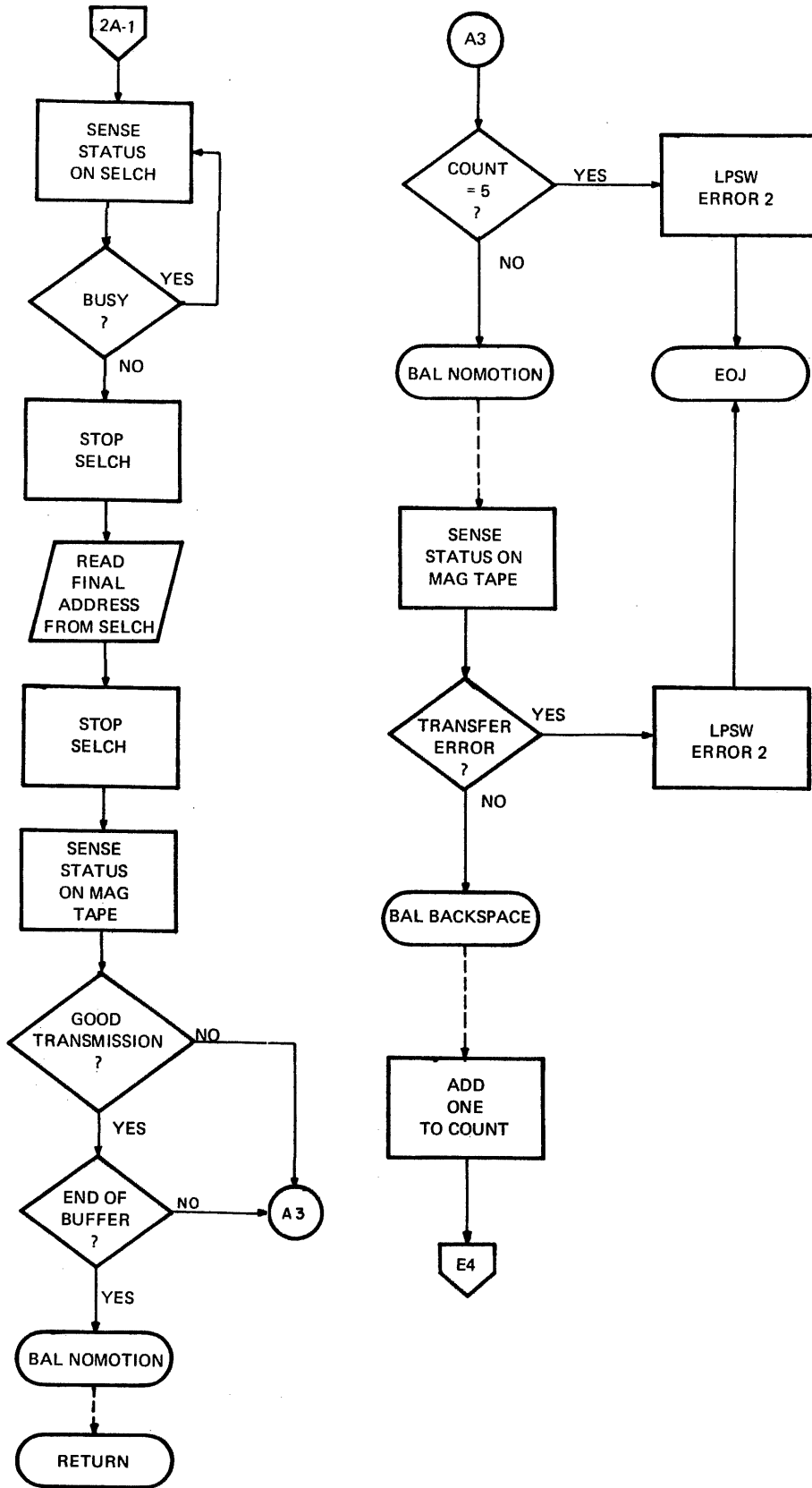
Example 1. (Continued)

APPENDIX 2 (Continued)



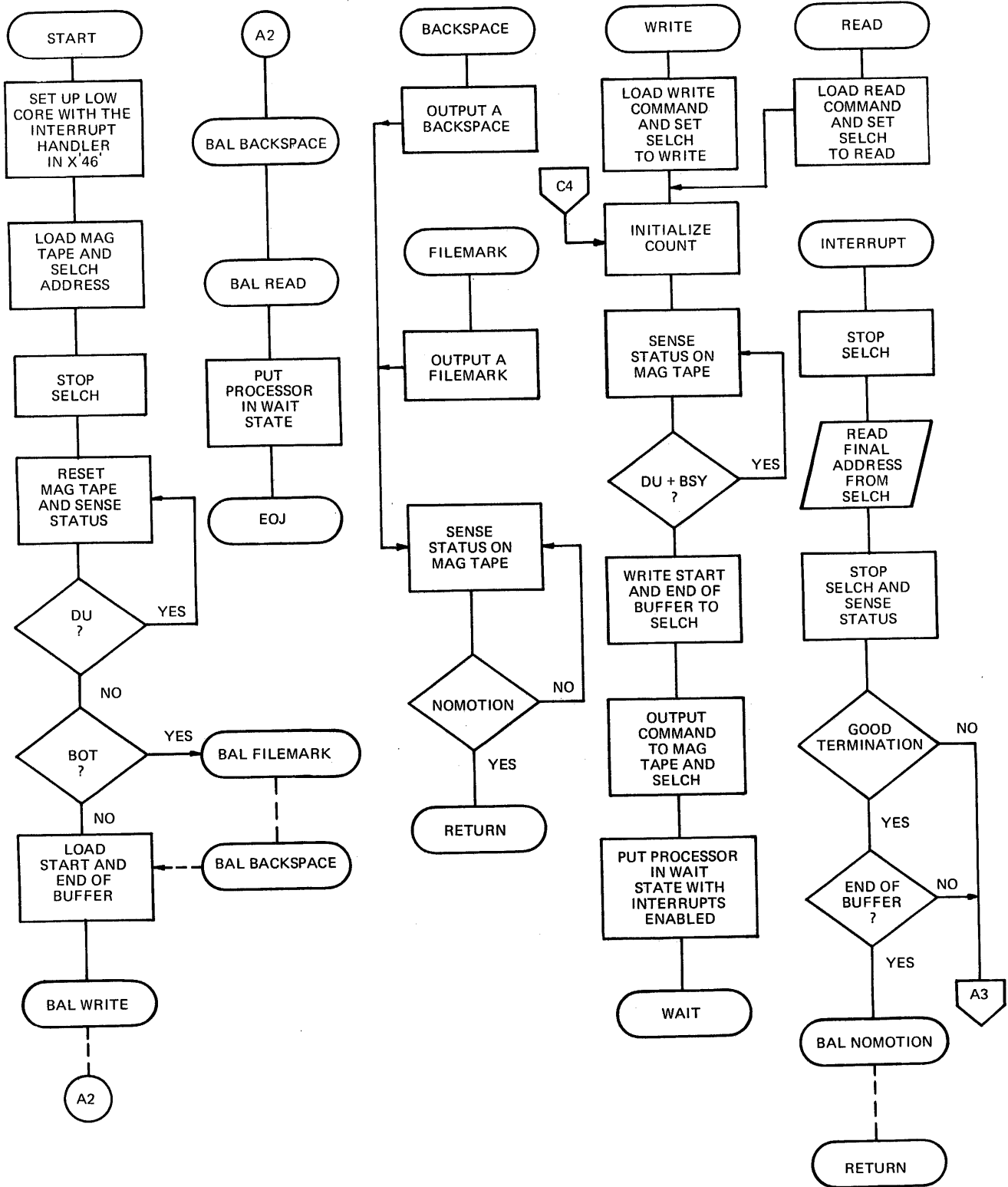
Example 2. 16-Bit Processor With Sense Status Loops SELCH, and Mag Tape

APPENDIX 2 (Continued)



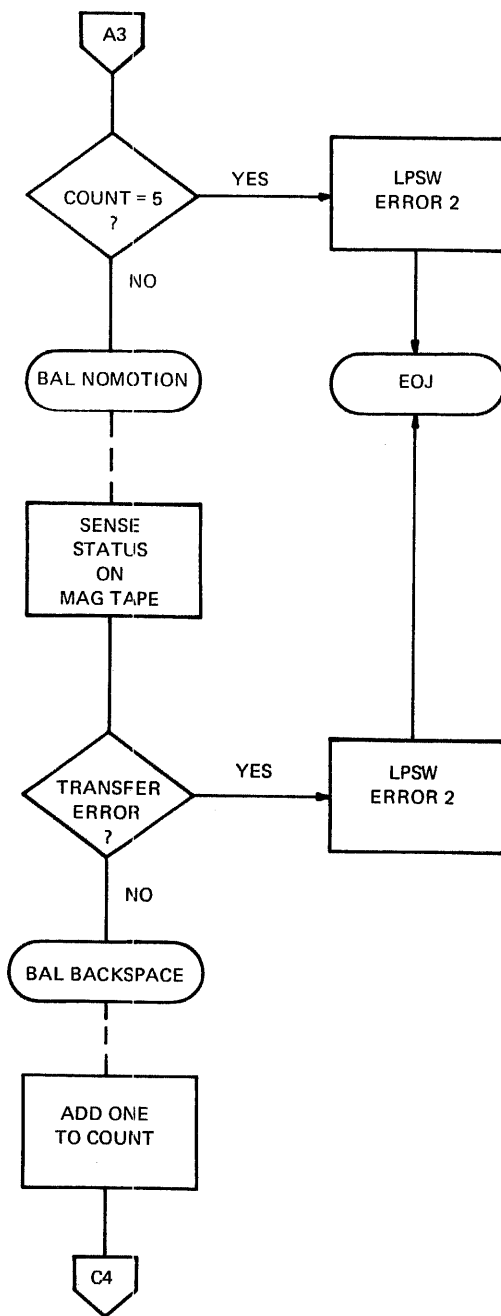
Example 2. (Continued)

APPENDIX 2 (Continued)



Example 3. 16-Bit Processor With SELCH, Immediate Interrupts, And Mag Tape

APPENDIX 2 (Continued)



Example 3. (Continued)

APPENDIX 2 (Continued)

PRJ6= MAGTP ASSEMBLED BY CAL 03-066R05-01 (32-BIT)

```

0000 0001          1  MAGTP  PROG 16-BIT  RB/WB,NO SELCH,SENSE STATUS
0000 0001          2  SC RAT
0000 0002          3  WIDTH 120
0000 0003          4  TARGT 16
0000 0004          5  CROSS
0000 0005          6  SQUEZ 2
0000 0006          7  *
0000 0007          8  * THIS PROGRAM USES THE WRITE BLOCK READ BLOCK
0000 0008          9  * INSTRUCTIONS, SENSE STATUS LOOPS, AND NO SELCH,
0000 0009         10  * TO WRITE A BUFFER ONTO MAG TAPE AND THEN
0000 0010         11  * READ BACK FROM THAT SAME MAG TAPE INTO MEMORY
0000 0011         12  *
0000 0012         13  * 16-BIT PROCESSORS
0000 0013         14  *
0000 0014         15  * REGISTER ASSIGNMENTS:
0000 0015         16  R1 EQU 1
0000 0016         17  DU EQU 1
0000 0017         18  R2 EQU 2
0000 0018         19  R3 EQU 3
0000 0019         20  R4 EQU 4
0000 0020         21  R5 EQU 5
0000 0021         22  R6 EQU 6
0000 0022         23  R7 EQU 7
0000 0023         24  R8 EQU 8
0000 0024         25  BSY EQU 8
0000 0025         26  R12 EQU 12
0000 0026         27  TRANS EQU 15
0000 0027         28  TEST EQU X'10',
0000 0028         29  BOT EQU X'20',
0000 0029         30  TERM EQU X'C0',
0000 0030         31  LH R3,MTADR
0000 0031         32  DUTEST OC
0000 0032         33  SSR R3,R4
0000 0033         34  BTC DU,DUTEST
0000 0034         35  TH R4,BOT
0000 0035         36  BZS WRT
0000 0036         37  BAL R5,FLMK
0000 0037         38  BAL R5,BAKSP
0000 0038         39  *
0000 0039         40  WRT LHI R1,BUFSRT
0000 0040         41  LHI R2,BUFEND
0000 0041         42  BAL R12,WRITE
0000 0042         43  *
0000 0043         44  BAL R5,BAKSP
0000 0044         45  BAL R12,RFAD
0000 0045         46  *
0000 0046         47  ENDI LPSW END
0000 0047         48  *
0000 0048         49  *****
0000 0049         50  * SUBROUTINE: BAKSP
0000 0050         51  * THIS SUBROUTINE BACKSPACES ONE RECORD,
0000 0051         52  * SEE CALLING SEQUENCE BELOW.
0000 0052         53  *
0000R 0030 00E4R
0004R 0030 00E8R
0008R 0034
000AR 2013
000CR C340 0020
0010R 2335
0012R 4150 0036R
0016R 4150 0032R
001AR C610 009CR
001ER C620 00DBR
0022R 41C0 0046R
0026R 4150 0032R
002AR 41C0 0052R
002ER C200 00E0R

```

```

MAG00010
MAG00020
MAG00030
MAG00040
MAG00050
MAG00060
MAG00070
MAG00080
MAG00090
MAG00100
MAG00110
MAG00120
MAG00130
MAG00140
MAG00150
MAG00160
MAG00170
MAG00180
MAG00190
MAG00200
MAG00210
MAG00220
MAG00230
MAG00240
MAG00250
MAG00260
MAG00270
MAG00280
MAG00290
MAG00300
MAG00310
MAG00320
MAG00330
MAG00340
MAG00350
MAG00360
MAG00370
MAG00380
MAG00390
MAG00400
MAG00410
MAG00420
MAG00430
MAG00440
MAG00450
MAG00460
MAG00470
MAG00480
MAG00490
MAG00500
MAG00510
MAG00520
MAG00530

```

```

START OF BUFFER REGISTER
DEVICE UNAVAILABLE
END OF BUFFER REGISTER
DEVICE ADDRESS REGISTER
STATUS REGISTER
RETURN REGISTER
WORK REGISTER
COUNTER
COMMAND STORAGE REGISTER
BUSY
RETURN REGISTER
TRANSFER STATUS=15
TEST=NOMOTION BIT
TEST FOR BOT
TERMINATION STATUS=X'C0'
LOAD MAG TAPE ADDRESS
OUTPUT RESET
SENSE STATUS ON MAG TAPE
IS MAG TAPE UNAVAILABLE
TEST FOR BOT
BRANCH TO WRITE
FILEMARK ROUTINE
BACKSPACE ROUTINE
LOAD START OF BUFFER
LOAD END OF BUFFER
GO WRITE
BACKSPACE TAPE ROUTINE
DO READ ROUTINE

```

APPENDIX 2 (Continued)

```

0032R DE30 00EAR
0036R 2303
0038R DE30 00E9R
003CR 9D34
003ER C340 0010
0042R 2233
0044R 0305

54 * SUBROUTINE: FLMK
55 * THIS SUBROUTINE WRITES A FILEMARK
56 * SEE CALLING SEQUENCE BELOW.
57 *
58 *
59 *
60 * SUBROUTINE: NOMOT
61 * THIS SUBROUTINE WAITS FOR THE MAG TAPE TO
62 * STOP MOVING.
63 *
64 * CALLING SEQUENCE: BAL R5,BAKSP
65 * BAL R5,FLMK
66 * BAL R5,NOMOT
67 *
68 * R3=DEVICE ADDRESS REGISTER
69 * R5=RETURN REGISTER
70 *
71 * WORK REGISTERS:
72 *
73 * R4=DEVICE STATUS REGISTER
74 *
75 *
76 * BAKSP OC R3,BKSP OUTPUT BACKSPACE
77 * BS NOMOT BRANCH TO NOMOTION
78 * FLMK OC R3,FLMK OUTPUT A FILE MARK
79 * NOMOT SSR R3,R4 SENSE STATUS ON MAG TAPE
80 * THI R4,TEST TEST FOR NO MOTION
81 * BZS NOMOT SENSE AGAIN
82 * BR R5 RETURN
83 *
84 *
85 * SUBROUTINE: WRITE
86 * THIS SUBROUTINE WRITES ONE BLOCK OF DATA TO THE
87 * MAG TAPE.
88 * SEE CALLING SEQUENCE BELOW
89 *
90 * SUBROUTINE READ
91 * THIS SUBROUTINE READS A BLOCK OF DATA FROM THE
92 * MAG TAPE.
93 *
94 *
95 * CALLING SEQUENCE: BAL R12,READ
96 * R1=START OF BUFFER BAL R12,WRITE
97 * R2=END OF BUFFER
98 * R3=DEVICE ADDRESS REGISTER
99 * R12=RETURN REGISTER
100 *
101 * WORK REGISTERS:
102 *
103 * R4=STATUS REGISTER
104 * R5=NOMOTION RETURN REGISTER
105 * R6=FLAG REGISTER
106 * R7=RETRY COUNT
107 * R8=COMMAND REGISTER
108 *
MAG00540
MAG00550
MAG00560
MAG00570
MAG00580
MAG00590
MAG00600
MAG00610
MAG00620
MAG00630
MAG00640
MAG00650
MAG00660
MAG00670
MAG00680
MAG00690
MAG00700
MAG00710
MAG00720
MAG00730
MAG00740
MAG00750
MAG00760
MAG00770
MAG00780
MAG00790
MAG00800
MAG00810
MAG00820
MAG00830
MAG00840
MAG00850
MAG00860
MAG00870
MAG00880
MAG00890
MAG00900
MAG00910
MAG00920
MAG00930
MAG00940
MAG00950
MAG00960
MAG00970
MAG00980
MAG00990
MAG01000
MAG01010
MAG01020
MAG01030
MAG01040
MAG01050
MAG01060
MAG01070
MAG01080

```

APPENDIX 2 (Continued)

16-BIT RB/WB,NO SELCH,SENSE STATUS PAGE 3 22:59:00 09/11/79

```

109 *****
110 WRITE LB R8,WRTCMD
111 XHR R6,R6 LOAD WRITE COMMAND
112 STH R6,FLAG ZERO OUT R6
113 BS RETRY STORE ZERO IN FLAG
114 * LIS R6,1 BRANCH TO RETRY
115 * READ R6,1 LOAD ONE IN R6
116 LB R6,FLAG SET READ FLAG
117 XHR R8,RDCMD LOAD READ COMMAND
118 RETRY XHR R7,R7 ZERO OUT R7
119 SENSE1 SSR R3,R4 SENSE STATUS ON MAG TAPE
120 * BTBS DU+BSY,SENSEL TEST FOR BUSY + DEVICE UNAVAILABLE
121 * OCK R3,R8 OUTPJT APPROPRIATE I/O COMMAND
122 * LH R6,FLAG LOAD FLAG
123 * RZ WRTBLK BRANCH ON WRITE FLAG
124 * RBR R3,R1 READ BLOCK
125 * BTC TRANS,ERROR TRANSFER AGAIN IF NO GOOD
126 * SS TEST1 BRANCH TO TEST1
127 * WRTBLK WBR R3,R1 WRITE BLOCK
128 * BTC TRANS,ERROR TRANSFER AGAIN IF NO GOOD
129 * TEST1 SAL R5,NOMOT WAIT FOR TAPE TO STOP
130 * BR R12 RETURN
131 * * THIS ROUTINE ABORTS THE PROGRAM ON NON-RECOVERABLE STATUS ERRORS
132 * * AND TRIES 5 TIMES TO RECOVER ON DATA TRANSFER STATUS ERRORS
133 * * BEFORE TERMINATING.
134 * *
135 * *
136 * * ERROR CLHI R7,5 TEST FOR 5 TRIES
137 * * ERROR1 SE ERROR1 BRANCH TO ERROR1
138 * * BAL R5,NOMOT WAIT FOR TAPE TO STOP
139 * * SSR R3,R4 SENSE STATUS ON MAG TAPE
140 * * THI R4,TERM TEST FOR GOOD TERMINATION
141 * * BZ ERROR1 TRY AGAIN
142 * * BAL R5,BAKSP BACKSPACE ROUTINE
143 * * AIS R7,1 ADD ONE TO COUNT
144 * * B SENSE1 BRANCH TO SENSE1
145 * * ERROR1 LPSW ERROR2
146 * * ALIGN 4 ALIGN START OF BUFFER
147 * * ON HALFWORD BOUNDARY
148 * *
149 * * BUFFER
150 * *
151 * * BUFVRT DCX 0123,4567
152 * * DCX 89AB,CDEF
153 * * DCX 0123,4567
154 * * DCX 89AB,CDEF
155 * * DCX 0123,4567
156 * * DCX 89AB,CDEF
157 * * DCX 0123,4567

0046R 0380 00ECR
004AR 0766
004CR 4060 00E6R
0050R 2306
0052R 2461
0054R 4060 00E6R
0058R 0380 00EDR
005CR 0777
005ER 9D34
0060R 2091
0062R 9E38
0064R 4860 00E6R
0068R 2334
006AR 9731
006CR 21F7
006ER 2303
0070R 9631
0072R 21F4
0074R 4150 003CR
0078R 030C

007AR C570 0005
007ER 4330 0098R
0082R 4150 003CR
0086R 9D34 00C0
0088R C340 00C0
008CR 2336
008ER 4150 0032R
0092R 2671
0094R 4300 005ER
0098R C200 00DCR
009CR

009CR 0123
009ER 4567
00A0R 89AB
00A2R CDEF
00A4R 0123
00A6R 4567
00A8R 99AB
00AAR CDEF
00ACR 0123
00AER 4567
00BOR 89AB
00B2R CDEF
00B4R 0123

```

APPENDIX 2 (Continued)

16-BIT	RB/MB,NO	SELCH,SENSE	STATUS	PAGE	4	22:59:00	09/11/79
0086R	4567						
0088R	89AB	158	DCX			89AB,CDEF	MAG01580
008AR	CDEF						
008CR	0123	159	DCX			0123,4567	MAG01590
008ER	4567						
00COR	89AB	160	DCX			89AB,CDEF	MAG01600
00C2R	CDEF						
00C4R	0123	161	DCX			0123,4567	MAG01610
00C6R	4567						
00C8R	89AB	162	DCX			89AB,CDEF	MAG01620
00CAR	CDEF						
00CCR	0123	163	DCX			0123,4567	MAG01630
00CER	4567						
00D0R	89AB	164	DCX			89AB,CDEF	MAG01640
00D2R	CDEF						
00D4R	0123	165	DCX			0123,4567	MAG01650
00D6R	4567						
00D8R	89AB	166	DCX			89AB,CDEF	MAG01660
00DAR	CDEF						
0000	00D8R						
00DCR	8000	167	BUFEND			EQU	MAG01670
00DER	0098R	168	ERROR2			DC	MAG01680
00E0R	8000	169	END			DC	MAG01690
00E2R	002ER	170				DC	MAG01700
00E4R	0085	171	MTADR			A(END1)	MAG01710
00E6R	0000	172	FLAG			X'85'	MAG01720
00E8R	E0	173	RESET			DC	MAG01730
00E9R	30	174	FIL*RK			0	MAG01740
00EAR	11	175	8KSP			X'E0'	MAG01750
00EBR	38	176	REWIND			X'30'	MAG01760
00ECR	22	177	WRTCHD			X'11'	MAG01770
00EDR	21	178	ROC*MD			X'38'	MAG01780
00EER		179	END			X'21'	MAG01790
						WAIT STATE	
						MAG TAPE ADDRESS	
						READ/WRITE FLAG	
						RESET COMMAND	
						FILEMARK COMMAND	
						BACKSPACE COMMAND	
						REWIND COMMAND	
						WRITE COMMAND FOR MAG TAPE	
						READ COMMAND FOR MAG TAPE	

APPENDIX 2 (Continued)

16-BIT SELCH WITH MAG TAPE USING SENSE STATUS LOOPS PAGE 1 22:57:19 09/11/79
 PRG6= MAGTP ASSEMBLED BY CAL 03-066R05-01 (32-BIT)

```

1 MAGTP   PROG 16-BIT SELCH WITH MAG TAPE USING SENSE STATUS LOOPS
2 SCRAT
3 WIDTH 120
4 CROSS
5 TARGET 16
6 *
7 * THIS PROGRAM USES THE SELCH AND SENSE STATUS
8 * LOOPS TO WRITE A BUFFER TO THE MAG
9 * TAPE AND THEN READ IT BACK INTO MEMORY.
10 *
11 * 16-BIT PROCESSORS
12 *
13 * REGISTER ASSIGNMENTS:
14 *
15 R1 EQU 1
16 DU EQU 1
17 R2 EQU 2
18 R3 EQU 3
19 R4 EQU 4
20 R5 EQU 5
21 R7 EQU 7
22 BSY EQU 8
23 R8 EQU 8
24 R9 EQU 9
25 R10 EQU 10
26 R11 EQU 11
27 R12 EQU 12
28 TEST EQU X'10'
29 BOT EQU X'20'
30 TERM EQU X'C0'
31 LH R3,MTADR
32 LH R9,SELCH
33 OC R9,STOP
34 DUTEST OC R3,RESET
35 SSR R3,R4
36 BTC DU,DUTEST
37 THI R4,BOT
38 BZS WRT
39 BAL R5,FLMK
40 BAL R5,BAKSP
41 LHI R1,BUFSTR
42 LHI R2,BUFEND
43 BAL R12,WRITE
44 *
45 BAL R5,BAKSP
46 BAL R12,RFAD
47 END1 LPSM END
48 *****
49 * SUBROUTINE: BAKSP
50 * THIS SUBROUTINE BACKSPACES ONE RECORD.
51 * SEE CALLING SEQUENCE BELOW.
52 *
53 * SUBROUTINE: FLMK

```

```

MAG00020
MAG00030
MAG00040
MAG00050
MAG00070
MAG00080
MAG00090
MAG00100
MAG00110
MAG00120
MAG00130
MAG00140
MAG00150
MAG00160
MAG00170
MAG00180
MAG00190
MAG00200
MAG00210
MAG00220
MAG00230
MAG00240
MAG00250
MAG00260
MAG00270
MAG00280
MAG00290
MAG00300
MAG00310
MAG00320
MAG00330
MAG00340
MAG00350
MAG00360
MAG00370
MAG00380
MAG00390
MAG00400
MAG00410
MAG00420
MAG00430
MAG00440
MAG00450
MAG00460
MAG00470
MAG00480
MAG00490
MAG00500
MAG00510
MAG00520
MAG00530
MAG00540

```

```

START OF BUFFER REGISTER
DEVICE UNAVAILABLE
END OF BUFFER REGISTER
DEVICE ADDRESS REGISTER
STATUS
RETURN REGISTER
COUNTER
9USY
COMMAND BYTE STORE
SELCH ADDRESS REGISTER
SELCH COMMAND HOLD REGISTER
WORK REGISTER
RETURN REGISTER
TEST=NONMOTION BIT
TEST FOR BOT
TERMINATION STATUS=X'C0'
LOAD MAG TAPE ADDRESS
LOAD SELCH ADDRESS
STOP SELCH
OUTPUT RESET
SENSE STATUS ON MAG TAPE
IS MAG TAPE UNAVAILABLE
TEST FOR BOT
BRANCH TO WRITE
FILEMARK ROUTINE
BACKSPACE TAPE ROUTINE
LOAD START OF BUFFER
LOAD END OF BUFFER
DO WRITE
BACKSPACE TAPE ROUTINE
DO READ
*****
SUBROUTINE: BAKSP
THIS SUBROUTINE BACKSPACES ONE RECORD.
SEE CALLING SEQUENCE BELOW.
SUBROUTINE: FLMK

```

APPENDIX 2 (Continued)

16-BIT SELCH WITH MAG TAPE USING SENSE STATUS LOOPS PAGE 2 22:57:19 09/11/79

```

54 * THIS SUBROUTINE WRITES A FILEMARK
55 * SEE CALLING SEQUENCE BELOW.
56 *
57 *
58 *
59 * SUBROUTINE: NOMOT
60 * THIS SUBROUTINE WAITS FOR THE MAG TAPE TO
61 * STOP MOVING.
62 *
63 * CALLING SEQUENCE: BAL R5,BAKSP
64 * BAL R5,FLMK
65 * BAL R5,NOMOT
66 *
67 * R3=DEVICE ADDRESS REGISTER
68 * R5=RETURN REGISTER
69 *
70 * WORK REGISTERS:
71 *
72 * R4=DEVICE STATUS REGISTER
73 *
74 *
75 * OC R3,BAKSP OUTPUT BACKSPACE
76 * BS NOMOT BRANCH TO NOMOTION
77 * OC R3,FLMKR OUTPUT FILEMARK
78 * SSR R3,R4 SENSE STATUS ON MAG TAPE
79 * THI R4,TEST TEST FOR NO MOTION
80 * BZS NOMOT SENSE AGAIN
81 * BR R5 RETURN
82 *
83 * SUBROUTINE: WRITE
84 * THIS SUBROUTINE WRITES ONE BLOCK OF DATA TO THE
85 * MAG TAPE.
86 * SEE CALLING SEQUENCE BELOW
87 * SUBROUTINE READ
88 * THIS SUBROUTINE READS A BLOCK OF DATA FROM THE
89 * MAG TAPE.
90 *
91 * CALLING SEQUENCE: BAL R12,WRITE
92 * BAL R12,READ
93 *
94 * R1=START OF BUFFER
95 * R2=END OF BUFFER
96 * R3=DEVICE ADDRESS REGISTER
97 * R9=SELCH ADDRESS REGISTER
98 * R12=RETURN REGISTER
99 *
100 * WORK REGISTERS:
101 *
102 * R4=STATUS REGISTER
103 * R5=NOMOTION RETURN REGISTER
104 * R7=RETRY COUNT
105 * R8=COMMAND REGISTER
106 * R10=SELCH COMMAND REGISTER
107 * R11=WORK REGISTER
108 *
003CR DE30 0105R
0040R 2303
0042R DE30 0104R
0046R 9034
0048R C340 0010
004CR 2233
004ER 0305
MAG00550
MAG00560
MAG00570
MAG00580
MAG00590
MAG00600
MAG00610
MAG00620
MAG00630
MAG00640
MAG00650
MAG00660
MAG00670
MAG00680
MAG00690
MAG00700
MAG00710
MAG00720
MAG00730
MAG00740
MAG00750
MAG00760
MAG00770
MAG00780
MAG00790
MAG00800
MAG00810
MAG00820
MAG00830
MAG00840
MAG00850
MAG00860
MAG00870
MAG00880
MAG00890
MAG00900
MAG00910
MAG00920
MAG00930
MAG00940
MAG00950
MAG00960
MAG00970
MAG00980
MAG00990
MAG01000
MAG01010
MAG01020
MAG01030
MAG01040
MAG01050
MAG01060
MAG01070
MAG01080
MAG01090

```


APPENDIX 2 (Continued)

```

16-BIT SELCH WITH MAG TAPE USING SENSE STATUS LOOPS      PAGE 4 22:57:19 09/11/79
00CER 4567          160      DC  X'89AR',X'CDEF'          MAG01610
00DOR 89AB          161      DC  X'0123',X'4567'          MAG01620
00DOR CDEF          162      DC  X'89AR',X'CDEF'          MAG01630
00D6R 4567          163      DC  X'0123',X'4567'          MAG01640
00DAR CDEF          164      DC  X'89AR',X'CDEF'          MAG01650
00DOR 89AB          165      DC  X'0123',X'4567'          MAG01660
00E2R COEF          166      DC  X'89AR',X'CDEF'          MAG01670
00E4R 0123          167      DC  X'0123',X'4567'          MAG01680
00E6R 4567          168      DC  X'89AR',X'CDEF'          MAG01690
00EAR CDEF          169      EQU  *-1
00EER 0123          170      DC  X'8000',A(ERROR1)
00FOR 89AB          171      DC  X'8000'
00F2R CDEF          172      DC  A(END1)
00F4R 8000          173      DC  X'F0'
00F6R 0088R        174      MTAOR DC  X'85'
00F8R 9000          175      SELRO DB  X'30'
00FAR 0038R        176      SELWRT DB  X'10'
00FCR 00F0          177      STOP  DB  X'08'
00FER 0085          178      RESET DB  X'E0'
0100R 30           179      FILWRK DB  X'30'
0101R 10           180      BKSP  DB  X'11'
0102R 08           181      REWIND DB  X'38'
0103R E0           182      WRTCMD DB  X'22'
0104R 30           183      RDCMD  DB  X'21'
0105R 11           184      END
0106R 38
0107R 22
0108R 21
010AR

SELECTOR CHANNEL ADDRESS
MAG TAPE ADDRESS
SELCH READ COMMAND
SELCH WRITE COMMAND
SELCH STOP COMMAND
RESET COMMAND
FILEMARK COMMAND
BACKSPACE COMMAND
REWIND COMMAND FOR MAG TAPE
WRITE COMMAND FOR MAG TAPE
READ COMMAND FOR MAG TAPE

```


APPENDIX 2 (Continued)

16-BIT SELCH WITH MAG TAPE USING IMMEDIATE INTERRUPTS PAGE 1 22:54:17 09/11/79

PRJG= MAGTP ASSEMBLED BY CAL 03-066R05-01 (32-BIT)

1 MAGTP PROG 16-BIT SELCH WITH MAG TAPE USING IMMEDIATE INTERRUPTS
 2 SCRAT
 3 WIDTH 120
 4 CROSS
 5 TARGT 16

MAG00020
 MAG00030
 MAG00040
 MAG00050
 MAG00070
 MAG00080
 MAG00090
 MAG00100
 MAG00110
 MAG00120
 MAG00130
 MAG00140
 MAG00150
 MAG00160
 MAG00170
 MAG00180
 MAG00190
 MAG00200
 MAG00210
 MAG00220
 MAG00230
 MAG00240
 MAG00250
 MAG00260
 MAG00270
 MAG00280
 MAG00290
 MAG00300
 MAG00310
 MAG00320
 MAG00330
 MAG00340
 MAG00350
 MAG00360
 MAG00370
 MAG00380
 MAG00390
 MAG00400
 MAG00410
 MAG00420
 MAG00430
 MAG00440
 MAG00450
 MAG00460
 MAG00470
 MAG00480
 MAG00490
 MAG00500
 MAG00510
 MAG00520
 MAG00530
 MAG00540

6 * THIS PROGRAM USES THE SELCH AND IMMEDIATE
 7 * INTERRUPTS TO WRITE A BUFFER TO THE
 8 * MAG TAPE AND THEN READ IT BACK
 9 * INTO MEMORY.
 10 *
 11 * 16-BIT PROCESSORS
 12 *
 13 * REGISTER ASSIGNMENTS:
 14 *
 15 *

16 R1 EQU 1 START OF BUFFER REGISTER
 17 DU EQU 1 DEVICE UNAVAILABLE
 18 R2 EQU 2 END OF BUFFER REGISTER
 19 R3 EQU 3 DEVIC ADDRESS REGISTER
 20 R4 EQU 4 STATUS
 21 R5 EQU 5 RETURN REGISTER
 22 R7 EQU 7 COUNTER
 23 BSY EQU 8 BUSY
 24 R8 EQU 8 COMMAND BYTE STORE
 25 R9 EQU 9 SELCH ADDRESS REGISTER
 26 R10 EQU 10 SELCH COMMAND HOLD REGISTER
 27 R11 EQU 11 WORK REGISTER
 28 R12 EQU 12 RETURN REGISTER
 29 R13 EQU 13 INTERRUPT DEVICE REGISTER
 30 R14 EQU 14 INTERRUPT STATUS REGISTER
 31 R15 EQU 15 WORK REGISTER
 32 TEST EQU X'10'
 33 BOT EQU X'20'
 34 TERM EQU X'C0'
 35 XHR R15,R15
 36 STH R15,X'44'
 37 LHI R15,MTINT
 38 STH R15,X'46'
 39 LH R3,MTADR
 40 R9,SELCH
 41 OC R9,STOP
 42 OC R3,RESET
 43 SRR R3,R4
 44 BTC DU,DU,TEST
 45 THI R4,BOT
 46 BZS WRT
 47 BAL R5,FLMK
 48 BAL R5,BAKSP
 49 WRT LHI R1,5UFSRT
 50 LHI R2,8UFBR
 51 BAL R12,WRITE
 52 *
 53 BAL R5,BAKSP

START OF BUFFER REGISTER
 DEVICE UNAVAILABLE
 END OF BUFFER REGISTER
 DEVIC ADDRESS REGISTER
 STATUS
 RETURN REGISTER
 COUNTER
 BUSY
 COMMAND BYTE STORE
 SELCH ADDRESS REGISTER
 SELCH COMMAND HOLD REGISTER
 WORK REGISTER
 RETURN REGISTER
 INTERRUPT DEVICE REGISTER
 INTERRUPT STATUS REGISTER
 WORK REGISTER
 TEST=NO MOTION BIT
 TEST FOR BOT
 TERMINATION STATUS=X'C0'
 ZERO OUT NEW PSM
 LOAD INTERRUPT HANDLER
 STORE INTERRUPT HANDLER IN NEW LOC
 LOAD MAG TAPE ADDRESS
 LOAD SELCH ADDRESS
 STOP SELCH
 OUTPUT RESET TO MAG TAPE
 SENSE STATUS ON MAG TAPE
 IS MAG TAPE UNAVAILABLE
 TEST FOR BOT
 BRANCH TO WRITE
 FILEMARK ROUTINE
 BACKSPACE ROUTINE
 LOAD START OF BUFFER
 LOAD END OF BUFFER
 DO WRITE
 BACKSPACE TAPE ROUTINE

APPENDIX 2 (Continued)

```

0042R 41C0 006AR
0046R C200 0100R

54 ENJ1 BAL R12,READ DD READ
55 *****
56 *****
57 * SUBROUTINE: BAKSP *****
58 * THIS SUBROUTINE BACKSPACES ONE RECORD.
59 * SEE CALLING SEQUENCE BELOW.
60 *
61 * SUBROUTINE: FLMK
62 * THIS SUBROUTINE WRITES A FILEMARK
63 * SEE CALLING SEQUENCE BELOW.
64 *
65 *****
66 *****
67 *
68 * SUBROUTINE: NOMOT
69 * THIS SUBROUTINE WAITS FOR THE MAG TAPE TO
70 * STOP MOVING.
71 *
72 * CALLING SEQUENCE: BAL R5,BAKSP
73 * ----- BAL R5,FLMK
74 * ----- BAL R5,NOMOT
75 * R3=DEVICE ADDRESS REGISTER
76 * R5=RETURN REGISTER
77 *
78 * WORK REGISTERS:
79 * -----
80 * R4=DEVICE STATUS REGISTER
81 *
82 *****
83 *
84 BAKSP OC R3,BKSP OUTPUT A BACKSPACE
85 BS NOMOT BRANCH TO NOMOTION
86 FLMK OC R3,FILEMRK OUTPUT A FILEMARK
87 NOMOT SSR R3,R4 SENSE STATUS
88 THI R4,TEST TEST FOR NO MOTION
89 BZS NOMOT SENSE AGAIN
90 BR R5 RETURN
91 *****
92 *
93 * SUBROUTINE: WRITE *****
94 * THIS SUBROUTINE WRITES ONE BLOCK OF DATA TO THE
95 * MAG TAPE.
96 * SEE CALLING SEQUENCE BELOW
97 *
98 * SUBROUTINE READ *****
99 * THIS SUBROUTINE READS A BLOCK OF DATA FROM THE
100 * MAG TAPE.
101 *
102 * CALLING SEQUENCE: BAL R12,WRITE
103 * ----- BAL R12,READ
104 * R1=START OF BUFFER
105 * R2=END OF BUFFER
106 * R3=DEVICE ADDRESS REGISTER
107 * R9=SELCH ADDRESS REGISTER
108 * R12=RETURN REGISTER

```

APPENDIX 2 (Continued)

```

109 *
110 *
111 *
112 *
113 *
114 *
115 *
116 *
117 *
118 *
119 *
120 *
121 *
122 *
123 *
124 *
125 *
126 *
127 *
128 *
129 *
130 *
131 *
132 *
133 *
134 *
135 *
136 *
137 *
138 *
139 *
140 *
141 *
142 *
143 *
144 *
145 *
146 *
147 *
148 *
149 *
150 *
151 *
152 *
153 *
154 *
155 *
156 *
157 *
158 *
159 *
160 *
161 *
162 *

WORK REGISTERS:
-----
R4=STATUS REGISTER
R5=NOMOTION RETURN REGISTER
R7=RETRY COUNT
R8=COMMAND REGISTER
R10=SELCH COMMAND REGISTER
R11=WORK REGISTER
R13=INTERRUPT DEVICE REGISTER
R14=INTERRUPT STATUS REGISTER
*****
LB R8,WRTCMD
LB R10,SFLWRT
B RETRY
*****
LB R8,RDCMD
LB R10,SFLRD
XHR R7,R7
SSR R3,R4
BTBS DU+BSY,SENSE1
OC R9,STOP
WHR R9,R1
WHR R9,R2
OCR R3,R8
OCR R9,R10
LPSW WAIT
AIR R13,R14
OC R9,STOP
RHR R9,R11
OC R9,STOP
SSR R3,R4
THI R4,TERM
BNZ ERROR
CLHR R11,R2
BNE ERROR
BAL R5,NOMOT
BR R12
*****
* THIS ROUTINE ABORTS THE PROGRAM ON NON-RECOVERABLE STATUS ERRORS
* AND TRIES 5 TIMES TO RECOVER ON DATA TRANSFER STATUS ERRORS
* BEFORE TERMINATING.
*****
CLHI R7,5
BE ERROR1
BAL R5,NOMOT
BAL R5,BAKSP
AIS R7,1
B SENSE1
LPSW ERROR2
ALIGN 4
*****
DC X'0123',X'4567'
DC X'89AR',X'CDEF'

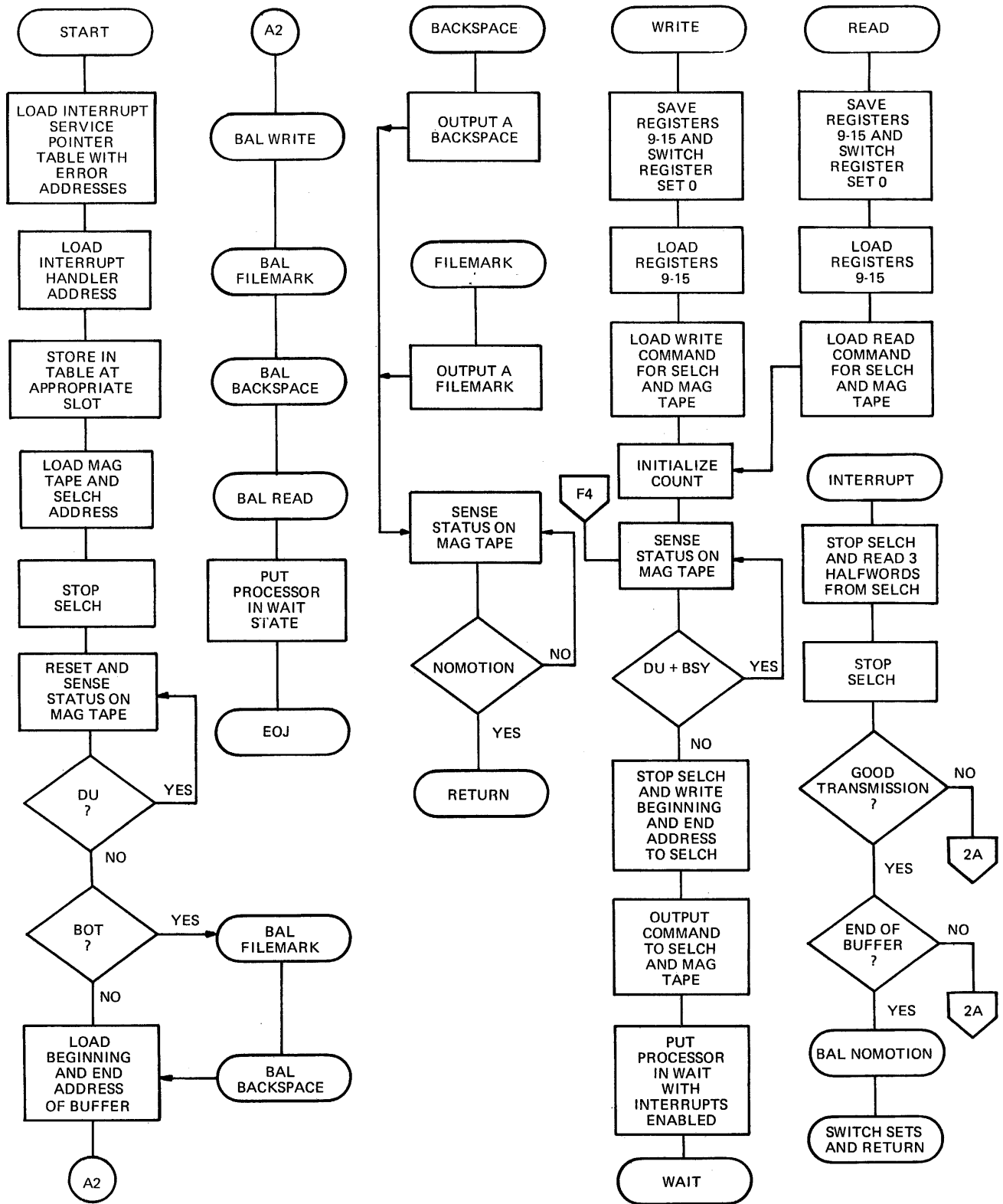
```


APPENDIX 2 (Continued)

16-BIT SELCH WITH MAG TAPE USING IMMEDIATE INTERRUPTS PAGE 6 22:54:17 09/11/79

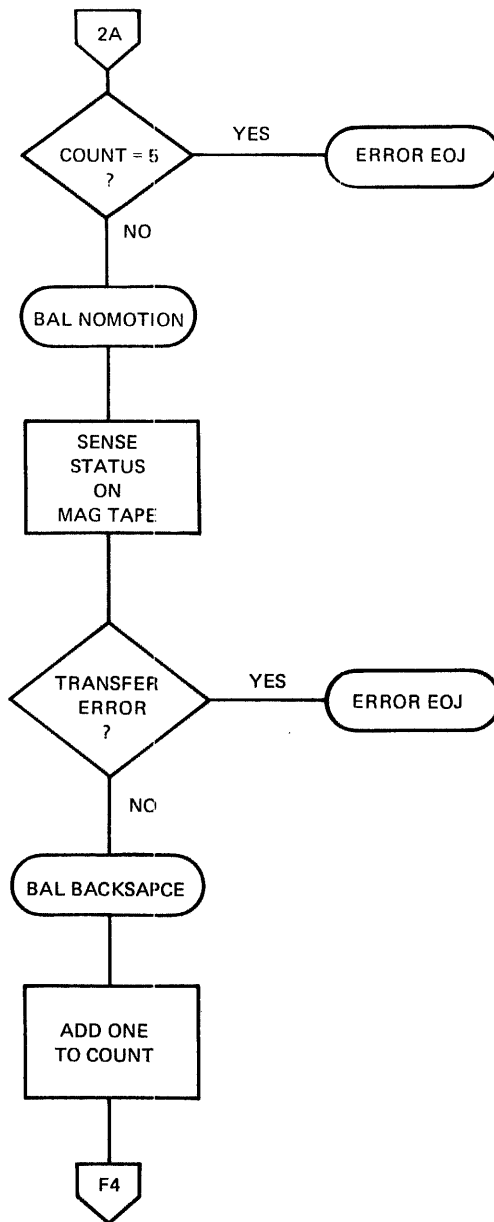
TERM	0000 00C0	34*	141
TEST	0000 0010	32*	98
WAIT	0000 0108R	135	181*
WAITL	0000 0084R	135*	182
WRITE	0000 005ER	51	121*
WRT	0000 0032R	46	49*
WRTCMD	0000 0113R	121	190*

APPENDIX 3
PROGRAMS AND FLOW CHARTS FOR 32-BIT PROCESSOR



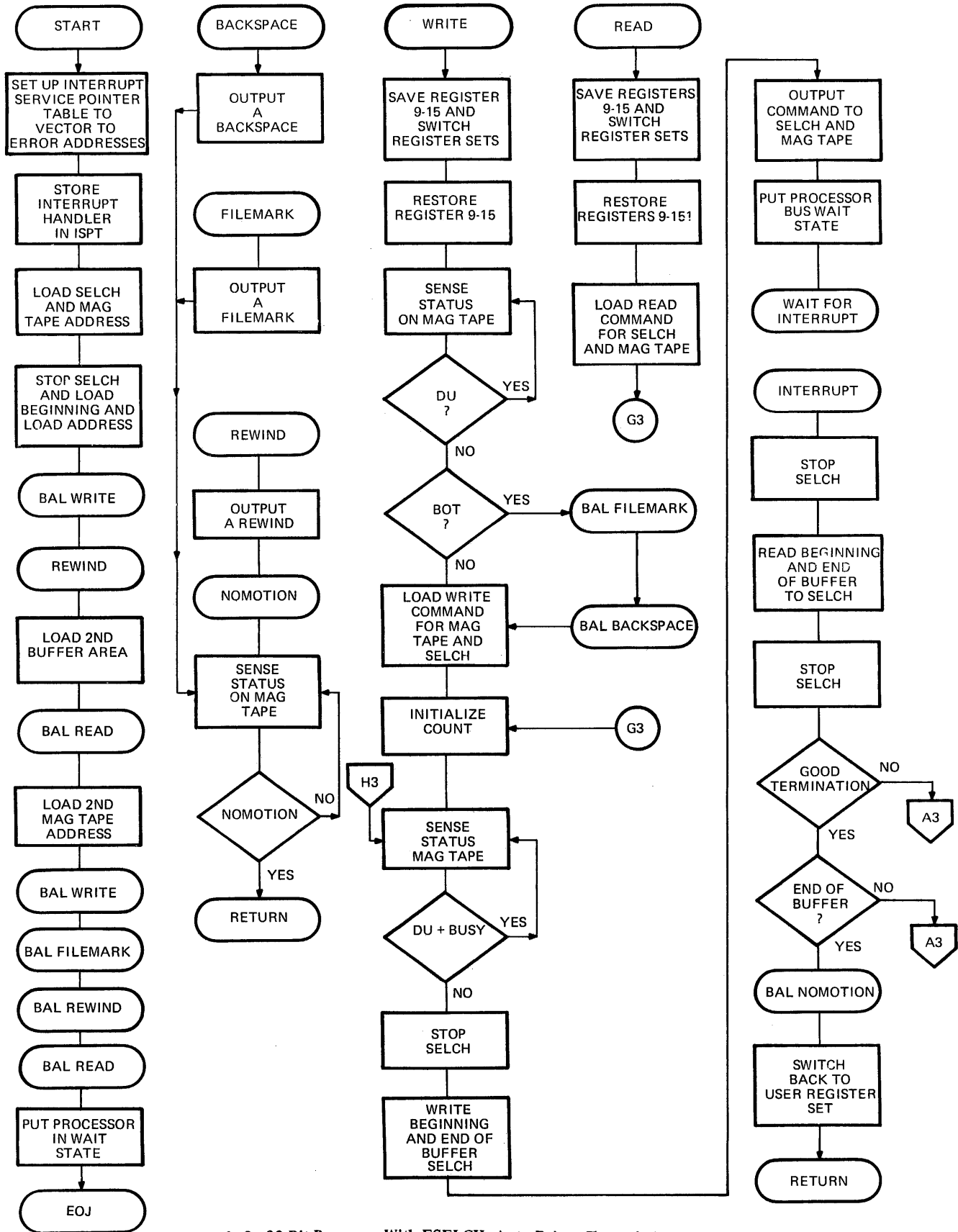
Example 1. 32 Bit Processor With Immediate Interrupts, And Mag Tape

APPENDIX 3 (Continued)



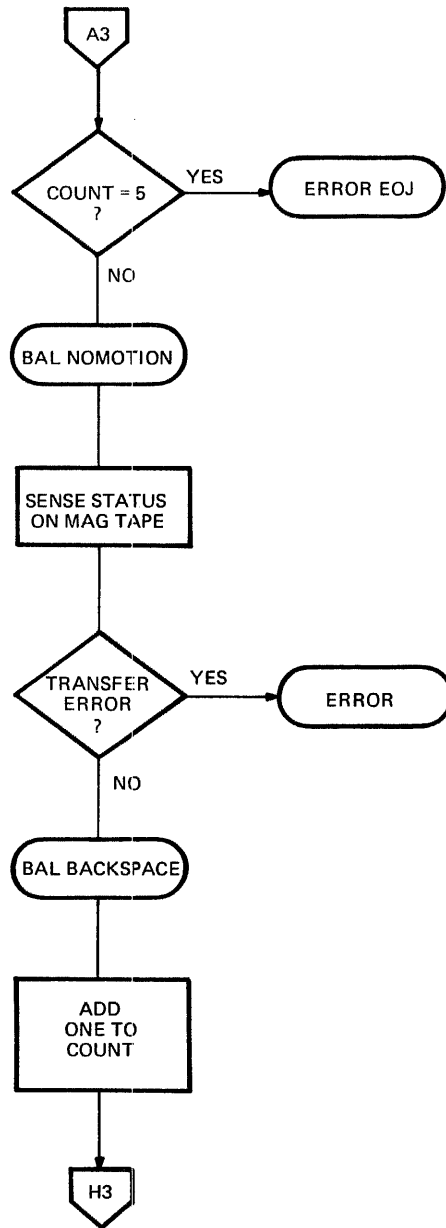
Example 1. (Continued)

APPENDIX 3 (Continued)



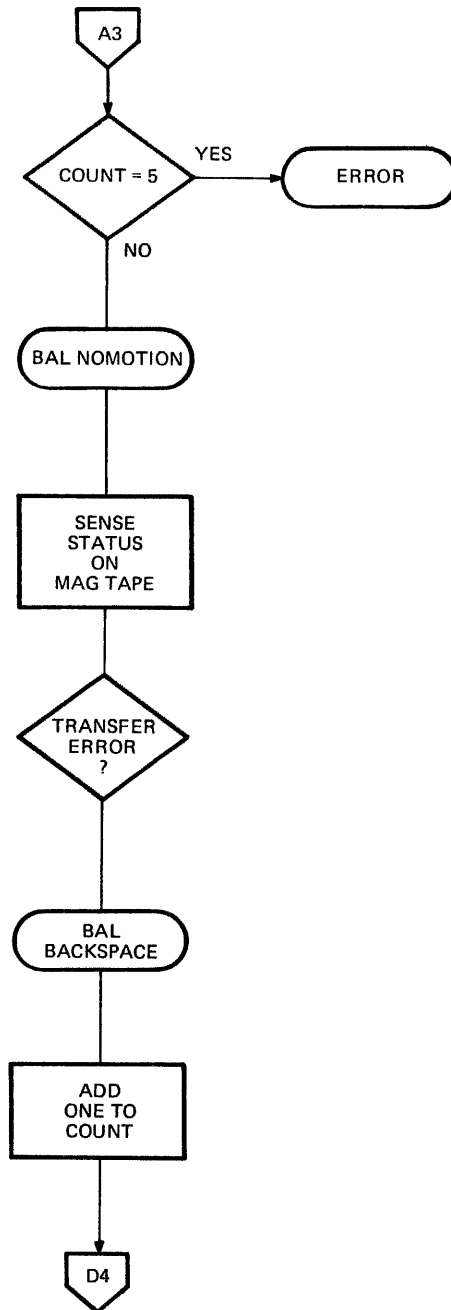
Example 2. 32 Bit Processor With ESELCH, Auto Driver Channel, And Mag Tape

APPENDIX 3 (Continued)



Example 2. (Continued)

APPENDIX 3 (Continued)



Example 3. (Continued)

APPENDIX 3 (Continued)

```

32-BIT          R/W,MULTI-DEVICE,E-SELCH,IMMEDIATE INTERRUPTS PAGE 1 23:05:51 09/11/79
PRG= MAGTP     ASSEMBLED BY CAL 03-066R05-01 (32-BIT)
1  MAGTP      PROG 32-BIT      R/W,MULTI-DEVICE,E-SELCH,IMMEDIATE INTERRUPTS
2  SCRAT
3  WIDTH 120
4  CROSS
5  NORX3
6  TARGET 32
7
8  * THIS PROGRAM USES THE E-SELCH AND IMMEDIATE
9  * INTERRUPTS TO WRITE A BUFFER TO THE
10 * MAG TAPE,READ IT BACK INTO A SECOND BUFFER AREA AND
11 * COPY IT ONTO ANOTHER MAG TAPE.
12 *
13 * 32-BIT PROCESSORS
14 *
15 * NOTE: THE INTERRUPT SERVICE POINTER TABLE IS SET
16 * UP TO VECTOR TO ERROR ROUTINES (EOJ) EXCEPT
17 * FOR THE SELCH INTERRUPT HANDLER
18 *
19 * REGISTER ASSIGNMENTS:
20 *
21 R0 EQU 0      WORK REGISTER
22 R1 EQU 1      WORK REGISTER
23 DU EQU 1      DEVICE UNAVAILABLE
24 R3 EQU 3      STATUS REGISTER
25 R4 EQU 4      STATUS
26 R5 EQU 5      RETURN REGISTER
27 R7 EQU 7      COUNTER
28 BSY EQU 8     RUSY
29 R8 EQU 8     COMMAND BYTE STORE
30 R9 EQU 9     SELCH ADDRESS REGISTER
31 R10 EQU 10    SELCH COMMAND HOLD REGISTER
32 R12 EQU 12    MAG TAPE ADDRESS REGISTER
33 R13 EQU 13    BUFFER START ADDRESS REGISTER
34 R14 EQU 14    BUFFER END ADDRESS REGISTER
35 R15 EQU 15    RETURN REGISTER
36 TEST EQU X'10' TEST=NO MOTION BIT
37 BOT EQU X'20' TEST FOR BOT
38 TERM EQU X'C0' TERMINATION STATUS=X'C0'
39 *
40 * SWITCH INTO REGISTER SET 0
41 *
42 * LHI R15,X'00F0'
43 * EPSR R9,R15
44 *
45 * SET UP INTERRUPT SERVICE
46 * POINTER TABLE
47 *
48 XR R15,R15
49 LHI R12,ERROR1
50 LDAGAIN STH R12,X'D0'(R15)
51 AIS R15,R2
52 CLHI R15,X'200'
53 BL LDAGAIN

0000 0000
0000 0001
0000 0001
0000 0003
0000 0004
0000 0005
0000 0007
0000 0008
0000 0008
0000 0009
0000 000A
0000 000C
0000 000D
0000 000E
0000 000F
0000 0010
0000 0020
0000 00C0

0000001 C8F0 00F0
0000004 959F

0000061 07FF
0000081 C8C0 0064I
00000C1 40CF 00D0
0000101 26F2
0000121 C5F0 0200
0000161 4280 FFF2 =00000C1

MAG00010
MAG00020
MAG00030
MAG00040
MAG00050
MAG00060
MAG00070
MAG00080
MAG00090
MAG00100
MAG00110
MAG00120
MAG00130
MAG00140
MAG00150
MAG00160
MAG00170
MAG00180
MAG00190
MAG00200
MAG00210
MAG00220
MAG00230
MAG00240
MAG00250
MAG00260
MAG00270
MAG00280
MAG00290
MAG00300
MAG00310
MAG00320
MAG00330
MAG00340
MAG00350
MAG00360
MAG00370
MAG00380
MAG00390
MAG00400
MAG00410
MAG00420
MAG00430
MAG00440
MAG00450
MAG00460
MAG00470
MAG00480
MAG00490
MAG00500
MAG00510
MAG00520
MAG00530

```

APPENDIX 3 (Continued)

```

32-BIT      R/W,MULTI-DEVICE,IE-SELCH,IMMEDIATE INTERRUPTS PAGE 2 23:05:51 09/11/79
00001A1 4890 81DA =0001F81
00001E1 1191
0J00201 C8F0 00EAI
0000241 40F9 00D0

54 * * * * *
55 * * * * *
56 * * * * *
57 * * * * *
58 * * * * *
59 * * * * *
60 * * * * *
61 * * * * *
62 * * * * *
63 * * * * *
64 * * * * *
65 * * * * *
66 * * * * *
67 * * * * *
68 * * * * *
69 * * * * *
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93 * * * * *
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96 * * * * *
97 * * * * *
98 * * * * *
99 * * * * *
100 * * * * *
101 * * * * *
102 * * * * *
103 * * * * *
104 * * * * *
105 * * * * *
106 * * * * *
107 * * * * *
108 * * * * *

LH R9,SELCH
SLLS R9,1
LHI R15,STNTR
STH R15,X'D0,(R9)

SET UP SELCH AND MAG TAPE

LH R9,SELCH
OC R9,STOP
LH R12,MTADR
LA R13,81UFST2
LA R14,81UFEND
BAL R15,WRITE
BAL R5,REWIND
LA R13,81UFST2
LA R14,81UFEND2
BAL R15,READ
LH R12,MTADR2
BAL R15,WRITE
BAL R5,FLWK
BAL R5,REWIND
BAL R15,READ
LPSW END

*****
* SUBROUTINE: BAKSP
* THIS SUBROUTINE BACKSPACES ONE RECORD.
* SEE CALLING SEQUENCE BELOW.
*
SUBROUTINE: FLWK
THIS SUBROUTINE WRITES A FILEMARK
SEE CALLING SEQUENCE BELOW.
*
SUBROUTINE: REWIND
THIS SUBROUTINE REWINDS THE MAG TAPE.
SEE CALLING SEQUENCE BELOW
*****

LH R9,SELCH ADDRESS
STOP SELCH
LOAD MAG TAPE ADDRESS
LOAD START OF BUFFER ADDRESS
LOAD END OF BUFFER
DO WRITE
REWIND ROUTINE
LOAD START OF SECOND BUFFER
LOAD END OF SECOND BUFFER
DO READ
LOAD ADDRESS OF SECOND MAG TAPE
DO WRITE ROUTINE
FILEMARK ROUTINE
REWIND ROUTINE
DO READ ROUTINE

*****
* SUBROUTINE: NOMOT
* THIS SUBROUTINE WAITS FOR THE MAG TAPE TO
STOP MOVING.
CALLING SEQUENCE: BAL R5,BAKSP
BAL R5,FLWK
BAL R5,REWIND
BAL R5,NOMOT
R12=DEVICE ADDRESS REGISTER
R5=RETURN REGISTER
WORK REGISTERS:
R4=DEVICE STATUS REGISTER
*****

```

MAG00540
MAG00550
MAG00560
MAG00570
MAG00580
MAG00590
MAG00600
MAG00610
MAG00620
MAG00630
MAG00640
MAG00650
MAG00660
MAG00670
MAG00680
MAG00690
MAG00700
MAG00710
MAG00720
MAG00730
MAG00740
MAG00750
MAG00760
MAG00770
MAG00780
MAG00790
MAG00800
MAG00810
MAG00820
MAG00830
MAG00840
MAG00850
MAG00860
MAG00870
MAG00880
MAG00890
MAG00900
MAG00910
MAG00920
MAG00930
MAG00940
MAG00950
MAG00960
MAG00970
MAG00980
MAG00990
MAG01000
MAG01010
MAG01020
MAG01030
MAG01040
MAG01050
MAG01060
MAG01070
MAG01080

APPENDIX 3 (Continued)

```

32-BIT          R/W,MULTI-DEVICE,SE-SELCH,IMMEDIATE INTERRUPTS PAGE 3 23:05:51 09/11/79
000068I  DECO 8197 =000203I
000069I  2306
00006EI  DECO 8190 =000202I
000072I  2303
000074I  DECO 818C =000204I
000078I  9DC4
00007AI  C340 0010
00007EI  2333
000080I  0305

109 *
110 BAKSP OC R12,BKSP OUTPUT A BACKSPACE
111 BS NOMOT BRANCH TO NOMOTION
112 FLMK OC R12,FILMRK OUTPUT A FILEMARK
113 BS NOMOT BRANCH TO NOMOTION
114 REWD OC R12,RFWIND OUTPUT A REMIND
115 NOMOT SSR R12,R4 SENSE STATUS ON MAG TAPE
116 THI R4,TEST TEST FOR NO MOTION
117 BZS NOMOT SENSE AGAIN
118 BR R5 RETURN
119 *****
120 * SUBROUTINE: WRITE
121 * THIS SUBROUTINE WRITES ONE BLOCK OF DATA TO THE
122 * MAG TAPE.
123 * SEE CALLING SEQUENCE BELOW
124 *
125 * SUBROUTINE READ
126 * THIS SUBROUTINE READS A BLOCK OF DATA FROM THE
127 * MAG TAPE.
128 *
129 * CALLING SEQUENCE: BAL R12,WRITE
130 * BAL R12,READ
131 *
132 * R9 =SELCH ADDRESS REGISTER
133 * R12=MAG TAPE ADDRESS REGISTER
134 * R13=BUFFER START REGISTER
135 * R14=BUFFER END REGISTER
136 * R15=RETURN REGISTER
137 *
138 * WORK REGISTERS:
139 * -----
140 * REGISTER SET 0 IS USED TO PERFORM THE READ/WRITE ROUTINES
141 *
142 * *****
143 * WRITE STM R9,REGSAV STORE R9-R15 IN SAVE AREA
144 * LPSW SWITCH1 SWITCH TO REGISTER SET 0
145 * LM R9,REGSAV RESTORE REGISTERS 9-15
146 * SSR R12,R4 SENSE STATUS ON MAG TAPE
147 * BTC DU,SENS IS MAG TAPE UNAVAILABLE
148 * THI R4,BOT TEST FOR BOT
149 * BZ WRT BRANCH TO WRT
150 * BAL R5,FLMK FILEMARK ROUTINE
151 * BAL R5,BAKSP BACKSPACE ROUTINE
152 * LB R8,WRTCMD LOAD WRITE COMMAND FOR SELCH
153 * LB R10,SFLWRT LOAD SELCH WRITE
154 * B RETRY BRANCH TO RETRY
155 *
156 * READ STM R9,REGSAV STORE R9-R15 IN SAVE AREA
157 * SWITCH2 SWITCH TO REGISTER SET 0
158 * LM R9,REGSAV RESTORE REGISTERS 9-15
159 * LB R8,RDCMD LOAD READ COMMAND FOR MAG TAPE
160 * LB R10,SFLRD LOAD SELCH READ
161 * XR R7,R7 ZERO OUT COUNT
162 * RETRY XR R7,R7 SENSE STATUS ON MAG TAPE
163 * SENSE1 SSR R12,R4
164 *
000082I  D090 80CA =000150I
000086I  C200 80AE =000138I
00008AI  J190 80C2 =000150I
00008EI  9DC4 4210 FFFA =00008EI
000090I  C340 0020
000094I  C340 8008 =0000A4I
000098I  4150 FFCE =00006EI
00009CI  4150 FFC4 =000068I
0000A0I  D380 815D =000205I
0000A4I  D3A0 8153 =0001FFI
0000A8I  4300 8014 =0000C4I
0000ACI
0000B0I  D090 809C =000150I
0000B4I  C200 8088 =000140I
0000B8I  D190 8094 =000150I
0000BCI  D380 8146 =000206I
0000C0I  D3A0 813A =0001FEI
0000C4I  0777
0000C6I  9DC4

```

APPENDIX 3 (Continued)

32-BIT	R/W, MULTI-DEVICE, E-SELCH, IMMEDIATE INTERRUPTS	PAGE	4	23:05:51	09/11/79	
0000C8I	2091					MAG01640
0000CAI	DE90	8132	=000200I		TEST FOR BUSY+DEVICE UNAVAILABLE	MAG01650
0000CEI	084D				STOP SELCH	MAG01660
0000DDI	EC40	0010			LOAD START OF BUFFER IN WORK AREA	MAG01670
0000D4I	9A94				SHIFT OVER 16 PLACES	MAG01680
0000D6I	989D				WRITE UPPER BYTE	MAG01690
0000D8I	084E				WRITE LOWER HALFWORD	MAG01700
0000DAI	EC40	0010			LOAD END OF BUFFER IN WORK AREA	MAG01710
0000DEI	9A94				SHIFT OVER TO WRITE HIGH ORDER BITS	MAG01720
0000E0I	989E				WRITE UPPER BYTE	MAG01730
0000E2I	9EC8				WRITE LOWER HALFWORD	MAG01740
0000E4I	9E9A				OUTPUT COMMAND TO MAG TAPE	MAG01750
0000E6I	C200	805E	=000148I		OUTPUT COMMAND TO SELCH	MAG01760
0000EAI	DE90	8112	=000200I		WAIT FOR INTERRUPTS	MAG01770
0000EEI	9894				STOP SELCH	MAG01780
0000FOI	ED40	0010			READ UPPER BYTE	MAG01790
0000F4I	9994				SHIFT OVER TO ACCESS LOW ORDER BITS	MAG01800
0000F6I	DE90	8106	=000200I		READ LOWER BYTE	MAG01810
0000FAI	C330	80C0			STOP THE SELCH	MAG01820
0000FEI	4230	8012	=000114I		TEST FOR GOOD TRANSMISSION	MAG01830
000102I	05E4				BRANCH TO TRY AGAIN.	MAG01840
000104I	4230	800C	=000114I		TEST FOR END OF BUFFER	MAG01850
000108I	4150	FF6C	=000078I		BRANCH TO ERROR	MAG01860
00010CI	C800	00F0			WAIT FOR TAPE TO STOP	MAG01870
000110I	9510				CHANGE TO REGISTER SET F	MAG01880
000112I	030F				ACTIVATE NEW REGISTER SET	MAG01890
					RETURN	MAG01900
					* THIS ROUTINE ABORTS THE PROGRAM ON NON-RECOVERABLE STATUS ERRORS	MAG01910
					* AND TRIES 5 TIMES TO RECOVER ON DATA TRANSFER STATUS ERRORS	MAG01920
					* BEFORE TERMINATING.	MAG01930
000114I	C570	0005			CLHI R7,5	MAG01940
000118I	4330	FF48	=000064I		BE ERROR1	MAG01950
00011CI	4150	FF58	=000078I		BAL R5,NOMOT	MAG01960
000120I	4150	FF44	=000068I		BAL R5,BAKSP	MAG01970
000124I	2671				AIS R7,1	MAG01980
000126I	4300	FF9C	=0000C6I		B SENSE1	MAG01990
					ERROR1 EQU ENDI	MAG02000
000130I	0000	8000			ALIGN 8	MAG02010
000134I	0000	0641			DC Y'8000'	MAG02020
000138I	0000	0000			DC A(ENDI)	MAG02030
00013CI	0000	008A1			DC Y'0000'	MAG02040
000140I	0000	0000			DC A(WRITE1)	MAG02050
000144I	0000	0088I			DC Y'0000'	MAG02060
000148I	0000	C000			DC A(READ1)	MAG02070
00014CI	0000	00E6I			DC Y'C000'	MAG02080
000150I	0000	01BEI			DC WAIT1	MAG02090
000188I	0000	0123			DC REGSAV DS 56	MAG02100
0001C0I	0123	4567			DC BUFST2 DS 56	MAG02110
0001C2I	89AB	CDEF			DC BUFEND2 EQU *-2	MAG02120
0001C6I	0123	4567			DC X'0123',X'4567'	MAG02130
0001C8I	0123	4567			DC X'89AB',X'CDEF'	MAG02140
0001CAI	4567				DC X'0123',X'4567'	MAG02150

32-BIT

0001CCI 89AB	216	DC	X'89AB',X'CDEF'	MAG02160
0001CEI CDEF	217	DC	X'0123',X'4567'	MAG02170
0001D0I 0123	218	DC	X'89AR',X'CDEF'	MAG02180
0001D2I 4567	219	DC	X'0123',X'4567'	MAG02190
0001D4I 89AB	220	DC	X'89AR',X'CDEF'	MAG02200
0001D6I CDEF	221	DC	X'0123',X'4567'	MAG02210
0001D8I 0123	222	DC	X'89AB',X'CDEF'	MAG02220
0001DAI 4567	223	DC	X'0123',X'4567'	MAG02230
0001DCI 89AB	224	DC	X'89AR',X'CDEF'	MAG02240
0001DEI CDEF	225	DC	X'0123',X'4567'	MAG02250
0001E0I 0123	226	DC	X'89AR',X'CDEF'	MAG02260
0001E2I 4567	227	EGU	**1	MAG02270
0001E4I 89AB	228	DC	X'F0'	MAG02280
0001E6I CDEF	229	DC	X'85'	MAG02290
0001E8I 0123	230	DC	X'C5'	MAG02300
0001EAI 4567	231	DB	X'70'	MAG02310
0001ECI 89AB	232	DB	X'50'	MAG02320
0001EEI CDEF	233	DB	X'48'	MAG02330
0001F0I 0123	234	DB	X'E0'	MAG02340
0001F2I 4567	235	DB	X'30'	MAG02350
0001F4I 89AB	236	DB	X'11'	MAG02360
0001F6I CDEF	237	DB	X'38'	MAG02370
0000 01F7I	238	DB	X'22'	MAG02380
0001F8I 00F0	239	DB	X'21'	MAG02390
0001FAI 0085	240	END		MAG02400
0001FCI 00C5				
0001FEI 70				
0001FFI 50				
000200I 48				
000201I E0				
000202I 30				
000203I 11				
000204I 38				
000205I 22				
000206I 21				
000208I				

APPENDIX 3 (Continued)

SELECTOR CHANNEL ADDRESS
MAG TAPE ADDRESS
SECOND MAG TAPE ADDRESS
READ, EXT ADD
WRITE, EXT ADD
STOP, EXT ADD
RESET COMMAND
FILEMARK COMMAND
BACKSPACE COMMAND
REWIND COMMAND
WRITE COMMAND FOR MAG TAPE
READ COMMAND FOR MAG TAPE

REWMD	0000 0074I	67	74	114*
SELCH	0000 01F8I	54	61	228*
SELRO	0000 01FEI	161	231*	
SELWRT	0000 01FFI	154	232*	
SENS	0000 008EI	147*	148	
SENSE1	0000 00C6I	163*	164	199
SINTR	0000 00EAI	56	177*	
STOP	0000 0200I	62	165	177
SWITCH1	0000 0138I	145	204*	181
SWITCH2	0000 0140I	158	206*	233*
TERM	0000 00C0	38*	182	
TEST	0000 0010	36*	116	
WAIT	0000 0148I	176	208*	
WAIT1	0000 00E6I	176*	209	
WRITE	0000 0082I	66	72	144*
WRITE1	0000 008AI	146*	205	
WRT	0000 00A4I	150	153*	
WRTCMD	0000 0205I	153	238*	

APPENDIX 3 (Continued)

```

0000 0000
0000 0001
0000 0002
0000 0003
0000 0004
0000 0005
0000 0007
0000 0008
0000 0009
0000 000A
0000 000C
0000 000D
0000 000E
0000 000F
0000 0010
0000 0020
0000 00C0

090000I C8F0 00F0
000004I 959F
000006I 07FF
000008I CAC0 005CI
00000CI 40CF 0000
090010I 25F2
000012I C5F0 0200
*000016I 2085

1  MAGTP  PROG  32-BIT  R/W,E-SELCH,IMMEDIATE INTERRUPTS
2  SCRAT
3  WIDTH 120
4  CROSS
5  NORX3
6  TARGET 32
7  SQUEZ 2
8  *
9  * THIS PROGRAM USES THE E-SELCH AND IMMEDIATE
10 * INTERRUPTS TO WRITE A BUFFER TO THE
11 * MAG TAPE AND THEN READ IT BACK
12 * INTO MEMORY.
13 *
14 * 32-BIT PROCESSORS
15 *
16 * NOTE: THE INTERRUPT SERVICE POINTER TABLE IS SET
17 * UP TO VECTOR TO ERROR ROUTINES (EOJ) EXCEPT
18 * FOR THE SELCH INTERRUPT HANDLER
19 *
20 * REGISTER ASSIGNMENTS:
21 *
22 R0 EQU 0
23 R1 EQU 1
24 DU EQU 1
25 R2 EQU 2
26 R3 EQU 3
27 R4 EQU 4
28 R5 EQU 5
29 R7 EQU 7
30 BSY EQU 8
31 R8 EQU 8
32 R9 EQU 9
33 R10 EQU 10
34 R12 EQU 12
35 R13 EQU 13
36 R14 EQU 14
37 R15 EQU 15
38 TEST EQU X'10'
39 BOT EQU X'20'
40 TERM EQU X'C0'
41 *
42 * SET UP INTERRUPT
43 * SERVICE POINTER
44 * TABLE
45 *
46 LHI R15,X'00F0'
47 EPSR R9,R15
48 XR R15,R15
49 LHI R12,ERROR1
50 LDAGAIN STH R12,X'D0',(R15)
51 AIS R15,2
52 CLHI R15,X'200'
53 BL LDAGAIN

WORK REGISTER
WORK REGISTER
DEVICE UNAVAILABLE
END OF BUFFER REGISTER
STATUS REGISTER
WORK REGISTER
RETURN REGISTER
COUNTER
BUSY
COMMAND BYTE STORE
SELCH ADDRESS REGISTER
SELCH COMMAND HOLD REGISTER
MAG TAPE ADDRESS REGISTER
START OF BUFFER REGISTER
END OF BUFFER REGISTER
RETURN REGISTER
TEST=NO MOTION BIT
TEST FOR BOT
TERMINATION STATUS=X'C0'

LOAD REGISTER SET 15
ACTIVATE
ZERO OUT WORK REGISTER
LOAD ERROR ROUTINE ADDRESS
INTO FIRST SLOT OF ISPT
BUMP COUNTER BY 2
COMPARE TO LIMIT
COMPLETE TABLE
  
```

APPENDIX 3 (Continued)

32-BIT R/W,E-SELCH,IMMEDIATE INTERRUPTS PAGE 2 23:04:16 09/11/79

```

000018I 4890 8174 =000190I 54 LH R9,SELCH LOAD SELCH ADDRESS
00001CI 1191 55 SLLS R9,1 MULTIPLY BY 2
00001EI C8F0 00C4I 56 LHI R15,STNTR LOAD INTERRUPT HANDLER
000022I 40F9 00D0 57 STH R15,X'D0,(R9) PLACE IN TABLE AT PROPER SLOT
000026I 48C0 8168 =000192I 58 LH R12,MTADR LOAD MAG TAPE ADDRESS
00002AI 4890 8162 =000190I 59 LH R9,SELCH LOAD SELCH ADDRESS
00002EI DE90 8164 =000196I 60 OC R9,STOP STOP SELCH
000032I DEC0 8161 =000197I 61 DUTEST OC R12,RFSET OUTPUT RESET TO MAG TAPE
000036I 9DC4 62 SSR R12,R4 SENSE STATUS ON MAG TAPE
*000038I 2013 63 BTC DU,DUTEST IS MAG TAPE UNAVAILABLE
00003AI C340 0020 64 THI R4,BOT TEST FOR BOT
00003EI 2335 65 BZS WRT BRANCH TO WRITE
000040I 4150 8022 =000066I 66 BAL R5,FLMK FILEMARK ROUTINE
000044I 4150 8018 =000060I 67 BAL R5,BAKSP BACKSPACE ROUTINE
000048I E6D0 810C =000158I 68 * WRT LA R13,BJFSRT LOAD START OF BUFFER
00004CI E6E0 813F =00018FI 69 LA R14,BUFEND LOAD END OF BUFFER
000050I 41F0 8020 =000074I 71 BAL R15,WRITE DO WRITE
000054I 4150 8008 =000060I 72 * BAL R5,BAKSP BACKSPACE ROUTINE
000058I 41F0 802E =00008AI 73 BAL R15,READ DO READ
00005CI C200 80A0 =000100I 74 END1 LPSW END
75 *****
76 * SUBROUTINE: BAKSP *****
77 * THIS SUBROUTINE BACKSPACES ONE RECORD.
78 * THIS SUBROUTINE BACKSPACES ONE RECORD.
79 * SEE CALLING SEQUENCE BELOW.
80 *
81 * SUBROUTINE: FLMK *****
82 * THIS SUBROUTINE WRITES A FILEMARK *****
83 * SEE CALLING SEQUENCE BELOW. *****
84 *
85 * *****
86 * *****
87 *
88 * SUBROUTINE: NOMOT *****
89 * THIS SUBROUTINE WAITS FOR THE MAG TAPE TO *****
90 * STOP MOVING. *****
91 *
92 * CALLING SEQUENCE: BAL R5,BAKSP *****
93 * ----- BAL R5,FLMK *****
94 * ----- BAL R5,NOMOT *****
95 * R12=DEVICE ADDRESS REGISTER *****
96 * R5=RETURN REGISTER *****
97 *
98 * WORK REGISTERS: *****
99 * ----- *****
100 * R4=DEVICE STATUS REGISTER *****
101 * *****
102 * *****
103 * *****
104 BAKSP OC R12,BAKSP OUTPUT A BACKSPACE
105 BS NOMOT BRANCH TO NOMOTION
106 FLMK OC R12,FTLMRK OUTPUT A FILEMARK
107 NOMOT SSR R12,R4 SENSE STATUS ON MAG TAPE
108 THI R4,TEST TEST FOR NO MOTION

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APPENDIX 3 (Continued)

32-BIT	R/W,E-SELCH,IMMEDIATE INTERRUPTS	PAGE	5	23:04:16	09/11/79	
000182I	4567					MAG02090
000184I	89AB	209	DC	X'89AR',X'CDEF'		MAG02100
000186I	CDEF					MAG02110
000188I	0123	210	DC	X'0123',X'4567'		MAG02120
00018AI	4567					MAG02130
00018CI	89AB	211	DC	X'89AR',X'CDEF'		MAG02140
00018EI	CDEF					MAG02150
0000	018FI					MAG02160
000190I	00F0	212	BUFEND	**1	SELECTOR CHANNEL ADDRESS	MAG02170
000192I	0085	213	SELCH	X'F0'	MAG TAPE ADDRESS	MAG02180
000194I	70	214	MTADR	X'85'	READ,EXT ADD	MAG02190
000195I	50	215	SELRD	X'70'	WRITE,EXT ADD	MAG02200
000197I	E0	216	SELWRT	X'50'	STOP,EXT ADD	MAG02210
000198I	30	217	STOP	X'48'	RESET COMMAND	MAG02220
000199I	11	218	RESET	X'E0'	FILEMARK COMMAND	MAG02230
00019AI	38	219	FILMRK	X'30'	BACKSPACE COMMAND	MAG02240
000198I	22	220	BKSP	X'11'	REWIND COMMAND	
00019CI	21	221	REWIND	X'88'	WRITE COMMAND FOR MAG TAPE	
00019EI		222	WRTCMD	X'22'	READ COMMAND FOR MAG TAPE	
		223	ROCMD	X'21'		
		224	END			

APPENDIX 3 (Continued)

32-BIT R/W/E-SELCH, IMMEDIATE INTERRUPTS

SELWRT	0000 0195I	140	216*
SENSE1	0000 00A0I	149*	150
SINTR	0000 00C4I	56	163*
STOP	0000 0196I	60	151
SWITCH1	0000 0108I	137	191*
SWITCH2	0000 0110I	144	193*
TERM	0000 00C0	40*	168
TEST	0000 0010	38*	108
WAIT	0000 0118I	162	195*
WAIT1	0000 00C0I	162*	196
WRITE	0000 0074I	71	136*
WRITE1	0000 007CI	138*	192
WRT	0000 0048I	65	69*
WRTCMD	0000 0198I	139	222*
		163	167
			217*

APPENDIX 3 (Continued)

PROG= MAGTP ASSEMBLED BY CAL 03-066R05-01 (32-BIT)

1	MAGTP	PROG	32-BIT	R/W,AUTO DRIVER CHANNEL
2		SCRAT		
3		WIDTH	120	
4		CROSS		
5		NORX3		
6		TARGET	32	
7		SQUEZ	2	
8	*			
9	*	THIS PROGRAM USES THE AUTO DRIVER CHANNEL TO WRITE		
10	*	A BUFFER OF DATA FROM MEMORY TO MAG TAPE		
11	*	AND THEN READ IT BACK FROM THAT MAG TAPE.		
12	*			
13	*	32-BIT PROCESSORS		
14	*			
15	*			
16	*	NOTE: THE INTERRUPT SERVICE POINTER TABLE IS SET		
17	*	UP TO VECTOR TO ERROR ROUTINES (EOJ) EXCEPT		
18	*	FOR THE SELCH INTERRUPT HANDLER		
19	*	REGISTER ASSIGNMENTS:		
20	*			
21	R0	EQU	0	WORK REGISTER
22	R1	EQU	1	WORK REGISTER
23	DU	EQU	1	DEVICE UNAVAILABLE
24	R4	EQU	4	STATUS
25	R5	EQU	5	RETURN REGISTER
26	R7	EQU	7	COUNTER
27	R8	EQU	8	COMMAND BYTE STORE
28	R9	EQU	9	SELCH ADDRESS REGISTER
29	R12	EQU	12	MAG TAPE ADDRESS REGISTER
30	R13	EQU	13	START OF BUFFER ADDRESS REGISTER
31	R14	EQU	14	END OF BUFFER ADDRESS REGISTER
32	R15	EQU	15	WORK AND RETURN REGISTER
33	TEST	EQU	X'10'	TEST FOR BOT
34	BOT	EQU	X'20'	TEST FOR BOT
35	TERM	EQU	X'C0'	TERMINATION STATUS=X'C0'
36		LHI	R15,X'00F0'	LOAD REGISTER SET 15
37		EPSR	R9,R15	ACTIVATE
38		XR	R15,R15	ZERO OUT WORK REGISTER
39		LHI	R12,ERROR1	LOAD ERROR ROUTINE ADDRESS
40	LDAGAIN	STH	R12,X'D0'(R15)	INTO FIRST SLOT OF ISPT
41		ALS	R15,2	BUMP COUNTER BY 2
42		CLHI	R15,X'2D0'	COMPARE TO LIMIT
43		BL	LDAGAIN	COMPLETE TABLE
44		LHI	R0,CCW+1	LOAD ODD ADDRESS INTO A REGISTER
45		LH	R9,MTADR	LOAD MAG TAPE ADDRESS
46		SLLS	R9,1	MULTIPLY BY 2
47		STH	R0,X'D0'(R9)	STORE AT INDICATED ADDRESS
48		LHI	R15,STNTR	LOAD INTERRUPT HANDLER
49		STH	R15,STBADR	STORE EXIT ROUT. ADDRESS INTO CCB
50		LA	R13,BUFRT	LOAD START OF BUFFER
51		LA	R14,BUFEND	LOAD END OF BUFFER
52		LH	R12,MTADR	LOAD MAG TAPE ADDRESS
53	DUTEST	OC	R12,RESET	OUTPUT RESET TO MAG TAPE

APPENDIX 3 (Continued)

32-BIT R/W,AUTO DRIVER CHANNEL PAGE 2 23:10:12 09/11/79

```

00003EI 9DC4
*000040I 2013
000042I C340 0020
000046I 2335
000048I 4150 801A =000066I
00004CI 4150 8010 =000060I

000050I 41F0 8020 =000074I
000054I 4150 8008 =000060I
000058I 41F0 802E =00008AI
00005CI C200 8090 =0000F0I

54 SSR R12,R4 SENSE STATUS ON MAG TAPE
55 BTC DU,DU,TEST IS MAG TAPE UNAVAILABLE
56 THI R4,BOT TEST FOR BOT
57 BZS WRT BRANCH TO WRITE
58 BAL R5,FLMK FILEMARK ROUTINE
59 BAL R5,BAKSP BACKSPACE ROUTINE
60 *
61 WRT BAL R15,WRITE DD WRITE ROUTINE
62 *
63 BAL R5,BAKSP BACKSPACE ROUTINE
64 BAL R15,RPAD DD READ
65 ENDI LPSM END EOJ
66 *****
67 * SUBROUTINE: BAKSP
68 * THIS SUBROUTINE BACKSPACES ONE RECORD.
69 * SEE CALLING SEQUENCE BELOW.
70 *
71 * SUBROUTINE: FLMK
72 * THIS SUBROUTINE WRITES A FILEMARK
73 * SEE CALLING SEQUENCE BELOW.
74 *
75 *
76 *****
77 *
78 * SUBROUTINE: NOMOT
79 * THIS SUBROUTINE WAITS FOR THE MAG TAPE TO
80 * STOP MOVING.
81 *
82 * CALLING SEQUENCE: BAL R5,BAKSP
83 * BAL R5,FLMK
84 * BAL R5,NOMOT
85 *
86 * R12=DEVICE ADDRESS REGISTER
87 * R5=RETURN REGISTER
88 *
89 * WORK REGISTERS:
90 *
91 * R4=DEVICE STATUS REGISTER
92 *
93 *
94 BAKSP OC R12,BKSP OUTPUT A BACKSPACE
95 BS NOMOT BRANCH TO NOMOTION
96 FLMK OC R12,FLMRK OUTPUT A FILEMARK
97 NOMOT SSR R12,R4 SENSE STATUS ON MAG TAPE
98 THI R4,TEST TEST FOR NO MOTION
99 BZS NOMOT SENSE AGAIN
100 BR R5 RETURN
101 *****
102 * SUBROUTINE: WRITE
103 * THIS SUBROUTINE WRITES ONE BLOCK OF DATA TO THE
104 * MAG TAPE.
105 * SEE CALLING SEQUENCE BELOW
106 * SUBROUTINE READ
107 * THIS SUBROUTINE READS A BLOCK OF DATA FROM THE
108 * MAG TAPE.

000060I DECO 8148 =0001ACI
000064I 2303
000066I DECO 8141 =0001ABI
00006AI 9DC4
00006CI C340 0010
000070I 2233
000072I 0305

```

MAG00540
MAG00550
MAG00560
MAG00570
MAG00580
MAG00590
MAG00600
MAG00610
MAG00620
MAG00630
MAG00640
MAG00650
MAG00660
MAG00670
MAG00680
MAG00690
MAG00700
MAG00710
MAG00720
MAG00730
MAG00740
MAG00750
MAG00760
MAG00770
MAG00780
MAG00790
MAG00800
MAG00810
MAG00820
MAG00830
MAG00840
MAG00850
MAG00860
MAG00870
MAG00880
MAG00890
MAG00900
MAG00910
MAG00920
MAG00930
MAG00940
MAG00950
MAG00960
MAG00970
MAG00980
MAG00990
MAG01000
MAG01010
MAG01020
MAG01030
MAG01040
MAG01050
MAG01060
MAG01070
MAG01080

APPENDIX 3 (Continued)

32-BIT	R/W,AUTO DRIVER CHANNEL	PAGE	4	23:10:12	09/11/79	MAG01640
0000F01	164	ALIGN 8				MAG01640
0000F01	165	END				MAG01650
0000F41	166	DC Y'8000'			PUT PROCESSOR IN WAIT STATE	MAG01660
0000F91	167	DC A(EN01)				MAG01670
0000FC1	168	DC Y'8000',A(ERROR1)				MAG01680
0001001	169	DC Y'0000'			REG SET 0	MAG01690
0001041	170	DC A(WRITE1)				MAG01700
0001081	171	DC Y'0000'			REG SET 0	MAG01710
00010C1	172	DC A(READ1)				MAG01720
0001101	173	DC Y'C000'			PUT PROCESSOR IN WAIT STATE INT ENAB	MAG01730
0001141	174	DC WAIT			WITH EXTERNAL INTERRUPTS ENABLED	MAG01740
0001181	175	DC Y'0000'			SAVE AREA FOR REGISTER 13	MAG01750
00011C1	176	DC 56			REGISTER SAVE AREA	MAG01760
0001541	177	DC X'0123',X'4567'				MAG01770
0001561	178	DC X'89AR',X'CDEF'				MAG01780
0001581	179	DC X'0123',X'4567'				MAG01790
00015A1	180	DC X'89AR',X'CDEF'				MAG01800
00015C1	181	DC X'0123',X'4567'				MAG01810
00015E1	182	DC X'89AR',X'CDEF'				MAG01820
0001601	183	DC X'0123',X'4567'				MAG01830
0001621	184	DC X'89AR',X'CDEF'				MAG01840
0001641	185	DC X'0123',X'4567'				MAG01850
0001661	186	DC X'89AR',X'CDEF'				MAG01860
0001681	187	DC X'0123',X'4567'				MAG01870
00016A1	188	DC X'89AR',X'CDEF'				MAG01880
00016C1	189	DC X'0123',X'4567'				MAG01890
00016E1	190	DC X'89AB',X'CDEF'				MAG01900
0001701	191	DC X'0123',X'4567'				MAG01910
0001721	192	DC X'89AR',X'CDEF'				MAG01920
0001741	193	DC X'0123',X'4567'				MAG01930
0001761	194	DC X'89AR',X'CDEF'				MAG01940
0001781	195	DC X'0123',X'4567'				MAG01950
00017A1	196	DC X'89AR',X'CDEF'				MAG01960
00017C1	197	DC X'0123',X'4567'				MAG01970
00017E1	198	DC X'89AB',X'CDEF'				MAG01980
0001801	199	DC X'0123',X'4567'				MAG01990
0001821	200	DC X'89AR',X'CDEF'				MAG02000
0001841	201	DC X'0123',X'4567'				MAG02010
0001861	202	DC X'89AB',X'CDEF'				MAG02020
0001881	203	DC X'0123',X'4567'				MAG02030
00018A1	204	DC X'89AB',X'CDEF'				
00018C1	190	BUFEND	DC	*-1		
00018E1	191	CCW	DC	H'0'		
0001901	192	BUFCOUNT	DC	H'0'	* CHANNEL COMMAND WORD	
0001941	193	BUFEND0	DC	F'0'	* BUFFER COUNT	
0001961	194		DC	H'0'	* BUFFER END ADDRESS	
0001981	195		DC	H'0'	* CHECK WORD (NOT USED IN THIS EX.)	
00019C1	196		DC	F'0'	* BUF 1 COUNT (NOT USED IN THIS EX.)	
0001A01	197		DC	H'0'	* BUF 1 END AD (NOT USED IN THIS EX)	
0001A21	198	SUBADR	DC	H'0'	* TRAN TAB ADR (NOT USED IN THIS EX)	
0001A41	199	CHCMW	DC	X'F784'	* SUBROUTINE ADDRESS	
0001A61	200	CHCMR	DC	X'F780'	CHANNEL COMMAND WORD,WRITE	
0001A81	201	MTADR	DC	X'85'	CHANNEL COMMAND WORD,READ	
0001AA1	202	DISARM	DC	X'C000'	MAG TAPE ADDRESS	
	203	RESET	DB	X'E0'	DISARM MAG TAPE	
					RESET COMMAND	

APPENDIX 3 (Continued)

32-BIT	R/W	AUTO	DRIVER	CHANNEL	
SAVE	0000	0118I	137	157	174*
SINTR	0000	00BCI	48	144*	
SUBADR	0000	01A0I	49	198*	
SWTCH1	0000	0100I	124	168*	
SWTCH2	0000	0108I	131	170*	
TERM	0000	00CO	35*	146	
TEST	0000	0010	33*	98	
WAIT	0000	0110I	143	172*	
WAIT1	0000	0088I	143*	173	
WRITE	0000	0074I	61	123*	
WRITE1	0000	007CI	125*	169	
WRT	0000	0050I	57	61*	
WRTCMD	0000	01AEI	127	207*	

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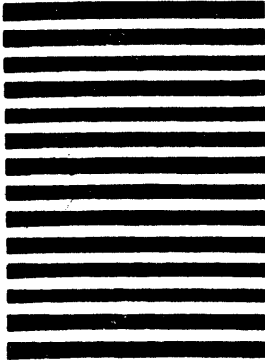


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