

NO. 21723112  
 SHEET 0  
 OF 19

# DIAGNOSTIC TEST

TITLE CORE STORAGE TEST - L 20K - DTX05A  
 MACH. TYPE 1620-1 BY HNJ APPR. \_\_\_\_\_ DATE \_\_\_\_\_

## ENGINEERING CHANGE HISTORY

E/C NO.	DATE	SHEETS AFFECTED
404980	5-7-64	1 - 19

E/C NO.	404980						
DATE	5-7-64						

## DTX05 L

Because this test destroys the contents of core, it is not DIPAL compatible.

### PURPOSE OF TEST

This diagnostic tests the standard 20 K storage for marginal conditions by generating worst case half select noise patterns. It is divided into two parts which are loaded separately as if two different diagnostics.

5H - Tests all addresses ending in 50 through 99 (over 50 sense segment)

5L - Tests all addresses ending in 00 through 49 (under 50 sense segment)

The running time of either part is 36 seconds per pass.

### SWITCH CONTROL

The Console Program Switches have the following control functions in this test:

Program Switch 1	ON - Bypass all error routines OFF - Type out errors
Program Switch 2	ON - Loop at test address OFF - Sequence through entire test
Program Switch 3	- Not Used
Program Switch 4	ON - Repeat Diagnostic OFF - Card Input: Load next program at end of pass

The PARITY Switch set in the PROGRAM position will allow the program to run without manual intervention but in the case where the error forms an illegal character it will be necessary to run with the switch in the STOP position in order to determine the failing bit.

The I/O and OVERFLOW Switches are set to the PROGRAM position.

### TEST PROCEDURE

Card Input:

1. Clear Core Storage by inserting 31 00003 00002. Release and Start.
2. Load test deck from 1622 by pushing LOAD button or by inserting 36 00000 00300 R-S.

3. Both programs are on the same tape, however they must be loaded separately. The first program on the tape is X05H, the second is X05L.

To reproduce the Paper Tape, insert 3600000 00300, release, and press "SIE" button. DO NOT reposition the tape on the Reader after the program is read in, as the second program is in position to be read. After the program has been loaded, insert

LOC			
00000	38	00024	00200
00012	35	00402	00200
00024	49	00402	00000
00036	36	00402	00300
00048	36	10002	00300
00060	49	00402	00004

After MAR has counted to 04954, stop by pressing "SCE", reset, insert

LOC			
00000	35	19999	00200
00012	35	10002	00200
00024	35	19999	00200
00036	48		

Release/start.

This is done to prevent writing 5000 zeros on tape.

When the first program, DTX05H, has been punched, clear core and insert 36 00000 00300 to read the second program, DTX05L. Release, and press "SIE" button.

After the program has been loaded, insert

LOC			
00000	38	00024	00200
00012	35	05052	00200
00024	49	05052	00000
00036	36	05052	00300
00048	36	15052	00300
00060	49	05052	00004

Release/start.

When MAR reaches 09604, stop by pressing "SCE" button, insert

LOC			
00000	35	19999	00200
00012	35	15052	00200
00024	35	19999	00200
00036	48		

Release/start.

When MAR reaches 16779, stop by pressing "SCE" button, reset, and start.

4. 1620 will Halt with 00011 in MAR after deck is loaded. Push START to execute program.

The diagnostic was assembled by the SPS Assembly Program and uses a standard SPS Load routine. A detailed listing of the loader along with a brief introduction to the SPS Assembly Program may be found in the DTX02 Diagnostic write-up.

### TEST DESCRIPTION

The pattern 7788 is written in the area to be tested and then each address in turn is complimented and then restored to its original value. Read out of the originally written character is with the half select noise in phase. Read out of the complimented character is with the half select noise out of phase. After all addresses are tested the pattern is reversed and the procedure is repeated so that every core is tested under both conditions.

In order to provide the best chance of successful test operation, the two parts of the test are confined to diagonally opposite corners of storage. No common X or Y drive line, or sense segments are used in the program area. There is no possibility of isolation in the address decode, matrix switch area.

The machine add and multiply tables are not used in this program as no arithmetic instructions have been used. The program is made up of information Transfer and Branch type instructions only.

Any reference to add tables in the listing or flow chart are to program tables, not the machine add table.

The pattern is first written throughout the tested area of storage with a Transmit Record instruction in blocks of fifty characters. If an error occurs during this phase of the program, the error routine must scan the fifty character field with a Transmit Digit instruction in order to find the specific address that failed.

The Compliment and Restore phase uses a Transmit Digit instruction, so the error address is always known. Because of the lengthy nature of this test, a rather large address modification loop is used in order to reduce the running time to a minimum. Ten addresses are completely checked each time through the loop. The pattern set up in the loop is 7788778877 (10 digits). It can be seen that it will be necessary to compliment this pattern each time through the loop in order to keep it progressing properly. In the flow chart, this is done in a clock labelled "Swap Test Digit Area".

### ERROR HALTS

### Explanation

00011	Program Loading Complete
04637	LH Pass complete and Program Switch 4 off. Waiting for card reader
09287	LL Pass complete and Program Switch 4 off. Waiting for card reader

### ERROR PRINT OUT

In the pattern writing phase, an error print out may be one or two lines. An error in an even adr only will give a one line print out. An error in an odd address will give a two line print out. One for both the even and odd address.

Even address error:

X is error char      XXXXX is error ADR (Even Address)

Odd address error:

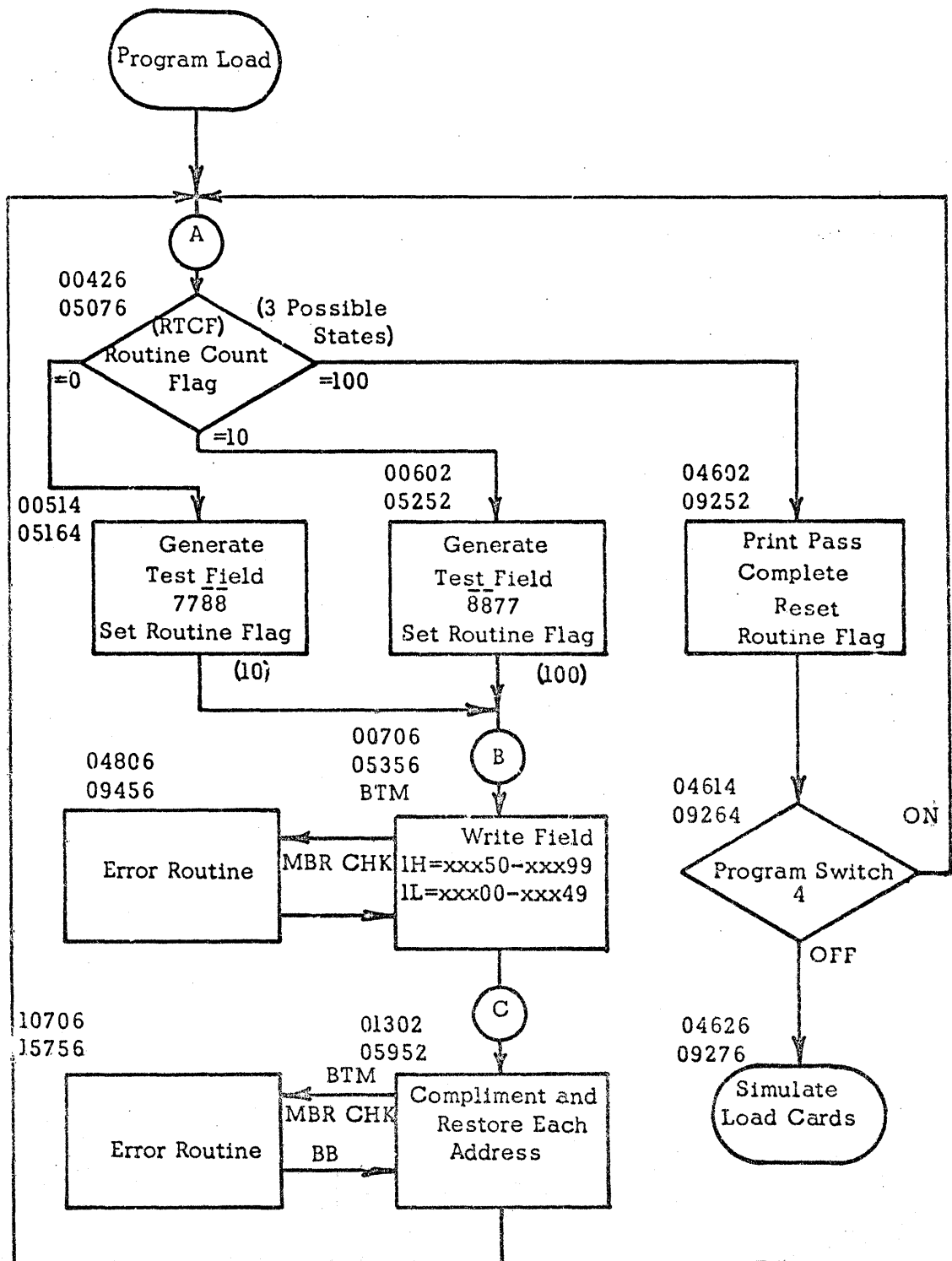
X is error char      XXXXX is error ADR (Even Address)

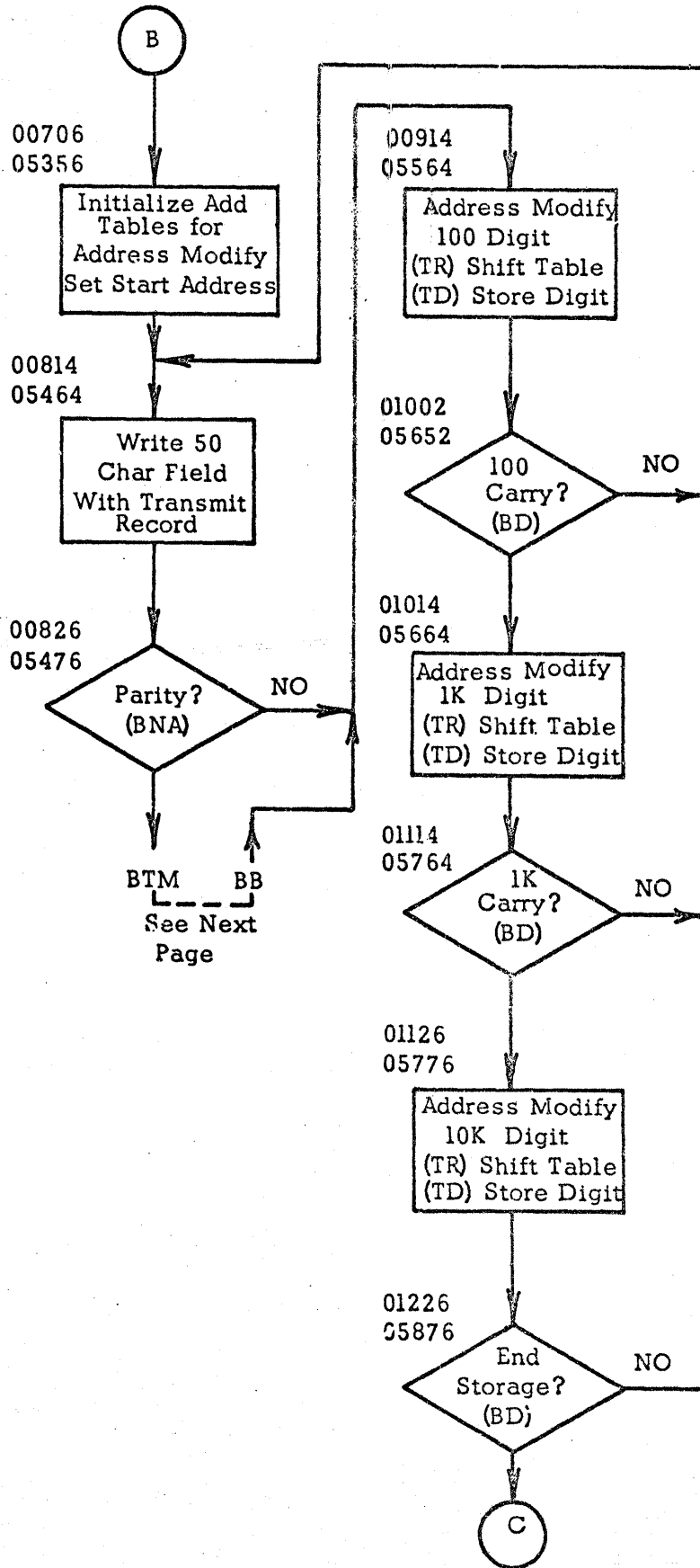
X is error char      XXXXX is error ADR (Odd Address)

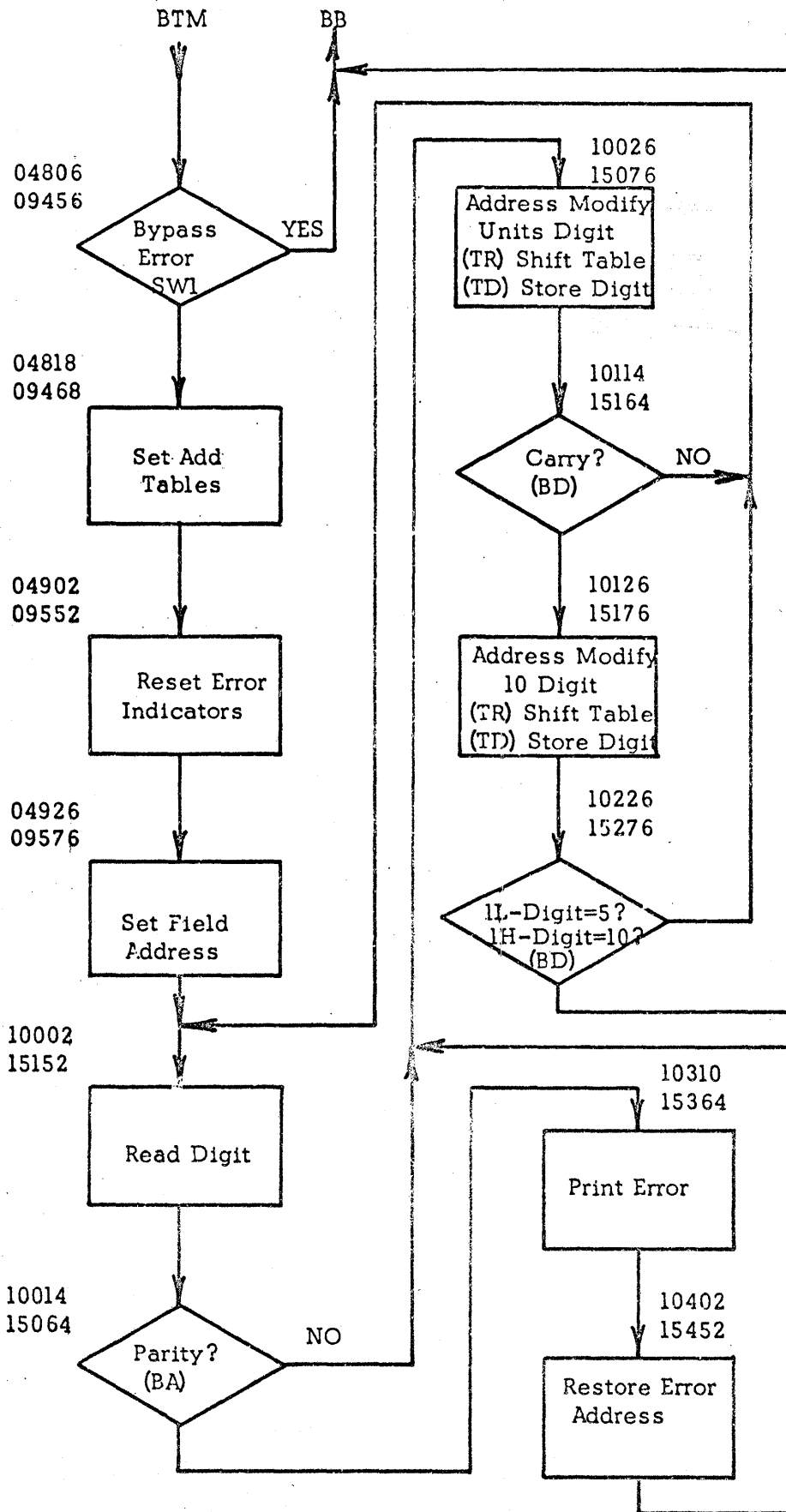
In the Compliment and Restore phase the characters from both even and odd addresses are always printed along with the even address on a single line.

XX is error char      XXXXX is error ADR (Even Address)

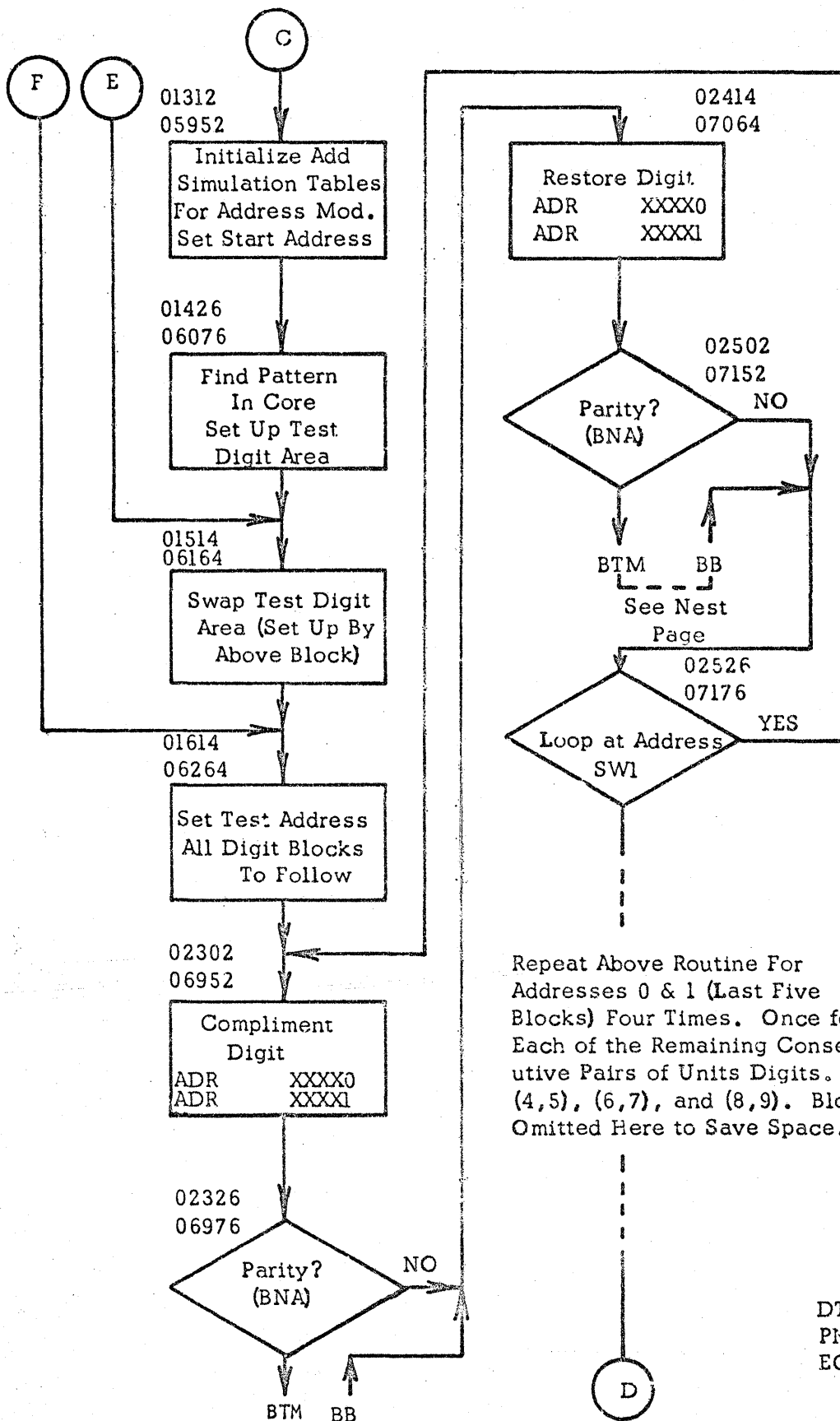
Both characters will be indicated as out of parity regardless of the failure.





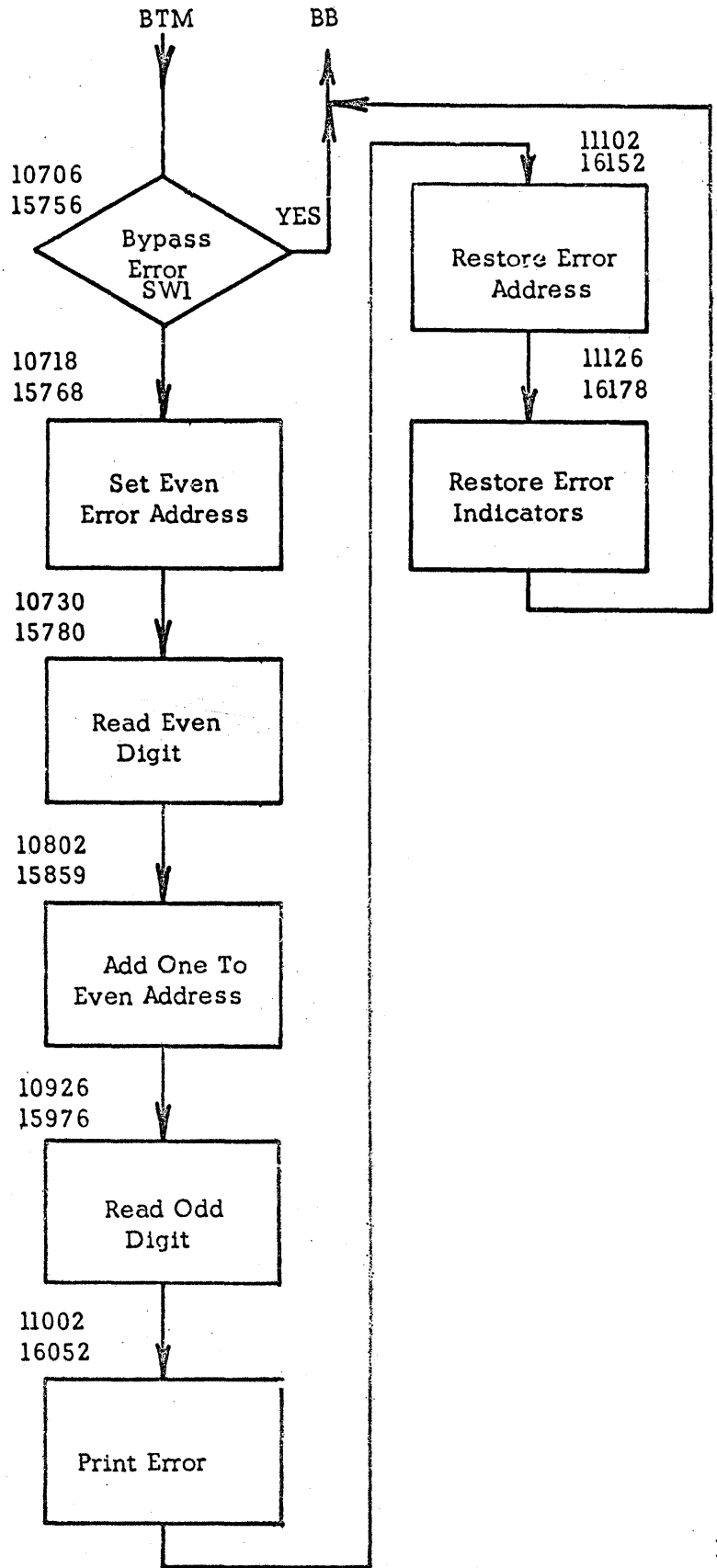


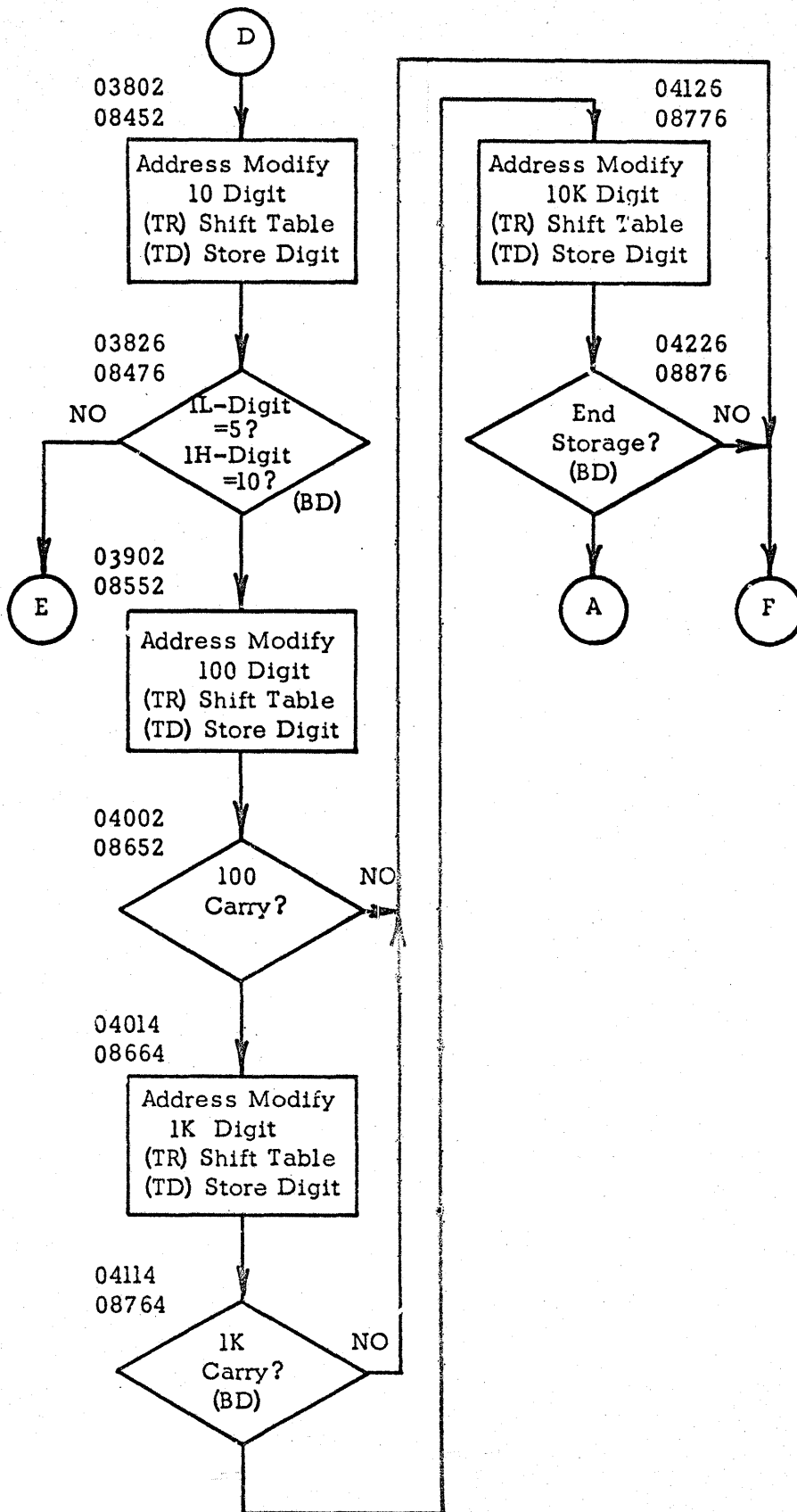




Repeat Above Routine For Addresses 0 & 1 (Last Five Blocks) Four Times. Once for Each of the Remaining Consecutive Pairs of Units Digits. (2, 3), (4, 5), (6, 7), and (8, 9). Blocks Omitted Here to Save Space.

SEE NEXT PAGE





DTX05L  
1620 MEMORY TEST 20K MACHINE

THIS FIRST PART KEEPS TRACK OF WHICH PATTERN IS  
TO BE WRITTEN NEXT AND GENERATES SAME IN LOWEST  
TESTED AREA IN STORAGE 00-49 FOR 1L 50-99 FOR 1H

05052				DORG	5052	
05052	16	05355	-0000	RCA	TFM	RTCF,,, RESET ROUTINE COUNT
05064	49	09352	00000		B	HEAD
05076	43	09252	05354	RCB	BD	EXIT,RTCF-1,, EXIT
05088	49	05152	00000		B	RCC
05152					DORG	*+53
05152	43	05252	05355	RCC	BD	RCD,RTCF,, BR PASS TWO 8877
05164	31	00000	08952		TR	,TP1,, SET UP
05176	31	00026	08954		TR	26,TP1+2,, 7788 TEST
05188	17	05356	-0001		BTM	IA,1,
05252					DORG	*+53
05252	31	00000	09052	RCD	TR	,TP2,, SET UP
05264	31	00026	09054		TR	26,TP2+2,, 8877 TEST
05276	17	05356	-0010		BTM	IA,10,
05288	41	00000	00000		NOP	
05351					DORG	*+52

WRITE PATTERN THROUGH STORAGE  
IN BLOCKS OF 50 WITH TRANSMIT RECORD

05355		00005		RTCF	DC	5,0,
05356	31	09163	08981	IA	TR	AAD,KAD,, INITIALIZE
05368	31	09188	08981		TR	BAD,KAD,, ADD
05380	31	09084	08995		TR	CAD,KCAD,, SIMULATION TABLES
05392	49	05452	00000		B	PUA
05452					DORG	*+49
05452	16	05470	-0000	PUA	TFM	WTR+6,,, SET START ADR
05464	31	00000	00000	WTR	TR	,,, WRITE FROM 00-49 INTO XXX00-XXX49
05476	47	05564	01900		BNA	AMA
05488	49	05552	00000		B	PUB
05552					DORG	*+53
05552	27	09556	05470	PUB	BT	ER,WTR+6,, ERROR

THIS ROUTINE STEPS THE TRANSMIT RECORD ADDRESS IN  
INCREMENTS OF 100

05564	31	09162	09163	AMA	TR	AAD-1,AAD,, STEP 100
05576	25	05468	09163		TD	WTR+4,AAD,, SET 100
05588	49	05652	00000		B	AMB
05652					DORG	*+53
05652	43	05464	09163	AMB	BD	WTR,AAD,, BR NO CARRY
05664	31	09163	08981		TR	AAD,KAD,, RESTORE 100
05676	31	09187	09188		TR	BAD-1,BAD,, STEP 1K
05688	49	05752	00000		B	AMC
05752					DORG	*+53
05752	25	05467	09188	AMC	TD	WTR+3,BAD,, SET 1K
05764	43	05464	09188		BD	WTR,BAD,, BR NO CARRY
05776	31	09188	08981		TR	BAD,KAD,, RESTORE 1K
05788	49	05852	00000		B	AMD
05852					DORG	*+53
05852	31	09083	09084	AMD	TR	CAD-1,CAD,, STEP 10K

05864 25 05486 09084  
05876 43 05484 09084  
05888 49 05952 00000  
05952

TD WTR+2,CAD,, SET 10K  
BD WTR,CAD,, NO BR ON END MEM  
B HKL  
DORG \*\*53

\*  
\* COMPLEMENT AND RESTORE THE PATTERN DIGIT BY DIGIT  
\*

\* FIRST SET UP STARTING ADDRESSES  
\*

05952 15 06957 00005  
05964 31 15789 06891  
05976 31 09188 08981  
05988 49 06052 00000  
06052  
06052 31 09163 08981  
06064 31 09084 08995  
06076 25 06963 00000  
06088 49 06152 00000  
06152  
06152 25 07263 00002  
06164 25 06263 06963  
06176 25 06963 07263  
06188 49 06252 00000  
06252  
06252 15 07263 00000  
06263 00000

HKL TDM WRA+5,5,, SET STARTING ADDRESS  
TR RAD,KRAD,, INITIALIZE  
TR SAD,KAD,, ADD  
B HKM  
DORG \*\*53  
HKM TR TAD,KAD,, SIMULATION  
TR UAD,KCAD,, TABLES  
TD X,,, INITIALIZE X  
B HKN  
DORG \*\*53  
HKN TD Y,2,, INITIALIZE Y  
HKP TD XS,X,, SWAP  
TD X,Y,, TEST  
B HKQ  
DORG \*\*53  
HKQ TDM Y,,, DIGITS  
XS DS ,\*

\*  
\* THE SUCCEEDING ARRAY OF TRANSMIT FIELD INSTRUCTIONS  
\* ARE USED TO UPDATE THE CURRENT ADDRESS IN THE FOLLOWING  
\* COMPLEMENT AND-RESTORE ROUTINE  
\*

06264 26 06969 06957  
06276 26 07069 06957  
06288 49 06352 00000  
06352  
06352 26 07257 06957  
06364 26 07081 06957  
06376 26 07269 06957  
06388 49 06452 00000  
06452  
06452 26 07557 06957  
06464 26 07369 06957  
06476 26 07381 06957  
06488 49 06552 00000  
06552  
06552 26 07569 06957  
06564 26 07669 06957  
06576 26 07681 06957  
06588 49 06652 00000  
06652  
06652 26 07857 06957  
06664 26 07869 06957  
06676 26 07969 06957  
06688 49 06752 00000  
06752  
06752 26 08157 06957  
06764 26 08169 06957  
06776 26 07981 06957  
06788 49 06852 00000

HKQ1 TF WRB+5,WRA+5,, SET ADR  
TF WRE+5,WRA+5,,  
B HKS  
DORG \*\*53  
HKS TF WRG+5,WRA+5,,  
TF WRF+5,WRA+5,,  
TF WRH+5,WRA+5,,  
B HKU  
DORG \*\*53  
HKU TF WRN+5,WRA+5,,  
TF WRL+5,WRA+5,,  
TF WRM+5,WRA+5,,  
B HKV  
DORG \*\*53  
HKV TF WRP+5,WRA+5,,  
TF WRS+5,WRA+5,,  
TF WRT+5,WRA+5,,  
B HKW  
DORG \*\*53  
HKW TF WRU+5,WRA+5  
TF WRV+5,WRA+5  
TF WRV+5,WRA+5,,  
B HKY  
DORG \*\*53  
HKY TF WRAA+5,WRA+5,,  
TF WRBA+5,WRA+5,,  
TF WRZ+5,WRA+5,,  
B HKZ

06852  
 06852 26 08269 06957  
 06864 26 08281 06957  
 06876 49 06952 00000  
 06886 00000  
 06888 41 00000 00000  
 06891 00007  
 06891 00000  
 06952

DORG \*\*53  
 HKZ TF WREA+5,WRA+5,,  
 TF WRFA+5,WRA+5,,  
 B WRA  
 TST DS ,\*-1  
 NOP  
 KRAD DSC 7,012340',\*-8  
 KEAD DS ,KRAD  
 DORG \*\*53

\*  
 \* NOW WE ACTUALLY COMPLEMENT AND RESTORE THE TESTED DIGIT  
 \* THE BIG LOOP IS USED TO SPEED UP THE PROGRAM WHICH  
 \* WOULD BE IMPOSSIBLY SLOW WITH A ONE ADDRESS AT A TIME LOOP  
 \*

06952 15 -0000 00000  
 06963 00000  
 06964 25 00001 06963  
 06976 47 07064 01900  
 06988 49 07052 00000  
 07052  
 07052 27 15856 06958  
 07064 25 00000 07263  
 07076 25 00001 07263  
 07088 49 07152 00000  
 07152  
 07152 47 07176 01900  
 07164 27 15856 07070  
 07176 46 06952 00200  
 07188 49 07252 00000  
 07252  
 07252 15 00002 07263  
 07263 00000  
 07264 25 00003 07263  
 07276 47 07364 01900  
 07288 49 07352 00000  
 07352  
 07352 27 15856 07258  
 07364 25 00002 06963  
 07376 25 00003 06963  
 07388 49 07452 00000  
 07452  
 07452 47 07476 01900  
 07464 27 15856 07370  
 07476 46 07252 00200  
 07488 49 07552 00000  
 07552  
 07552 25 00004 06963  
 07564 25 00005 06963  
 07576 47 07664 01900  
 07588 49 07652 00000  
 07652  
 07652 27 15856 07558  
 07664 25 00004 07263  
 07676 25 00005 07263  
 07688 49 07752 00000  
 07752  
 07752 47 07776 01900  
 07764 27 15856 07670  
 07776 46 07552 00200

WRA TDM 0,,2, WR COMP 0  
 X DS ,\*  
 WRB TD 1,X,, WR COMP 1  
 BNA WRE  
 B WRC1  
 DORG \*\*53  
 WRC1 BT ERR,WRA+6,, ERROR  
 WRE TD 0,Y,, RESTORE 0  
 WRF TD 1,Y,, RESTORE 1  
 B WRF1  
 DORG \*\*53  
 WRF1 BNA WRF2  
 BT ERR,WRE+6,, ERROR  
 WRF2 BC2 WRA,,, LOOP  
 B WRG  
 DORG \*\*53  
 WRG TDM 2,Y,, WR COMP 2  
 Y DS ,\*  
 WRH TD 3,Y,, WR COMP 3  
 BNA WRL  
 B WRH1  
 DORG \*\*53  
 WRH1 BT ERR,WRG+6,, ERROR  
 WRL TD 2,X,, RESTORE 2  
 WRM TD 3,X,, RESTORE 3  
 B WRM1  
 DORG \*\*53  
 WRM1 BNA WRM2  
 BT ERR,WRL+6,, ERROR  
 WRM2 BC2 WRG,,, LOOP  
 B WRN  
 DORG \*\*53  
 WRN TD 4,X,, WR COMP 4  
 WRP TD 5,X,, WR COMP 5  
 BNA WRS  
 B WRP1  
 DORG \*\*53  
 WRP1 BT ERR,WRN+6,, ERROR  
 WRS TD 4,Y,, RESTORE 4  
 WRT TD 5,Y,, RESTORE 5  
 B WRT1  
 DORG \*\*53  
 WRT1 BNA WRT2  
 BT ERR,WRS+6,,, ERROR  
 WRT2 BC2 WRN,,, LOOP

07788 49 07852 00000  
 07852  
 07852 25 00006 07263  
 07964 25 00007 07263  
 07876 47 07964 01900  
 07888 49 07952 00000  
 07952  
 07952 27 15856 07858  
 07964 25 00006 06963  
 07976 25 00007 06963  
 07988 49 08052 00000  
 08052  
 08052 47 08076 01900  
 08064 27 15856 07970  
 08076 46 07852 00200  
 08088 49 08152 00000  
 08152  
 08152 25 00008 06963  
 08164 25 00009 06963  
 08176 47 08264 01900  
 08188 49 08252 00000  
 08252  
 08252 27 15856 08158  
 08264 25 00008 07263  
 08276 25 00009 07263  
 08288 49 08352 00000  
 08352  
 08352 47 08376 01900  
 08364 27 15856 08270  
 08376 46 08152 00200  
 08388 49 08452 00000  
 08452

B WRU  
 DORG \*\*53  
 WRU TD 6,Y,, WR COMP 6  
 WRV TD 7,Y,, WR COMP 7  
 BNA WRY  
 B WRV1  
 DORG \*\*53  
 WRV1 BT ERR,WRU+6,, ERROR  
 WRY TD 6,X,, RESTORE 6  
 WRZ TD 7,X,, RESTORE 7  
 B WRZ1  
 DORG \*\*53  
 WRZ1 BNA WRZ2  
 BT ERR,WRY+6,, ERROR  
 WRZ2 BC2 WRU,,, LOOP  
 B WRAA  
 DORG \*\*53  
 WRAA TD 8,X,, WR COMP 8  
 WRBA TD 9,X,, WR COMP 9  
 BNA WREA  
 B WRBA1  
 DORG \*\*53  
 WRBA1 BT ERR,WRAA+6,, ERROR  
 WREA TD 8,Y,, RESTORE 8  
 WRFA TD 9,Y,, RESTORE 9  
 B WRFA1  
 DORG \*\*53  
 WRFA1 BNA WRFA2  
 BT ERR,WREA+6,, ERROR  
 WRFA2 BC2 WRAA,,, LOOP  
 B ASRA  
 DORG \*\*53

\*  
 \* THIS SECTION STEPS THE ADDRESS OF THE COMPLEMENT AND  
 \* RESTORE LOOP  
 \*

08452 31 15788 15789  
 08464 25 06957 15789  
 08476 43 06164 15789  
 08488 49 08552 00000  
 08552  
 08552 31 15789 06891  
 08564 31 09187 09188  
 08576 25 06956 09188  
 08588 49 08652 00000  
 08652  
 08652 43 06264 09188  
 08664 31 09188 08931  
 08676 31 09162 09163  
 08688 49 08752 00000  
 08752  
 08752 25 06955 09163  
 08764 43 06264 09163  
 08776 31 09163 08981  
 08788 49 08852 00000  
 08852  
 08852 31 09083 09084  
 08864 25 06954 09084  
 08876 43 06264 09084

ASRA TR RAD-1,RAD,, STEP TENS  
 TD WRA+5,RAD,, SET TENS  
 BD HKP,RAD,, BR NO CARRY  
 B ASRB  
 DORG \*\*53  
 ASRB TR RAD,KRAD,, RESTORE  
 TR SAD-1,SAD,, STEP 100  
 TD WRA+4,SAD,, SET 100  
 B ASRC  
 DORG \*\*53  
 ASRC BD HKQ1,SAD,, BR NO CARRY  
 TR SAD,KAD,, RESTORE 100  
 TR TAD-1,TAD,, STEP 1K  
 B ASRD  
 DORG \*\*53  
 ASRD TD WRA+3,TAD,, SET 1K  
 BD HKQ1,TAD,, BR NO CARRY  
 TR TAD,KAD,, RESTORE 1K  
 B ASRE  
 DORG \*\*53  
 ASRE TR UAD-1,UAD,, STEP 10K  
 TD WRA+2,UAD,, SET 10K  
 BD HKQ1,UAD,, BR NO CARRY

08888 49 05076 00000  
08952

B RCB  
DORG \*\*53

\*  
\*                   CONSTANTS AND ARITHMETIC WORKING AREAS  
\*

08952    00027  
08951  
08954    00004  
08958    00004  
08962    00004  
08966    00004  
08970    00004  
08974    00004  
08978    00004  
08981    00013  
08995    00004  
08997    00002  
09052  
09052    00027  
09052  
09052    00001  
09056    00004  
09060    00004  
09064    00004  
09068    00004  
09072    00004  
09076    00004  
09078    00002  
09163    00013  
09188    00012  
09084    00004  
09163    00000  
09188    00000  
09084    00000  
09252

TP1    DSS 27,,            FIELD OF 7788  
      DORG \*-27  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,877'  
KAD    DSC 13,012345678901',TP1+29  
KCAD   DSC 4,010',TP1+43  
      DC 2,-10,KCAD+2  
      DORG TP1+100  
TP2    DSS 27,,            FIELD OF 8877  
      DORG \*-26  
      DC 1,8  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 4,-8778  
      DC 2,8'  
AAD    DSS 13,TP2+111,    100 ADD WORK AREA  
BAD    DSS 12,AAD+25,    1K ADD WORK AREA  
CAD    DSS 4,TP2+32,    10K ADD WORK AREA  
TAD    DSS ,AAD  
SAD    DSS ,BAD  
UAD    DSS ,CAD  
      DORG AAD+89

\*  
\*                   PASS COUNTER  
\*

09252 39 09453 00100  
09264 46 05052 00400  
09276 36 00000 00500  
09288 49 00000 00000  
09352  
09352 34 00000 00102  
09364 39 09481 00100  
09376 34 00000 00102  
09388 49 05076 00000  
09452  
09453    00014  
09481    00007  
09551

EXIT   WATY PCNT,,,        PRINT PASS COMPLETE  
      BC4 RCA  
      RNCD ,,,            READ NEXT PROGRAM  
      B  
      DORG EXIT+100  
HEAD   RCTY  
      WATY HD,,,        PRINT FARSE 1  
      RCTY  
      B RCB  
      DORG HEAD+100  
PCNT   DAC 14,PASS COMPLETE',  
HD     DAC 7,DTX05L',  
      DORG PCNT+98

\*  
\*                   ERROR ROUTINE-- ERROR WHILE WRITING PATTERN  
\*

09555    00005  
09556 46 15388 00100  
09568 31 16587 08981  
09580 31 15789 06891  
09592 49 15052 00000

ERRX   DC 5,0,  
ER     BC1 BB,,,        BYPASS ERROR  
      TR DAD,KAD,,     SET ADD CONSTANTS  
      TR EAD,KEAD  
      B ERA



15052  
 15052 47 15064 51655  
 15064 47 15076 51755  
 15076 26 15163 09555  
 15088 49 15152 00000  
 15152  
 15152 25 16688 -0000  
 15164 46 15452 01900  
 15176 31 16586 16587  
 15188 49 15252 00000  
 15252  
 15252 25 15163 16587  
 15264 43 15152 16587  
 15276 31 16587 08981  
 15288 49 15352 00000  
 15352  
 15352 31 15788 15789  
 15364 25 15162 15789  
 15376 43 15152 15789  
 15388 42 00000 00000  
 15452  
 15452 26 16686 15163  
 15464 38 16688 00100  
 15476 26 15558 15163  
 15488 49 15552 00000  
 15552  
 15552 25 0-000 16688  
 15564 47 15576 50755  
 15576 47 15652 51755  
 15588 49 15652 00000  
 15652  
 15652 47 15664 51655  
 15664 39 16753 00100  
 15676 38 16682 00100  
 15688 49 15752 00000  
 15752  
 15752 39 16653 00100  
 15764 34 00000 00102  
 15776 49 15176 00000  
 15789 00007  
 15789 00000  
 15788 41 00000 00000  
 15851

15855 00065  
 15856 46 16564 00100  
 15868 26 15891 15855  
 15880 25 15973 00000  
 15892 49 15952 00000  
 15951  
 15952 25 16063 15855  
 15964 49 16052 00000  
 15973 00002  
 15975 00001  
 15976 41 00000 00000  
 15988 41 00000 00000  
 15990 00009

DORG 15052  
 ERA BNI ERA1,51655,, RESET MBR-E CHECK IND  
 ERA1 BNI ERA2,51755,, RESET MBR-O CHECK IND  
 ERA2 TF CTD+11,ERRX,, SET ERROR FIELD ADDRESS  
 B CTD  
 DORG \*\*53  
 CTD TD PRC,,7, READ DIGIT  
 BA ERD,,, ERROR  
 CTD2 TR DAD-1,DAD,, STEP UNIT ADR  
 B ERB  
 DORG \*\*53  
 ERB TD CTD+11,DAD  
 BD CTD,DAD  
 TR DAD,KAD,, RESTORE UNITS  
 B ERC  
 DORG \*\*53  
 ERC TR EAD-1,EAD,, STEP TENS  
 TD CTD+10,EAD  
 BD CTD,EAD,, BR NO CARRY  
 BB BB,,, RETURN  
 DORG \*\*53  
 ERD TF PRA+4,CTD+11,,SET ERROR ADDRESS  
 WNTY PRC,,, PRINT ERROR CHAR  
 TF ERE+6,CTD+11  
 B ERE  
 DORG \*\*53  
 ERE TD ,PRC,3, RESTORE ADR  
 BNI ERE1,50755,, RESET WRITE CHECK IND  
 ERE1 BNI ERF,51755,, RESET MBR-U CHECK IND  
 B ERF  
 DORG \*\*53  
 ERF BNI ERF1,51655,, RESET MBR-E CHECK IND  
 ERF1 WATY PRB  
 WNTY PRA,,, PRINT ERROR ADR  
 B ERG  
 DORG \*\*53  
 ERG WATY PR  
 RCTY  
 B CTD2  
 EAD DSS 7,\*\*2  
 RAD DSS ,EAD  
 NOP  
 DORG \*\*52

\*  
 \* ERROR ROUTINE - ERROR DURING COMPLIMENT OR RESTORE  
 \*  
 ERRY DC 5,0,  
 ERR BC1 RET,,, BYPASS ERROR  
 TF ERR1+11,ERRY,,SET EVEN ERROR ADDRESS  
 ERR1 TD PRJ,,, READ EVEN DIGIT  
 B ERRJ  
 DORG \*\*48  
 ERRJ TD ERRK+11,ERRY,,SET ADD TABLE ADDRESS  
 B ERRK  
 PRJ DSS 2,\*-2  
 DC 1,',\*  
 NOP  
 NOP  
 KHAD DSC 9,123456789,\*-9

```

18052
16052 25 15855 15990
16064 26 16087 15855
16076 25 15974 00000
16088 49 16152 00000
16152
16152 38 15973 00100
16164 26 16258 15891
16176 26 16270 16087
16188 49 16252 00000
16252
16252 25 00000 15973
16264 25 00000 15974
16276 47 16352 50755
16288 49 16352 00000
16352
16352 47 16364 51755
16364 47 16376 51655
16376 39 16753 00100
16388 49 16452 00000
16452
16452 26 16573 15891
16464 38 16569 00100
16476 39 16653 00100
16488 49 16552 00000
16552
16552 34 00000 00102
16564 42 00000 00000

16569 00006
16576 41 00000 00000
16587 00012
16588 41 00000 00000
16652
16653 00015
16682 00005
16687 00001
16688 00001
16689 00001
16752
16753 00014
05052

```

```

DORG **53
ERRK YD ERRY,KHAD,, READ TABLE
YF ERK1+11,ERRY,,SET ODD ERROR ADDRESS
ERK1 YD PRJ+1,,, READ ODD DIGIT
B ERRL
DORG **53
ERRL WNTY PRJ,,, PRINT ERROR DIGITS
YF ERRM+6,ERR1+11
YF ERMI+6,ERK1+11
B ERRM
DORG **53
ERRM YD ,PRJ,,, RESTORE ERROR DIGITS
ERMI YD ,PRJ+1
BNI ERRN,50755,, RESTORE CHECK IND
B ERKN,
DORG **53
ERRN BNI ERN1,51755
ERN1 BNI ERN2,51655
ERN2 WATY PRB
B ERRP
DORG **53
ERRP TF ERY1+4,ERR1+11,,SET ERROR ADDRESS FOR PRINT
WNTY ERY1,,, PRINT ERROR ADR
WATY PR
B ERRQ
DORG **53
ERRQ RCTY
RET BB ... RETURN
*
*
*
CONSTANTS AND ARITHMETIC WORKING AREAS
*
ERY1 DSC 6,*,*-6
NOP
DAD DSS 12,*
NOP
DORG ERRQ+100
PR DAC 15, IS ERROR ADR ',
PRA DSS 5
DC 1, '
PRC DSS 1
DC 1, '
DORG PR+99
PRB DAC 14, IS ERR CHAR ',
DEND 5052

```

DT X05L 80/80 LIST

360007200500360020100500440001200276260005900274250001100000260009000269	-0000
260009500264310000000200260011400274250000000011490001200000	-0001
250726300002250626306963250696307263490625200000#	0-1-6152-6200 -0002
1605355-0000490935200000430925205354490515200000#	0-1-5052-5100 -0003
4305252053553100000089523100026089541705356-0001#	0-1-5152-5200 -0004
3100000090523100026090541705356-0010410000000000#	0-1-5252-5300 -0005
-0000#	1-1-5351-5356 -0006
310916308981310918808981310908408995490545200000#	0-1-5356-5404 -0007
1605470-0000310000000000470556401900490555200000#	0-1-5452-5500 -0008
270955605470310916209163250546809163490565200000#	0-1-5552-5600 -0009
430546409163310916308981310918709188490575200000#	0-1-5652-5700 -0010
250546709188430546409188310918808981490585200000#	0-1-5752-5800 -0011
310908309084250546609084430546409084490595200000#	0-1-5852-5900 -0012
150695700005311578906891310918808981490605200000#	0-1-5952-6000 -0013
310916308981310908408995250696300000490615200000#	0-1-6052-6100 -0014
250726300002250626306963250696307263490625200000#	0-1-6152-6200 -0015
150726300000260696906957260706906957490635200000#	0-1-6252-6300 -0016
260725706957260708106957260726906957490645200000#	0-1-6352-6400 -0017
260755706957260736906957260738106957490655200000#	0-1-6452-6500 -0018
260756906957260766906957260768106957490665200000#	0-1-6552-6600 -0019
260785706957260786906957260796906957490675200000#	0-1-6652-6700 -0020
260815706957260816906957260798106957490685200000#	0-1-6752-6800 -0021
260826906957260828106957490695200000410000000000#	0-1-6852-6900 -0022
012340#	1-1-6891-6898 -0023
15-00000000250000106963470706401900490705200000#	0-1-6952-7000 -0024
271585606958250000007263250000107263490715200000#	0-1-7052-7100 -0025
470717601900271585607070460695200200490725200000#	0-1-7152-7200 -0026
150000207263250000307263470736401900490735200000#	0-1-7252-7300 -0027
271585607258250000206963250000306963490745200000#	0-1-7352-7400 -0028
470747601900271585607370460725200200490755200000#	0-1-7452-7500 -0029
250000406963250000506963470766401900490765200000#	0-1-7552-7600 -0030
27158560758250000407263250000507263490775200000#	0-1-7652-7700 -0031
470777601900271585607670460755200200490785200000#	0-1-7752-7800 -0032
250000607263250000707263470796401900490795200000#	0-1-7852-7900 -0033
271585607858250000606963250000706963490805200000#	0-1-7952-8000 -0034
470807601900271585607970460785200200490815200000#	0-1-8052-8100 -0035
250000806963250000906963470826401900490825200000#	0-1-8152-8200 -0036
271585608158250000807263250000907263490835200000#	0-1-8252-8300 -0037
470837601900271585608270460815200200490845200000#	0-1-8352-8400 -0038
311578815789250695715789430616415789490855200000#	0-1-8452-8500 -0039
311578906891310918709188250695609188490865200000#	0-1-8552-8600 -0040
430626409188310918808981310916209163490875200000#	0-1-8652-8700 -0041
250695509163430626409163310916308981490885200000#	0-1-8752-8800 -0042
310908309084250695409084430626409084490507600000#	0-1-8852-8900 -0043
Q77QQ77QQ77QQ77QQ77QQ77QQ77#	1-1-8951-8979 -0044
012345678901#	1-1-8981-8994 -0045
010#	1-1-8995-8999 -0046
J-#	1-1-8996-8998 -0047
QQ77QQ77QQ77QQ77QQ77QQ77QQ#	1-1-9052-9079 -0048
390945300100460505200400360000000500490000000000#	0-1-9252-9300 -0049
340000000102390948100100340000000102490507600000#	0-1-9352-9400 -0050
N74162620043565457534563450#	1-1-9452-9480 -0051
M463677075530#	1-1-9480-9494 -0052
-0000#	1-1-9551-9556 -0053
46153800100311658708981311578906891491505200000#	0-1-9556-9604 -0054
471506451655471507651755261516309555491515200000#	0-1J5052J5100 -0055
2516688-0000461545201900311658616587491525200000#	0-1J5152J5200 -0056
251516316587431515216587311658708981491535200000#	0-1J5252J5300 -0057

311578815789251516215789431515215789420000000000#	0-1J5352J5400 -0058
261668615163381668800100261555815163491555200000#	0-1J5452J5500 -0059
250-00016688471557650755471565251755491565200000#	0-1J5552J5600 -0060
471566451655391675300100381668200100491575200000#	0-1J5652J5700 -0061
391665300100340000000102491517600000410000000000#	0-1J5752J5800 -0062
-0000#	1-1J5851J5856 -0063
461656400100261589115855251597300000491595200000#	0-1J5856J5904 -0064
251606315855491605200000#	0-1J5952J5976 -0065
#	1-1J5975J5976 -0066
410000000000410000000000#	0-1J5976J6000 -0067
123456789#	1-1J5990J5999 -0068
251585515990261608715855251577400000491615200000#	0-1J6052J6100 -0069
381597300100261625815891261627016087491625200000#	0-1J6152J6200 -0070
250000015973250000015974471635250755491635200000#	0-1J6252J6300 -0071
471636451755471637651655391675300100491645200000#	0-1J6352J6400 -0072
261657315891381656900100391665300100491655200000#	0-1J6452J6500 -0073
340000000102420000000000#	0-1J6552J6576 -0074
00000#	1-1J6569J6575 -0075
410000000000410000000000#	0-1J6576J6600 -0076
-0496200455959565900414459000#	1-1J6652J6682 -0077
#	1-1J6687J6688 -0078
#	1-1J6689J6690 -0079
-04962004559590043484159000#	1-1J6752J6780 -0080
00000 L600000005004900000#	-8-0096-0115 -0081
3600100005003600172005003600244005003600316005003600000000500	-0082
000000000000102030400020406080003060902100408021610050015102006021814200#	-0083
704112820080614223009081726300000000005060708090012141618151811242720242#	-0084
822363520353045403632484455324946536048465462754453627180123456789123456#	-0085
789-23456789-J3456789-JK456789-JKL56789-JKLM6789-JKLMN789-JKLMNO89-JKLMN#	-0086
M8000000000049-50520P9-JKLMNOPQ# L10038800019M90000000000GM9000360000	-0087

DT X05H 80/80 LIST