

IDENTIFICATION

Product Code: MAINDEC-08-D1EB-D  
Product Name: PDP-8, 8/I Extended Memory Checkerboard  
Date Created: May 1, 1968  
Maintainer: Diagnostics Group  
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1. ABSTRACT

The PDP-8, 8/I Extended Memory Checkerboard diagnostic is designed to provide worst case half-select noise conditions in order to determine the operational status of core memory. Four data patterns, and their complements, are written and checked for error. The patterns provided will generate the worst case noise conditions for a PDP-8 or 8/I equipped with standard or specially purchased core stacks, and will test systems equipped with from 8K to 32K words of core memory. Automatic program relocation is provided in order to test all memory stacks from each stack.

Teletype print-outs are provided for error identification. Also, the operator is given a degree of control over the program by various SR settings. These are explained in detail in Section 8.2.

2. REQUIREMENTS

2.1 Equipment

A standard PDP-8 or 8/I equipped with at least 8K words of core memory.

2.2 Storage

The program occupies locations 0010 to 3334.

2.3 Preliminary Programs

The Binary Loader must be in memory. Also, all diagnostics for a basic 4K PDP-8 must have been previously run successfully.

3. LOADING PROCEDURE

- a. Turn off the Teletype reader.
- b. Set the SR to 7777.
- c. Press LOAD ADDRESS; then START.
- d. Place the Binary tape in Teletype reader and turn on the reader.
- e. When the program has been loaded, stop the computer, turn off the reader, and remove the tape.

#### 4. STARTING PROCEDURE

##### 4.1 Starting Address

Start from address 200 to specify the amount of core memory to test; SR settings, and to receive a header print-out.

##### 4.2 Restarting Address

Start from address 207 to change the test limits; SR settings, and to inhibit the header print-out.

##### 4.3 Operator Action

Immediately after starting from address 200 or 207, the program will print TEST LIMITS. The operator must then specify, via the Teletype keyboard, the amount of core memory to test, followed by a carriage return.

The following rules govern the amount of memory to test:

- a. Type two octal numbers, separating the numbers with a comma. The first number signifies the lowest order 4K stack to test; the second signifies the highest order.
- b. The program expects the 4K stacks to be numbered sequentially starting with a stack 0.
- c. If the highest order stack to test is typed as the first stack, the program will interchange the two values so as to make the second value the first to test.
- d. After typing the second octal number, press the carriage return key to terminate the line.
- e. The program will test the lowest and highest order 4K stack specified, plus every stack between, starting with the lowest specified.
- f. Any single stack, or two or more sequential stacks may be specified.
- g. The stack containing the program may be included when specifying two or more stacks.

The stack containing the program will be tested after automatic program relocation takes place (see Section 5.3.1).

h. If a typing error is made, press the RUB-OUT key. TEST LIMITS will be printed again. All previous input is disregarded.

For the following examples assume the program to be located in stack 0, and the program has been started from address 200 or 207. The amount of core memory available is 32K.

Example A:

TEST LIMITS

0,7↵ (↵ denotes carriage return)

Example A indicates stacks 0, 1, 2, 3, 4, 5, 6 and 7 will be tested.

Example B:

TEST LIMITS

7,0

The program will perform exactly as Example A.

Example C:

TEST LIMITS

4,5

Only stacks 4 and 5 will be tested.

Example D:

TEST LIMITS

3,3

Stack 3 alone will be tested.

Example E:

TEST LIMITS

0,0 PROGRAM IS LOCATED IN FIELD 0

TEST LIMITS

0,1

Example E shows the message printed by the program when a single stack is selected which currently contains the program. TEST LIMITS is printed again, and the operator must then correct the test limits.

Operation of the program is unpredictable if the amount of memory selected for testing exceeds the actual amount available, i.e., selecting 32K for testing on a PDP-8 or 8/I equipped with a maximum of 28K.

#### 4.3.1 Setup SR

After the test limit is specified, the program will print SETUP SR. For normal program operation, the SR must be set to equal 0000<sub>8</sub>. Press the carriage return key after setting the SR to 0000. The program will then run until stopped by the operator. Normal program operation is defined as performing all four checkerboard patterns on all of available memory from every memory stack.

## 5. OPERATING PROCEDURE

### 5.1 Program and Operator Action

- a. Load the program into stack 0 using the procedure described in Section 3.
- b. Set the SR to 200; press LOAD ADDRESS, and then start.
- c. The message TEST LIMITS will be printed. Specify the limits, via keyboard, as described in Section 4.3.
- d. The message SETUP SR will be printed. Set the SR to 0000<sub>8</sub>, and press the carriage return key.
- e. The program will perform all four tests on all of core memory specified, after which, automatic program relocation takes place.

### 5.2 Operational Switch Settings

Normal operation of the program requires the SR set to 0000<sub>8</sub>. Refer to Section 8.2, applications, for switch settings provided for trouble-shooting.

### 5.3 Subroutine Abstracts

#### 5.3.1 Program Relocation

Program relocation is governed entirely by the amount of core memory selected for testing. Under certain conditions the program will not relocate at all, but will remain in the current 4K stack to perform the tests (see below). The program first relocates to the highest order 4K stack under test. From there it relocates to the next lower stack (after performing all four tests). The program keeps relocating to the next lower stack until it reaches the lowest order stack under test. The testing and relocation cycle is then repeated.

The contents of the entire 4K stack are relocated. This enables the RIM Loader, and any other information to be carried with the program.

The program provides a degree of protection for itself by recording the first error encountered in any stack. When a faulty stack is next in sequence to contain the program, the program will skip the faulty stack and relocate to the first lower order stack which is error-free. If all lower order stacks are faulty, program relocation will not take place. The tests will be run again from the current stack. Relocation will resume when an error-free stack is found.

Also, the program will not relocate if any of the conditions described below exist.

- a. Only one 4K stack is selected for testing.
- b. SR 9 is on a 1 to inhibit relocation (see Section 8.2.6).

The INSTRUCTION FIELD indicators will indicate the current stack containing the program.

### 5.3.2 The Checkerboard Patterns

Four test patterns, and their complements, are used to test memory. All memory stacks, except the one with the program, are tested with one pattern before the next test is executed.

Any one, or any combination, of the four tests may be run by placing one, or any combination, of SR 3, 4, 5, or 6 on a 1 after the message SETUP SR is printed. The test specified by the most significant switch on a 1 will be executed first. SR 3, 4, 5 and 6 all on a 0 will enable all tests to be run. SR 3= test 1; 4= test 2; 5= test 3; 6= test 4.

The following steps are performed by each of the four tests:

- a. Write the pattern once in all stacks selected for testing; starting with the lowest order stack.
- b. Select the lowest order stack and perform a read, complement data, write sequence once on each location, until all 4K has been complemented.

- c. Repeat step b 31 more times. The stack will end up with the pattern originally loaded.

No error checking has been performed as yet.

- d. Read 4-word segments and complement each segment 4 times; then read each of the 4 words and check for error.

- e. After checking the entire 4K stack for errors, repeat step d again. This time stall for a random period of time after reading and checking every 400<sub>8</sub> word block. The maximum stall is 18.4 ms; the minimum is 3 μs.

- f. Setup for the next sequential 4K stack and repeat steps b through f.

When all selected stacks have been checked the next test in sequence is executed, and steps a through f repeated. Program relocation takes place after the fourth test is executed in this manner.

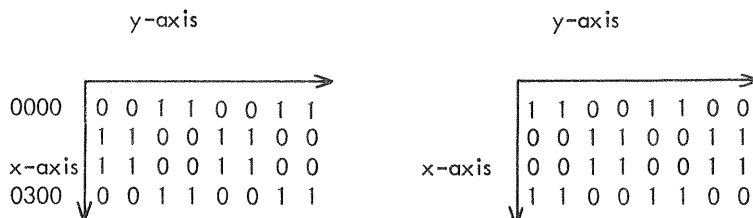
The patterns generated by each test are shown below. The matrices represent portions of one bit plane.

Test 1:

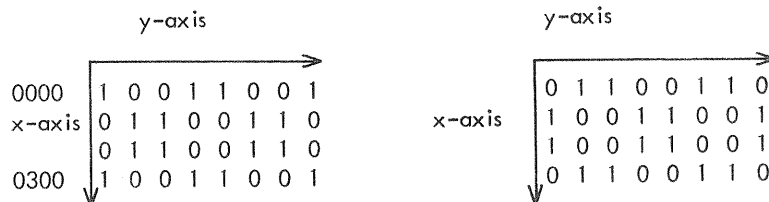
		y-axis	
		→	
0000	0	0	1
x-axis	0	0	1
	1	1	0
0300	1	1	0
	↓	0	0

		y-axis	
		→	
	1	1	0
x-axis	1	1	0
	0	0	1
	0	0	1
	↓	1	1

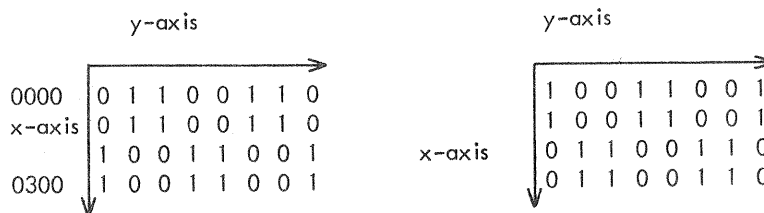
Test 2:



Test 3:



Test 4:



6. ERRORS

Starting the program from address 200 will give a header print-out after the SR has been setup. The header identifies the information printed when a data error is found. The header appears as:

	FIELD	OCTAL ADR.	GOOD	BAD	TEST
Where:	FIELD	= an octal number (0 to 7) indicating the 4K field containing the error.			
	OCTAL ADR.	= the memory address which contains the incorrect data.			
	GOOD	= what the data in octal, should have been. This will always equal 0000 or 7777.			
	BAD	= the data as read. This will equal the good data except for one or more bits complemented.			
	TEST	= the number (1 to 4) of the test which detected the error.			

After each error print-out the program continues on with the next sequential memory location.



6.1 Error Halts and Description

Placing SR 0 on a 1 during an error print-out will cause a halt at location 2641.  
Press CONTINUE to resume testing.

7. RESTRICTIONS

7.1 Starting Restrictions

Start from address 200 to indicate the amount of core memory to test; to setup the SR and to receive a header print-out.

Starting from 207 requires the same operator action, but no header will be printed.

7.2 Operating Restrictions

None

8. MISCELLANEOUS

8.1 Execution Time

The time required to perform all four tests on one 4K memory stack is approximately 26 seconds.

8.2 Applications

For operating convenience, and as an aid to trouble-shooting, the SR may be used to control the program. The switch assignments and their effect on the program are described below. Please note that it is important that the program should be halted before changing the test selection switches. These switches are not sensed by the program during testing.

Halting the program with SR 0 is preferred, rather than with the STOP key. Using the STOP key may result in a halt while the program is in the process of relocating, which is disastrous.

8.2.1 Halt after Test or Error - SR 0

Placing SR 0 on a 1 at any time while the program is running will cause a halt after the current test is completed. The MB will equal 2461 in the current stack containing the program. Press CONTINUE to resume testing, or restart from 200 or 207 to enter new parameters.

Placing SR 0 on a 1 during an error type-out will also cause a halt at location 2461. Proceed exactly as described in the above paragraph.

#### 8.2.2 Inhibit Error Print-out - SR 1

Placing SR 1 on a 1 causes all error print-outs to be inhibited. All other messages will not be inhibited. The program will continue to recognize errors, but will not print any information. SR 1 may be placed on a 1 or 0 while the program is running.

#### 8.2.3 Bell on Error - SR 2

SR 2 on a 1 causes the program to ring the TTY BELL whenever an error is detected. This is convenient when testing with power supply margins. SR 2 has precedence over SR 1 if both should happen to be on a 1. SR 2 may be placed on a 1 or 0 while the program is running.

#### 8.2.4 Test Selection SR 3 through 6

Any one, or any combination of tests may be executed by placing any one or any combination of SR 3 through 6 on a 1. Test selections may be made only when starting from 200 or 207. SR 3 specifies test 1; SR 4 test 2; SR 5 test 3; SR 6 test 4. The test specified by the most significant SR on a 1 will be executed first.

For most PDP-8s, SR 4 will provide the worst case pattern. For most PDP-8/Is, SR 5 will provide the worst case pattern.

If all four switches are on a 0, all four tests will be executed in order starting with test 1. Program relocation is not effected, regardless of the SR settings.

#### 8.2.5 Inhibit Program Relocation - SR 7

The program normally relocates automatically as indicated by the INSTRUCTION FIELD indicators. To retain the program in its current 4K field, place SR 7 on a 1 at any time. Changing SR 7 to a 0 will permit relocation to resume.

#### 8.2.6 SR 8, 9 and 10 - Not Used

#### 8.2.7 Change TEST LIMITS and SR - SR 11

Placing SR 11 on a 1 will cause the program to automatically restart from address 207. The TEST LIMITS and SR may then be changed. SR 11 is sensed only after all specified tests have been completed on all of memory under test.

### 8.2.8 Loop on Address

A subroutine is provided which may be used to continuously loop on a single location, or a group of consecutive locations. No error checking is performed. The routine performs a read, and immediately follows with a write, on each location. The loop time between two reads, or two writes, is approximately 22.5  $\mu$ s.

#### Operating Procedure:

- a. Set the INSTRUCTION FIELD switches to the current field, and the SR to 1700.
- b. Set the DATA FIELD switches to equal the 4K field number to test.
- c. Press LOAD ADDRESS.
- d. Set the SR to equal the first address of the group.
- e. Press START. A halt will occur at 1703. Set the SR to equal the last address of the group.
- f. Press CONTINUE. The address(s) specified will be looped until stopped by the operator with STOP. SR 0 will not halt this routine.

To resume normal operation, restart the program from 200 or 207 of the current field.

## 9. PROGRAM DESCRIPTION

The PDP-8, 8/I Extended Memory Checkerboard diagnostic is designed to create worst case memory noise conditions on systems equipped with 8K to 32K words of memory. The program executes four checkerboard patterns, plus their complements, on each 4K memory field. In addition, the program automatically relocates from field to field in order to test all 4K fields from every 4K field. Under normal operation, the amount of core memory tested at one time is that specified by the operator minus the 4K field containing the program. A TTY keyboard input routine is provided to enable the operator to specify the exact number of 4K fields to be tested. A print-out is provided for each error detected by the program.

Further control of the program is given to the operator by means of the SR. The operator may halt the program, inhibit error print-outs, substitute the TTY BELL for error indication, halt after error print-out, select any one or a group of tests, inhibit program relocation, and create an automatic restart to change the amount of memory to test.

A small subroutine is provided which will continuously read and write any single, or a group of locations within any 4K field. The operator must specify the locations by means of the SR.





0054	0263	K263,	263
0055	0264	K264,	264
0056	7760	M20,	7760

0057	7740	M40,	7740
0060	7774	M4,	7774
0061	7773	M5,	7773
0062	0000	TADM,	0
0063	1607	XBRNK,	CBANK
0064	0652	X011,	X011
0065	0667	X1100,	X1100
0066	0724	X0110,	X0110
0067	0721	X1001,	X1001
0070	1600	XBRNK,	CKBNK
0071	1624	XBRNK,	NXTBNK
0072	0000	COUNT,	2
0073	0000	FLECNT,	2
0074	0000	LOOP,	0
0075	0736	XRALL,	RDALL
0076	1037	XCHK1,	RCHK1
0077	1054	XCHK10,	RCHK10
0100	1071	XCHK2,	RCHK2
0101	1106	XCHK20,	RCHK20
0102	1123	XCHK3,	RCHK3
0103	1140	XCHK30,	RCHK30
0104	1220	XCHK4,	RCHK4
0105	1217	XCHK40,	RCHK40
0106	1056	TDM20,	TAD M20
0107	1057	TDM40,	TAD M40
0110	4515	JMS1,	JMS I XR01
0111	4516	JMS2,	JMS I XR02
0112	4517	JMS3,	JMS I XR03
0113	4520	JMS4,	JMS I XR04
0114	4552	JMS5,	JMS I XSALL
0115	1245	XR01,	RD1
0116	1322	XR02,	RD2
0117	1400	XR03,	RD3
0120	1455	XR04,	RD4
0121	2000	XRROR,	ERROR
0122	0000	MEMADR,	0
0123	0000	FIRST1,	0
0124	0000	LAST1,	0
0125	6201	KCIF,	6201
0126	6202	KCIF,	6202
0127	2641	XHLT,	HALT
0130	0213	XRTN,	RTN1
0131	1646	XFILD,	FIELD
0132	2146	XPRER,	PRERR
0133	0007	K7,	7
0134	0000	CHAR,	0
0135	2474	XMR,	PHDR
0136	2146	XPERR,	PRERR
0137	2115	XPING,	SPING

M14, 7764  
 M10, 7770  
 K260, 260  
 K215, 215  
 K377, 377  
 K370, 370  
 K277, 277  
 XCHLF, CRLF  
 K1, 1  
 NXLOC, 2  
 XSALL, STALL  
 EXIT, 0  
 LASTX, LAST

0143 7764  
 0141 7770  
 0142 0260  
 0143 0215  
 0144 0377  
 0145 0370  
 0146 0277  
 0147 2154  
 0150 0001  
 0151 0000  
 0152 2702  
 0153 0000  
 0154 2166



0200	0200	*200	
0201	0201	REGIN,	
0202	0202	IOF	/PI OFF
0203	0203	CLA	
0204	0204	DCA FLAGS	/CLEAR PROGRAM FLAGS
0205	0205	DCA INSFLD	
0206	0206	JMS I XLMTS	/SETUP TEST LIMITS
0207	0207	JMS I XTSTP	/SETUP SR
0210	0210	JMS I XHDR	/PRINT HEADER
0211	0211	JMP RTN1	
0212	0212	/	
0213	0213	/RESTART HERE	
0214	0214	/	
0215	0215	RSTR11,	JMS I XLMTS
0216	0216	JMS I XTSTR	/SET TEST LIMITS
0217	0217	RIF	/SETUP SR
0218	0218	DCA INSFLD	/READ INSTRUCTION FIELD
0219	0219	JMS I XFILD	/CURRENT FIELD
0220	0220	TAD M10	
0221	0221	DCA LOOP	/-10
0222	0222	7600	
0223	0223	TAD ALAW	
0224	0224	ISZ ERWRD	
0225	0225	DCA I ERWRD	
0226	0226	ISZ LOOP	
0227	0227	JMP ALAW	
0228	0228	TAD ALAW	
0229	0229	DCA I LASTX	
0230	0230	TAD ERTBL	
0231	0231	DCA ERWRD	
0232	0232	/EXAMINE SR	
0233	0233	TAD MCWA	
0234	0234	AND K740	/MADK 3,4,5 AND 6
0235	0235	SZA	/DO ALL IF 0
0236	0236	JMP EXAM1	
0237	0237	TAD MCWA	
0238	0238	TAD K740	/SET ALL TEST BITS
0239	0239	DCA MCWA	/SAVE
0240	0240	CLA	
0241	0241	TAD MCWA	
0242	0242	AND K400	
0243	0243	SZA	/TEST 1 IF NO SKIP
0244	0244	JMP I XTST1	
0245	0245	CLA	
0246	0246	TAD MCWA	
0247	0247	AND K200	
0248	0248	SZA	/TEST 2 IF NO SKIP
0249	0249	JMP I XTST2	
0250	0250	CLA	
0251	0251	TAD MCWA	
0252	0252	AND K100	
0253	0253	SZA	/TEST 3 IF NO SKIP
0254	0254	JMP I XTST3	
0255	0255	CLA	
0256	0256	TAD MCWA	
0257	0257	AND K100	
0258	0258	SZA	
0259	0259	JMP I XTST3	

0260 7200  
0261 1033  
0262 0241  
0263 7440  
0264 5447  
0265 4531

EXAM4.

CLA  
TAD MCWA  
AND K40  
SZA /TEST 4 IF NO SKIP  
JMP I XTST4 /RESTORE DATA FIELD  
JMS I XFILD

0266 7604  
 0267 8150  
 0270 7440  
 0271 5211  
 0272 7624  
 0273 0742  
 0274 7440  
 0275 5213  
 0276 5450  
 0277 2645

LAS  
 AND K1  
 SZA RSTRT1  
 JMP RSTRT1  
 LAS  
 AND K2  
 SZA  
 JMP RTN1  
 JMP I XMOVE  
 / XSTSR, SETSR

/CHECK SR 11

/INHIBIT MOVE IF A 1  
/GO RELOCATE

```

/TEST 1, WRITE CHECKER PATTERN #1,
/
TST1, JMS I XSETU /SET DF TO 1ST FIELD
CLA TAD K261 /TEST NUMBER
DCA TNUM
CMA
DCA 10 /SFT ADDRESS COUNT TO 7777
JMS I XBANK /SEL IF FIELD HAS PROGRAM
SKP EXT1 /NO, BEGIN WRITING
JMP EXT1 /DONE ALL, NOW READ ALL
TAD KXT1
DCA EXIT
JMS I X0011 /WRITE 0011
JMS I X0011 /WRITE 0011 64 TIMES
JMS I X1100 /WRITE 1100 128 TIMES
JMS I X1100
JMP :-4 /KEEP WRITING
JMS I XTRNK /SETUP FOR NEXT FIELD
JMP TST1+4
XIT1,
/
EXT1, JMS I XCHK1 /READ EACH FIELD AND CHECK
/FOR ERRORS,
JMP TST1C /NOW WRITE COMPLEMENT
/
/WRITE COMPLEMENT OF PATTERN 1
/
TST1C, JMS I XSETU /SEE DF TO 1ST FIELD,
CLA CMA /SET ADDRESS COUNT TO 7777
DCA 10 /SEE IF FIELD HAS PROGRAM
JMS I XBANK
SKP /ALL DONE, READ ALL
JMP EXT1C
TAD KXT1C
DCA EXIT
JMS I X1100 /WRITE 1100
JMS I X1100 /WRITE 1100 16 TIMES
JMS I X0011 /WRITE 0011 128 TIMES
JMS I X0011 /KEEP WRITING
JMP :-4 /SETUP FOR NEXT FIELD
JMS I XTRNK
JMP TST1C+1
XIT1C,
/
EXT1C, JMS I XCHK1C /READ EACH BANK AND CHECK
/FOR ERRORS,
JMP EXAM2 /SEE IF TEST 2 IS SELECTED
/
KXT1, XIT1
KXT1C, XIT1C

```

0320 4451  
0321 7200  
0322 1052  
0323 3062  
0324 7040  
0325 3010  
0326 4463  
0327 7410  
0328 5322  
0329 1345  
0330 3153  
0331 4464  
0332 4464  
0333 4465  
0334 4465  
0335 5313  
0336 4471  
0337 5304

0322 4476  
0323 5324

0324 4451  
0325 7240  
0326 3010  
0327 4463  
0328 7410  
0329 5343  
0330 1346  
0331 3153  
0332 4465  
0333 4465  
0334 4464  
0335 5334  
0336 4471  
0337 5325

0343 4477  
0344 5246  
0345 0320  
0346 0341

/TEST 2, WRITE CHECKER PATTERN #2

/ \*400  
/ TST2,

JMS I XSETU	/SET DF FOR 1ST FIELD
CLA	
TAD K262	
DCA TNUM	/TEST #
CLA CMA	
DCA 10	/SET ADDRESS COUNT TO 7777
JMS I XBANK	/SEE IF FIELD HAS PROGRAM
SKP EXT2	/NO. BEGIN WRITING
JMP EXT2	/DONE ALL, NOW READ ALL
TAD KXT2	
DCA EXIT	
JMS I X0011	/WRITE 0011
JMS I X1100	/WRITE 1100 128 TIMES
JMS I X1100	
JMS I X0011	/WRITE 0011 128 TIMES
JMS I X0011	
JMP I=4	
JMS I XTBNK	/SETUP FOR NEXT FIELD
JMP TST2+4	

XIT2,

0420	0400
0451	0401
7200	0402
1053	0403
3062	0404
7240	0405
3010	0406
4463	0407
7410	0410
5223	0411
1250	0412
3153	0413
4464	0414
4465	0415
4465	0416
4464	0417
4464	0420
5214	0421
4471	0422
5204	

EXT2, JMS I XCHK? /READ EACH FIELD AND CHECK  
 JMP TST2C /NOW WRITE COMPLEMENT

/WRITE COMPLEMENT OF PATTERN 2

TST2C, JMS I XSETU /SET DF FOR FIRST FIELD

CLA CMA  
 DCA 10 /SET ADR. COUNT TO 7777  
 JMS I XBANK /SEE IF FIELD HAS PROGRAM  
 SKP /WRITE  
 JMP EXT2C /GO READ  
 TAD KXT2C  
 DCA EXIT

JMS I X1100 /WRITE 1100  
 JMS I X0011 /WRITE 0011 128 TIMES

JMS I X1100 /WRITE 1100 128 TIMES

XIT2C, JMP :-4 /SETUP FOR NEXT FIELD  
 JMS I XTBNK  
 JMP TST2C+1

/EXT2C, JMS I XCHK2C /READ EACH FIELD AND CHECK  
 JMP I ,\*1 /SEE IF TEST 3 IS SELECTED  
 EXAMS

/KXT2, XIT2  
 KXT2C, XIT2C

0423 4500  
 0424 5225

0425 4451  
 0426 7240  
 0427 3010  
 0430 4463  
 0431 7410  
 0432 5245  
 0433 1251  
 0434 3153  
 0435 4465  
 0436 4464  
 0437 4464  
 0440 4465  
 0441 4465  
 0442 5236  
 0443 4471  
 0444 5226

0445 4501  
 0446 5647  
 0447 0253

0450 0421  
 0451 0443

```

/TEST 3. WRITE CHECKER PATTERN #3
/
TST3, JMS I XSETU /SETUP FOR 1ST FIELD
CLA TAD K26J
DCA TNUM /TEST NUMBER
CLA CMA
DCA 10 /SET ADR. COUNT TO 7777
JMS I XBANK /SEE IF FIELD HAS PROGRAM
SKP /GO WRITE
JMP EXT3 /GO READ
TAD KXT3
DCA EXIT
JMS I X1001 /WRITE 1001
JMS I X0110 /WRITE 0110 128 TIMES
JMS I X0110
JMS I X1001 /WRITE 1001 128 TIMES
JMS I X1001
JMP ,--4
JMS I X1001
JMP TST3+4
XIT3, /SETUP FOR NEXT FIELD

/EXT3, /READ EACH FIELD AND CHECK
JMP TST3C /WRITE COMPLEMENT

/WRITE COMPLEMENT OF PATTERN 3
/
TST3C, JMS I XSETU /SETUP DF FOR 1ST FIELD
CLA CMA
DCA 10 /SET ADR. COUNT TO 7777
JMS I XBANK /SEE IF FIELD HAS PROGRAM
SKP /WRITE
JMP EXT3C /READ ALL
TAD KXT3C
DCA EXIT
JMS I X0110 /WRITE 0110
JMS I X1001 /WRITE 1001 128 TIMES
JMS I X1001
JMS I X0110 /WRITE 0110 128 TIMES
JMS I X0110
JMP ,--4
JMS I X1001
JMP TST3C+1
XIT3C, /SETUP FOR NEXT FIELD

/EXT3C, /READ EACH FIELD AND CHECK
JMP I ,+1 /SEE IF TEST 4 IS SELECTED
EXAM4

/KXT3, XIT3
KXT3C, XIT3C

```

```

0451 4451
0452 7240
0453 1054
0454 3062
0455 7240
0456 3010
0457 4463
0458 7410
0459 5275
0460 1322
0461 3153
0462 4467
0463 4466
0464 4466
0465 4467
0466 4467
0467 4467
0468 4467
0469 5266
0470 4471
0471 4471
0472 5256
0473 4502
0474 5277

0477 4451
0478 7240
0479 3010
0480 4463
0481 7410
0482 5317
0483 1323
0484 3153
0485 4466
0486 4467
0487 4467
0488 4466
0489 5310
0490 4471
0491 5300

0517 4503
0518 5721
0519 0260

0522 0473
0523 0515

```

```

/TFST 4, WRITE PATTERN #4
/
*610
/
TST4, JMS I XSETU /SET DF FOR 1ST FIELD
CLA
TAD K264 /TEST NUMBER
DCA TVUM
CLA CMA
DCA 10 /SET ADR, COUNT TO 7777
JMS I XBANK /SEE IF FIELD HAS PROGRAM
SKP EXT4 /WRITE
JMP EXT4 /GO READ
TAD KXT4
DCA EXIT /WRITE 0110
JMS I X0110 /WRITE 0110 64 TIMES
JMS I X0110 /WRITE 0110 64 TIMES
JMS I X1001 /WRITE 1001 128 TIMES
JMS I X1001
JMS I X0110
JMP *-4
JMS I XTBNK /SETUP FOR NEXT FIELD
JMP TST4+4

XIT4,

/
EXT4, JMS I XCHK4 /READ EACH FIELD AND CHECK
JMP TST4C /WRITE COMPLEMENT

/
WRITE COMPLEMENT OF PATTERN 4
/
TST4C, JMS I XSETU /SET DF FOR FIRST
CLA CMA /SET ADR, COUNT TO 7777
DCA 10 /SEE IF FIELD HAS PROGRAM
JMS I XBANK /WRITE
SKP EXT4 /READ
JMP EXT4C
TAD KXT4C
DCA EXIT /WRITE 1001 64 TIMES
JMS I X1001 /WRITE X0110 128 TIMES
JMS I X1001
JMS I X0110
JMS I X0110
JMS I X1001
JMP *-4
JMS I XTBNK /SETUP FOR NEXT FIELD
JMP TST4C+1

XIT4C,

/
KXT4, XIT4
KXT4C, XIT4C

```

0620

0621 4451  
0622 7240  
0623 1255  
0624 3062  
0625 7240  
0626 3010  
0627 4463  
0628 7410  
0629 5223  
0630 1245  
0631 3153  
0632 4466  
0633 4466  
0634 4467  
0635 4466  
0636 5214  
0637 4471  
0638 5224

0623 4504  
0624 5225

0625 4451  
0626 7240  
0627 3010  
0628 4463  
0629 7410  
0630 5247  
0631 1246  
0632 3153  
0633 4467  
0634 4466  
0635 4466  
0636 4467  
0637 4466  
0638 5236  
0639 4471  
0640 5226

0645 0621  
0646 0643



EXT4C, JMS I XCHK4C /READ FACH FIELD AND CHECK  
JMP I ,+1 /SEE IF READY TO MOVE  
EXAM4+5

/ROUTINE TO WRITE W011  
/  
W011, 2

TAD M20  
JCA COUNT /0  
DCA I 10 /0  
CMA /1  
DCA I 10 /1  
CMA /1  
ISZ COUNT /COUNT = -16 OR -32  
JMP W011+3 /LOOP  
JMS I XKRNK /SEE IF END OF FIELD  
JMP I W011 /EXIT

/ROUTINE TO WRITE 1100  
/  
W1100, 0

TAD M20  
DCA COUNT /1  
CMA /1  
DCA I 10 /0  
CMA /0  
DCA I 10 /-16 OR -32  
ISZ COUNT /LOOP  
JMP W1100+3 /SEE IF END OF FIELD  
JMS I XKRNK /EXIT  
JMP I W1100

/ROUTINE TO WRITE 0110  
/  
W0110, 0

TAD M20  
DCA COUNT /0  
CMA /1  
DCA I 10 /1  
CMA /1  
DCA I 10 /0  
ISZ COUNT /-16 OR -32  
JMP W0110+3 /SEE IIF END OF FIELD  
JMS I XKRNK /EXIT  
JMP I W0110

0647 4505  
0657 5651  
0651 0265

0652 0000  
0653 1056  
0654 3072  
0655 3410  
0656 3410  
0657 7040  
0660 3410  
0661 7040  
0662 3410  
0663 2072  
0664 5255  
0665 4470  
0666 5667

0667 0000  
0670 1056  
0671 3072  
0672 7040  
0673 3410  
0674 7040  
0675 3410  
0676 3410  
0677 3410  
0700 2072  
0701 5272  
0702 4470  
0703 5667

0704 0000  
0705 1056  
0706 3072  
0707 3410  
0710 7040  
0711 3410  
0712 7040  
0713 3410  
0714 3410  
0715 2072  
0716 5307  
0717 4470  
0720 5704

/ROUTINE TO WRITE 1001

0721	0302
0722	1050
0723	3072
0724	7040
0725	3410
0726	3410
0727	3410
0730	7040
0731	3410
0732	2072
0733	5324
0734	4470
0735	5721

```

/1001,
TAD M20
DCA COUNT
CMA
DCA I 10
DCA I 10
DCA I 10
CMA
DCA I 10
ISZ COUNT
JMP W1001+3
JMS I XKRNK
JMP I W1001
/1
/0
/0
/1
/-16 TO -32
/LOOP
/SEE IF END OF FIELD
/EXIT

```

```

0736 0000 /ROUTINE TO READ ALL OF MEMORY 8 TIMES, COMPLEMENTING
0737 7200 /THE PATTERN EACH PASS. NO ERROR CHECKING IS DONE.
0740 1057 /RDALL, 0
0741 3072 CLA M40 /-32 DECIMAL
0742 7248 DCA COUNT /COUNTS PASSES THRU MEMORY
0743 3012 CLA CMA /SET ADR, REGS. TO 777
0744 7040 DCA 10
0745 3011 DCA 11
0746 3073 DCA FLCNT /SEE IF FIELD HAS PROGRAM
0747 4463 JMS I XBANK /READ
0750 7410 SKP C00N1 /DONE
0751 5360 JMP C00N1 /READ ONE
0752 7220 CLA /WRITE BACK
0753 1410 TAD I 10 /DONE 1 FIELD WHEN SKIP
0754 7040 CMA /DONE 32 PASSES WHEN SKIP
0755 3411 DCA I 11 /DO ANOTHER PASS
0756 2073 ISZ FLCNT
0757 5353 JMP RDLOP /EXIT
0760 2072 C00N1, ISZ COUNT
0761 5342 JMP RDALL+4
0762 5736 JMP I RDALL

1000 0000 /READ AND CHECK FOR ERROR ROUTINE
1001 4451 /
1002 4475 *1000
1003 3122 /RCHKA, 0
1004 4463 JMS I XSETU /SET OF TO 1ST FIELD
1005 7410 JMS I XRALL /READ ALL, DON'T CHECK
1006 5600 DCA MEMADR /SET ADR. COUN TO 0
1007 1235 JMS I XBANK /SEE IF FIELD HAS PROGRAM
1008 7410 SKP
1009 5600 JMP I RCHKA
1010 1235 TAD KRXT
1011 3153 DCA EXIT
1012 0000 /RDLOP, 0
1013 0000 0
1014 0000 0
1015 0000 0
1016 7000 0
1017 5212 JMP RLOPA+1
1018 1216 TAD 102
1019 1236 TAD K1K /NOP IF 0
1020 7640 SZA CLA /=JMS I XSALL
1021 5227 JMP ,+4
1022 1114 TAD JMS5
1023 3216 DCA RXIT-2
1024 5203 JMP RCHKA+3
1025 7000 NOP
1026 1000 RXIT,
1027 7000

```

130 1227  
131 3216  
132 4471  
133 5202  
134 5620  
  
135 1020  
136 1000

TAU ,=1  
DCA RXIT-2  
JMS I XTRNK  
JMP RCHKA+2  
JMP I RCHKA

/SETUP FOR NEXT FIELD  
/EXIT

/  
KRXI,  
K14,  
RXIT  
1000

/SETUP ROUTINES FOR RCHK1

```

1137 0000 /RCHK1, Z
1140 1110 TAD JMS1 /JMS1 = JMS I XRD1
1141 3211 DCA RLOPA
1142 1110 TAD JMS1
1143 3212 DCA RLOPA+1 /JMS1 = JMS I XRD1
1144 1111 TAD JMS2
1145 3213 DCA RLOPA+2
1146 1111 TAD JMS2
1147 3214 DCA RLOPA+3
1150 1110 TAD JMS1
1151 3215 DCA RLOPA+4
1152 4200 JMS RCHKA
1153 5637 JMP I RCHK1 /GO READ
/EXIT

```

/RCHK1C, W

```

1154 0000 /RCHK1C, W
1155 1111 TAD JMS2 /JMS2 = JMS I XRD2
1156 3211 DCA RLOPA
1157 1111 TAD JMS2
1158 3212 DCA RLOPA+1
1159 1110 TAD JMS1
1160 3213 DCA RLOPA+2
1163 1110 TAD JMS1
1164 3214 DCA RLOPA+3
1165 1111 TAD JMS2
1166 3215 DCA RLOPA+4
1167 4200 JMS RCHKA
1170 5654 JMP I RCHK1C /GO READ
/EXIT

```

/RCHK2, Z

```

1171 0000 /RCHK2, Z
1172 1110 TAD JMS1 /JMS1 = JMS I XRD1
1173 3211 DCA RLOPA
1174 1111 TAD JMS2 /JMS I XRD2
1175 3212 DCA RLOPA+1
1176 1111 TAD JMS2
1177 3213 DCA RLOPA+2 /JMS I XRD1
1180 1110 TAD JMS1
1181 3214 DCA RLOPA+3
1182 1110 TAD JMS1
1183 3215 DCA RLOPA+4
1184 4200 JMS RCHKA
1185 5671 JMP I RCHK2 /GO READ
/EXIT

```

/RCHK2C, Z

```

1186 0000 /RCHK2C, Z
1187 1111 TAD JMS2
1188 3211 DCA RLOPA
1189 1110 TAD JMS1 /JMS I XRD1
1190 3212 DCA RLOPA+1
1191 1110 TAD JMS1
1192 3213 DCA RLOPA+2
1193 1111 TAD JMS2
1194 3214 DCA RLOPA+3
1195 1111 TAD JMS2
1196 3214 DCA RLOPA+3

```

1117 1111  
1120 3215  
1121 4220  
1122 5705

TAD JMS2  
DCA RLOPA+4  
JMS RCHKA /GO READ  
JMP I RCHK2C /EXIT

/ RCHK3, Z

1123 0000 TAD JMS4 /JMS I XRD4  
 1124 1113 DCA RLOPA /JMS I XRD3  
 1125 3211 TAD JMS3 /JMS I XRD3  
 1126 1112 DCA RLOPA+1  
 1127 3212 TAD JMS3 /JMS I XRD3  
 1130 1112 DCA RLOPA+2 /JMS I XRD4  
 1131 3213 TAD JMS4 /JMS I XRD4  
 1132 1113 DCA RLOPA+3  
 1133 3214 TAD JMS4 /GO READ  
 1134 1113 DCA RLOPA+4 /EXIT  
 1135 3215 JMS RCHKA  
 1136 4222 JMP I RCHK3

/ RCHK3C, Z

1140 0000 TAD JMS3 /JMS I XRD3  
 1141 1112 DCA RLOPA /JMS I XRD4  
 1142 3211 TAD JMS4 /JMS I XRD4  
 1143 1113 DCA RLOPA+1  
 1144 3212 TAD JMS4  
 1145 1113 DCA RLOPA+2  
 1146 3213 TAD JMS3  
 1147 1112 DCA RLOPA+3  
 1150 3214 TAD JMS3  
 1151 1112 DCA RLOPA+4  
 1152 3215 JMS RCHKA /GO READ  
 1153 4222 JMP I RCHK3C /EXIT

/ \*1200

/ RCHK4, Z

1200 0200 JMS I XFILD  
 1201 4531 TAD JMS3 /JMS I XRD3  
 1202 1112 DCA I XLOPA /JMS I XRD3  
 1203 3637 TAD JMS3 /JMS I XRD3  
 1204 1112 DCA I XLOPB  
 1205 3640 TAD JMS4 /JMS I XRD4  
 1206 1113 DCA I XLOPC  
 1207 3641 TAD JMS4 /JMS I XRD4  
 1210 1113 DCA I XLOPD  
 1211 3642 TAD JMS3  
 1212 1112 DCA I XLOPE  
 1213 3643 JMS I XCFL  
 1214 4644 JMS I XCHKA /GO READ  
 1215 4636 JMP I RCHK4 /EXIT  
 1216 5600

/ RCHK4C, Z

1217 0000 JMS I XFILD  
 1220 4531 TAD JMS4 /JMS I XRD4  
 1221 1113 DCA I XLOPA  
 1222 3637 TAD JMS4  
 1223 1113 DCA I XLOPB  
 1224 3640 TAD JMS3

1123 0000  
 1124 1113  
 1125 3211  
 1126 1112  
 1127 3212  
 1130 1112  
 1131 3213  
 1132 1113  
 1133 3214  
 1134 1113  
 1135 3215  
 1136 4222  
 1137 5723

1140 0000  
 1141 1112  
 1142 3211  
 1143 1113  
 1144 3212  
 1145 1113  
 1146 3213  
 1147 1112  
 1150 3214  
 1151 1112  
 1152 3215  
 1153 4222  
 1154 5742

1200 0200  
 1201 4531  
 1202 1112  
 1203 3637  
 1204 1112  
 1205 3640  
 1206 1113  
 1207 3641  
 1210 1113  
 1211 3642  
 1212 1112  
 1213 3643  
 1214 4644  
 1215 4636  
 1216 5600

1217 0000  
 1220 4531  
 1221 1113  
 1222 3637  
 1223 1113  
 1224 3640  
 1225 1112

1226 3641  
 1227 1112  
 1230 3642  
 1231 1113  
 1232 3643  
 1233 4644  
 1234 4635  
 1235 5617  
 1236 1720  
 1237 1711  
 1240 1712  
 1241 1713  
 1242 1714  
 1243 1715  
 1244 1722

DCA I XLOPC  
 TAD JMS3  
 DCA I XLOPD  
 TAD JMS4  
 DCA I XLOPE  
 JMS I XCFL  
 JMS I XCHKA  
 JMP I RCHK4C

/GO READ

/EXIT

XCHKA,  
 XLOPA,  
 XLOPR,  
 XLOPC,  
 XLOPD,  
 XLOPE,  
 XCFL,  
 RCHKA  
 RLOPA  
 RLOPA+1  
 RLOPA+2  
 RLOPA+3  
 RLOPA+4  
 CFLU

PAUSE



/BI-85 EXTENDED CHECKERBOARD - TAPE 2

/READ ROUTINES FOR 0111; 1100; 0110 AND 1001

1245	0100			
1246	1256	TAD M20	/-16	
1247	3072	DCA COUNT		
1251	1060	TAD M4	/-4	
1251	3073	DCA FLCNT		
1252	1141	TAD M10	/-8	
1253	3074	DCA LOOP		
1254	1522	TAD I MEMADR		
1255	7040	CMA		
1256	3520	DCA I MEMADR		
1257	2074	ISZ LOOP		
1260	5254	JMP +4		/COMPLEMENT 8 TIMES
1261	2073	ISZ FLCNT		/DONE 4 ADRS. WHEN SKIP
1262	7410	SKP		
1263	5266	JMP +3		
1264	2122	ISZ MEMADR		
1265	5252	JMP CLOP1		
/				
1266	1122	TAD MEMADR		
1267	1060	TAD M4	/SUBTRACT 4	
1270	3010	DCA 10	/NOW USE AUTO-INDEX	
1271	7100	CLL		
1272	1410	TAD I 10		
1273	7440	SZA	/0	
1274	4521	JMS I XRROR	/PRINT ERROR	
1275	7100	CLL		
1276	1410	TAD I 10		
1277	7440	SZA	/0	
1300	4521	JMS I XRROR	/PRINT ERROR	
1301	7120	STL		
1302	1410	TAD I 10		
1323	7040	CMA	/1	
1324	7440	SZA		
1325	4521	JMS I XRROR	/PRINT ERROR	
1306	7120	STL		
1327	1410	TAD I 10		
1317	7040	CMA	/1	
1311	7440	SZA		
1312	4521	JMS I XRROR	/PRINT ERROR	
1313	2072	ISZ COUNT		
1314	5320	JMP +4		
1315	4470	JMS I XKBK		/SEE IF END OF FIELD
1316	2122	ISZ MEMADR		
1317	5645	JMP I RD1		
/				
1320	2122	ISZ MEMADR		/KEEP READING
1321	5250	JMP RD1+3		

```

1322 2024 /
1323 1356 /RD2,
1324 3372 /-16
1325 1368 /-4
1326 3373 /-R
1327 1141 /RD2,
1328 3374 /-R
1329 1322 /READ
1330 7040 CMA
1331 1322 CCA I MEMADR
1332 7040 CCA I MEMADR
1333 3522 ISZ LOOP
1334 2074 JMP :+4
1335 5331 ISZ FLCNT
1336 2073 SKP
1337 7410 JMP :+3
1338 5343 ISZ MEMADR
1339 2122 JMP CLOP2
1340 5327 /
1341 1122 TAD MEMADR
1342 1360 TAD M4
1343 3310 DCA I0
1344 7120 STL
1345 1410 TAD I 10
1346 7040 CMA
1347 7040 SZL
1348 7440 JMS I XRROR
1349 4521 STL
1350 7120 TAD I 10
1351 1410 CMA
1352 7040 SZL
1353 7440 JMS I XRROR
1354 4521 CLL
1355 7100 TAD I 10
1356 1410 SZL
1357 7440 JMS I XRROR
1358 4521 CLL
1359 7100 TAD I 10
1360 1410 SZL
1361 7440 JMS I XRROR
1362 4521 CLL
1363 7100 TAD I 10
1364 1410 SZL
1365 7440 JMS I XRROR
1366 4521 CLL
1367 2072 JMP :+4
1368 5375 JMS I XKBK
1369 4470 ISZ MEMADR
1370 2122 JMP I RD2
1371 5722 /
1372 2122 ISZ MEMADR
1373 5325 JMP RD2+3
1374 /
1375 2122 /KEEP READING
1376 5325

```

/COMPLEMENT 4 TIMES

/DONE 4 ADRS. WHEN SKIP

/INCREMENT ADDRESS

/NOW USE AUTO-INDEX

/1

/PRINT ERROR

/1

/PRINT ERROR

/0

/PRINT ERROR

/0

/PRINT ERROR

/SEE IF END OF FIELD

/KEEP READING

```

1400 / *1400
1401 / RJS.
1402 /
1403 /
1404 /
1405 /
1406 /
1407 /
1408 /
1409 /
1410 /
1411 /
1412 /
1413 /
1414 /
1415 /
1416 /
1417 /
1420 /
1421 /
1422 /
1423 /
1424 /
1425 /
1426 /
1427 /
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1431 /
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1436 /
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1442 /
1443 /
1444 /
1445 /
1446 /
1447 /
1450 /
1451 /
1452 /
1453 /
1454 /

TAD M20 /-16
DCA COUNT
TAD M4 /-4
DCA FLCNT /-8
TAD M10 /READ
DCA LOOP
TAD I MEMADR
CMA
DCA I MEMADR /COMPLEMENT 8 TIMES
ISZ LOOP /DONE 4 IF 0
JMP ,+4
ISZ FLCNT
SKP
JMP ,+3
ISZ MEMADR /DO NEXT
JMP CLOP3

TAD MEMADR /USE AUTO-INDEX
TAD M4
DCA I0
CLL
TAD I 10
SZA /0
JMS I XRROR /PRINT ERROR
STL /1
TAD I 10
CMA
SZA /PRINT ERROR
JMS I XRROR /PRINT ERROR
STL /1
TAD I 10
CMA
SZA /PRINT ERROR
JMS I XRROR /PRINT ERROR
CLL /0
TAD I 10 /PRINT ERROR
SZA /PRINT ERROR
JMS I XRROR /PRINT ERROR
ISZ COUNT /SEE IF END OF FIELD
JMP ,+4
JMS I XKBK
ISZ MEMADR
JMP I R03

ISZ MEMADR
JMP R03+3

```

1455	0000			
1456	1056	TAD M21		/-16
1457	3072	DCA COUNT		
1460	1260	TAD M4		/-4
1461	3376	DCA FLCNT		
1462	1141	TAD M14		/-8
1463	3374	DCA LOOP		
1464	1522	TAD I MEMADR		
1465	7340	CMA		
1466	3522	DCA I MEMADR		
1467	2074	ISZ LOOP		/COMPLEMENT 8 TIMES
1470	5264	JMP *-4		
1471	2073	ISZ FLCNT		/DONE 4 ADRS, WHEN SKIP
1472	7410	SKP		
1473	5276	JMP *+3		
1474	2122	ISZ MEMADR		/INCREMENT ADDRESS
1475	5262	JMP CLOP4		
1476	1122	TAD MEMADR		
1477	1260	TAD M4		
1478	3310	DCA 10		/USE AUTO-INDEX
1479	7120	STL		
1480	1410	TAD I 10		/1
1481	7040	CMA		
1482	7440	SZA		/PRINT ERROR
1483	4521	JMS I XRROR		
1484	7100	CLL		
1485	1410	TAD I 10		/0
1486	7440	SZA		/PRINT ERROR
1487	4521	JMS I XRROR		
1488	7100	CLL		
1489	1410	TAD I 10		/0
1490	7440	SZA		/PRINT ERROR
1491	4521	JMS I XRROR		
1492	7120	STL		
1493	1410	TAD I 10		/1
1494	7040	CMA		
1495	7440	SZA		/PRINT ERROR
1496	4521	JMS I XRROR		
1497	2072	ISZ COUNT		
1498	5330	JMP *+4		
1499	4470	JMS I XBNK		/SEE IF END OF FIELD
1500	2122	ISZ MEMADR		
1501	5655	JMP I RD4		
1502	2122	ISZ MEMADR		
1503	5260	JMP RD4*3		

```

1600
1601
1602
1603
1604
1605
1606
1607
1610
1611
1612
1613
1614
1615
1616
1617
1620
1621
1622
1623

/ROUTINE TO CHECK FOR END OF FIELD
/
*1600
CBANK, 4
CLA
TAD 12
CMA
SZA CLA
JMP I CBANK /NOT DONE
JMP I EXIT /DONE
/
/ROUTINE TO SEE IF TESTED FIELD HAS PROGRAM
/
CBANK, 4
RIF
OCA SAVIF
RDF
CIA
TAD SAVIF
SZA CLA
JMP I CBANK /EQUAL IF AC=0
JMS I XTRNK /DOESN'T HAVE PROGRAM
JMP I CBANK /INCREMENT DATA FIELD
ISZ CBANK /TEST NEW FIELD
JMP I CBANK /DONE ALL CAUSE PROGRAM NOW
/EXIT /IN HIGHEST FIELD
SAVIF, 4

```

```

1624 0000 /ROUTINE TO SET DF FOR NEXT FIELD
1625 7200 /
1626 6214 /NEXTBNK, 0
1627 7041 /READ DATA FIELD
1630 1124 /C(LAST1) = LAST IN TEST
1631 7640 /ALL DONE IF 0
1632 5235 /
1633 2224 /C(LAST1) = LAST IN TEST
1634 5242 /
1635 6214 /
1636 1134 /
1637 1125 /
1640 3241 /
1641 6221 /

1642 7004 /CHECK SWITCH REGISTER
1643 7710 /
1644 4527 /
1645 5624 /

1646 0000 /RESTORE DATA FIELD AND CHECK SR
1647 7200 /
1650 6214 /FIELD, 0
1651 3014 /
1652 6224 /
1653 1125 /
1654 3255 /
1655 6201 /
1656 7200 /
1657 5646 /

```

```

1700
/ *1700
/ /START HERE TO LOOP ON ADDRESS
/
1701 CLA
1702 LAS /READ LOWER LIMIT
1703 DCA FIRST1 /NOW SETUP UPPER LIMIT
1704 HLT
1705 LAS
1706 DCA LAST1
1707 TAD FIRST1
1708 DCA MEMADR
1709 TAD I MEMADR /READ
1710 DCA I MEMADR /WRITE
1711 TAD MEMADR
1712 CIA
1713 TAD LAST1
1714 SNA CLA
1715 JMP OVER
1716 ISZ MEMADR
1717 JMP WRLOP
1718 HLT
1719
1720 / CFLOD,
1721
1722 CLA DATFLD /TEST FIELD
1723 TAD KCDF
1724 DCA ,+1
1725 CDF #0
1726 CLA /RESTORE TEST FIELD
1727 JMP I CFLOD /EXIT
1728
1729
1730
1731
1732

```

```

2000 /PRINT ERROR ROUTINE
2001 /
2002 *2.00
2003 ERROR,
2004
2005 S2L /READING 1'S IF LINK = 1
2006 CMA /SAVE RAD DATA
2007 DCA RAD /SAVE GOOD DATA
2008 S2L /OCTAL ADDRESS
2009 CMA /RESTORE DATA FIELD
2010 DCA GOOD /DATA FIELD
2011 TAU 10 /LAST = FIELD WITH LAST ERROR
2012 DCA 0CADR /SAME IF 0
2013 JMS I XFILD /DON'T STORE
2014 TAD DATFLO /TABLE POINTER
2015 CIA
2016 TAD LAST /END OF TABLE IF = 0
2017 SNA CLA /RESTORE POINTER
2018 JMP SW2 /INCREMENT POINTER
2019 TAD DATFLO /STORE IN TABLE
2020 DCA LAST
2021 TAD ERWRD
2022 CIA
2023 TAD ENTBL
2024 SZA CLA
2025 JMP 3
2026 TAD ERTBL
2027 DCA ERWRD
2028 TAD DATFLO
2029 ISZ ERWRD
2030 DCA I ERWRD
2031
2032
2033 LAS
2034 RTL /SR2 ON A 1 = RING BELL
2035 SMA CLA
2036 JMP SW1 /RING HELL
2037 TAD K207
2038 JMS PRERR
2039 JMP SW0
2040
2041 LAS
2042 RAL /SR1 A 1 = NO PRINT
2043 SMA CLA
2044 JMP ERPRNT
2045 TAD DATFLO
2046 TAD KCDF
2047 DCA 1
2048 CDF 00
2049 CLA
2050 JMP I ERROR
2051
2052
2053

```



```

2054 4354 JMS CRLF /CR,LF
2055 1214 TAU DATFLO /TEST NUMBER
2056 7012 RTR
2057 7010 RAR
2058 1142 TAU K260 /PRINT
2061 4346 JMS PRERR /-12 DECIMAL
2062 1140 TAD M14 /SPACE 12
2063 3074 DCA LOOP /OCTAL ADR,
2064 4315 JMS SPING /SAVE
2065 1365 TAD OCADR /PRINT
2066 3134 DCA CHAR /-8 DECIMAL
2067 4326 JMS PROCTL /SPACE 8
2070 1141 TAU M10
2071 3074 DCA LOOP
2072 4315 JMS SPING
2073 1364 TAD GOOD
2074 3134 DCA CHAR
2075 4326 JMS PROCTL /PRINT
2076 1061 TAD M5 /-5
2077 3074 DCA LOOP
2078 4315 JMS SPING /SPACE 5
2101 1363 TAD BAD
2102 3134 DCA CHAR
2103 4326 JMS PROCTL /PRINT
2104 1061 TAD M5
2105 3074 DCA LOOP
2106 4315 JMS SPING /SPACE 5
2107 1062 TAD TNUM /TEST NUMBER
2110 4346 JMS PRERR /PRINT

/ SK0, LAS /CH CK SR0
SPA CLA /GO HALT
JMS I XHLT /EXIT
JMP EREXT

/ /PRINT SPACES
/ SPING, 0 /SPACE
TAD K240 /PRINT
TLS
TSF
JMP I-1
ISZ LOOP
JMP SPING+2
CLA
JMP I SPING /EXIT

2111 7604
2112 7710
2113 4527
2114 5246

2115 0000
2116 1370
2117 6046
2120 6041
2121 5320
2122 2074
2123 5317
2124 7200
2125 5715

```

```

2126 0000 /PRINT OCTAL
2127 1060 /PROCTL, 0 /-4 /DIGIT COUNTER
2132 3074 DCA LOOP
2131 1134 TAD CHAR
2132 7104 CLL RAL
2133 7006 RTL
2134 3134 DCA CHAR
2135 1134 TAD CHAR
2136 7004 RAL
2137 0133 AND K7
2140 1142 TAD K260 /MAKE ASCII
2141 4346 JMS PRERR /PRINT ONE
2142 2074 ISZ LOOP
2143 5331 JMP POSITN /DO NEXT
2144 7200 CLA
2145 5726 JMP I PROCTL /EXIT

/PRINT A NUMBER
/
PRERR, 0 TLS
TSF
JMP ,+1
CLA
JMP I PRERR /EXIT

/CARRIAGE RETURN, LINE FEED
/
CRLF, 0
CLA K215 /CR
TAD K215
JMS PRERR /LF
TAD K212
JMS PRERR
JMP I CRLF

/
BAD, 0
GOOD, 0
OCADR, 0
LAST, 0
K207, 207
K240, 240
K212, 212

```

```

2200
/
*2200
/
/ROUTINE TO SET DF TO FIRST TEST FIELD
/
SETU1, 0
  CLA
  TAD FIRST1 /FIRST TO TEST
  TAD KODF
  DCA ,+1
  CDF 00 /CHANGE TO TEST FIELD
  JMP I SETU1 /EXIT
/
/ROUTINE TO ACCEPT TEST LIMITS FROM
/KEYBOARD INPUT
/
SLM1TS, 0
  JMS I XFILD /CR, LF
  JMS I XCRLF /PRINT TEST LIMITS
  JMS I XTLIM /CR, LF
  JMS I XCRLF /GO ACCEPT INPUT
  JMS KEYIN /SEE IF IT'S LEGAL
  JMS LEGAL
  TAD CHAR
  AND K7 /MASK AC 9=11
  CLL RAL
  RTL RAL /POSITION TO AC 6=8
  DCA FIRST1 /FIRST TO TEST
  JMS KEYIN /WAIT FOR COMMA
  TAD CHAR /GET INPUT
  CIA
  TAD K254 /OK IF 0
  SNA
  JMP ,+3 /PRINT QUESTION MARK
  JMS QUERY
  JMP SLM1TS+2 /WAIT FOR 2ND
  JMS KEYIN /SEE IF IT'S LEGAL
  JMS LEGAL
  TAD CHAR
  AND K7 /MASK AC 9=11
  CLL RAL
  RTL RAL /POSITION TO AC 6=8
  DCA LAST1 /LAST TO TEST
  TAD FIRST1
  CIA
  TAD LAST1
  SMA
  JMP OKAS
  CLA

```

2252 1123  
 2251 3134  
 2252 1124  
 2253 3123  
 2254 1134  
 2255 3124  
 2256 7200  
 2257 1124  
 2260 7041  
 2261 1123  
 2262 7440  
 2263 5273  
 2264 1123  
 2265 7241  
 2266 1216  
 2267 7640  
 2270 5273  
 2271 4752  
 2272 5211  
 2273 4323  
 2274 1134  
 2275 7241  
 2276 1143  
 2277 7450  
 2300 5607  
 2301 4344  
 2302 5211  
 2303 0000  
 2304 6032  
 2305 6031  
 2306 5305  
 2307 6036  
 2310 3134  
 2311 1134  
 2312 4536  
 2313 5703

TAD FIRST1  
 DCA CHAR  
 TAD LAST1  
 DCA FIRST1 /LAST NOW IS FIRST  
 TAD CHAR  
 DCA LAST1 /FIRST IS NOW LAST  
 CLA  
 TAD LAST1  
 CIA  
 TAD FIRST1 /SEE IF EQUAL  
 SZA /YES IF 0  
 JMP ALOK  
 TAD FIRST1 /NOW SEE IF IT HAS PROGRAM  
 CIA /CURRENT FIELD  
 TAU INSELD /NO IF A 1  
 SZA CLA  
 JMP ALOK  
 JMS I XLCAT /PRINT PROGRAM LOCATION  
 JMP SLMTS+2 /AND START OVER  
 JMS KEYIN /WAIT FOR C,R,  
 TAD CHAR  
 CIA  
 TAD K215 /NOT A C.R, IF A SKIP  
 SNA  
 JMP I SLMTS /PRINT QUESTION MARK  
 JMS QUERY /START OVER  
 JMP SLMTS+2  
 0  
 KCC  
 KSF  
 JMP .-1  
 KRB  
 DCA CHAR  
 TAD CHAR  
 JMS I XPERR  
 JMP I KEYIN

OKAS,

ALOK,

/ KEYIN,

2314	0000	/	LEGAL,	2	
2315	1134			TAD CHAR	
2316	7041			CIA	
2317	1144			TAD K377	/RUB-OUT IF 0
2320	7650			SNA CLA	
2321	5211			JMP SLMTS+2	
2322	1134			TAD CHAR	
2323	0145			AND K370	
2324	7041			CIA	
2325	1142			TAD K260	
2326	7650			SNA CLA	
2327	5714			JMP I LEGAL	
2330	1134			TAD CHAR	
2331	7041			CIA	
2332	1353			TAD K254	/A COMMA IF 0
2333	7650			SNA CLA	
2334	5714			JMP I LEGAL	
2335	1134			TAD CHAR	
2336	7041			CIA	
2337	1143			TAD K215	
2340	7650			SNA CLA	/A C.R. IF 0
2341	5714			JMP I LEGAL	
2342	4344			JMS QUERY	/QUERY
2343	5211			JMP SLMTS+2	/START OVER
2344	0000	/	QUERY,	0	
2345	4547			JMS I XCRLF	
2346	1146			TAD K277	
2347	4536			JMS I XPERR	/PRINT QUERY MARK
2350	5744			JMP I QUERY	
2351	2446	/	XTLIM,	TLIMIT	
2352	2400		XLOCAT,	LOCAT	
2353	0254		K254,	254	

```

2400 2400
2401 7200
2402 1216
2403 7712
2404 7712
2405 0133
2406 1142
2407 3244
2410 1217
2411 3712
2412 1412
2413 7450
2414 5600
2415 4536
2416 5212

2417 2417
2420 0320
2421 0322
2422 0317
2423 0307
2424 0322
2425 0301
2426 0315
2427 0240
2430 0311
2431 0323
2432 0240
2433 0311
2434 0316
2435 0240
2436 0306
2437 0311
2440 0305
2441 0314
2442 0304
2443 0240
2444 0700
2445 0000

/PRINT FIELD PROGRAM IS IN
/
*2400
/LOCAT,
CLA
TAD INSFLD
RTR
RAR
AND K7
TAD K260
DCA FLDN
TAD PRGAM
DCA 12
PLOC, TAD I 12
SNA
JMP I LOCAT
JMS I XPERR
JMP PLOC

/PRGAM,
/P
/R
/O
/G
/R
/A
/M
/
/I
/S
/I
/N
/
/F
/I
/E
/L
/D
/
/X
/TERMINATOR

/FLDN = PRGAM+25

```

```

2446 0000 /TLIMIT, 0
2447 7200 CLA
2450 1257 TAD TSTL
2451 3012 DCA 12
2452 1412 TAD I 12
2453 7450 SNA
2454 5646 JMP I TLIMIT
2455 4536 JMS I XPERR
2456 5252 JMP PLIMIT
2457 2457 /TSTL, 0
2460 0324 324
2461 0305 305
2462 0323 323
2463 0324 324
2464 0240 240
2465 0314 314
2466 0311 311
2467 0315 315
2470 0311 311
2471 0324 324
2472 0323 323
2473 0000 0

```

```

/PRINT TEST LIMITS
/CR, LF
/PRINT FIELD
/DONE IF 0
/DONE IF 0
/TERMINATOR
/HEADER ROUTINE
/PHDR, 0
JMS I XCRLF
TAD FILD
DCA 12
TAD I 12
SNA
JMP ,+3
JMS I XPERR
JMP PFILD
TAD M5
DCA LOOP
JMS I XPING
TAD OILDR
DCA 12

```

```

2474 0000
2475 4547
2476 1332
2477 3012
2500 1412
2501 7450
2502 5305
2503 4536
2504 5300
2505 1061
2506 3074
2507 4537
2510 1341
2511 3012

```

2512	1412	POCDR,	TAD I 12		
2513	7452		SNA	/DONE IF 0	/PRINT OCTAL ADR
2514	5317		JMP ,+3		
2515	4536		JMS I XPERR		
2516	5312	/	JMP POCOR		
2517	1061		TAD M5		
2520	3074		DCA LOOP	/SPACE 5	
2521	4537		JMS I XPING		
2522	1355		TAD GOOD		
2523	3012		DCA 12		
2524	1412	PGOOD,	TAD I 12	/DONE IF 0	/PRINT GOOD
2525	7452		SNA		
2526	5731		JMP I ,+3		
2527	4536		JMS I XPERR		
2530	5324		JMP PGOOD		
2531	2600	/	HSPACE	/NEXT PAGE	
2532	2532	FILD,			
2533	0306			/F	
2534	0311			/I	
2535	0305			/E	
2536	0314			/L	
2537	0304			/D	
2540	0000				
2541	2541	OTLDR,			
2542	0317			/O	
2543	0303			/C	
2544	0324			/T	
2545	0301			/A	
2546	0314			/L	
2547	0240				
2550	0301			/A	
2551	0304			/D	
2552	0322			/R	
2553	0256				/,
2554	0200				
2555	2555	GOOD,			
2556	0327			/G	
2557	0317			/O	
2560	0317			/O	
2561	0304			/D	
2562	0000				
2563	5674	EXHDR,	JMP I PHDR		



```

2600 2600 / *2600
2601 1261 / RSPACE,
2602 3074 /
2603 4537 /SPACE 5
2604 1234 /
2605 3012 /
2606 1412 /DONE IF 0
2607 7450 /PRINT BAD
2608 5212 /
2609 4536 /
2610 5205 /
2611 1061 /
2612 3074 /SPACE 5
2613 4537 /
2614 1226 /
2615 3012 /
2616 1412 /PRINT TEST
2617 7450 /DONE IF 0
2618 5224 /
2619 4536 /
2620 5217 /
2621 4547 /CR, LF
2622 5644 /EXIT
2623 2626 /
2624 0324 /T
2625 0305 /E
2626 0323 /S
2627 0324 /T
2628 0000 /
2629 2634 /
2630 0302 /B
2631 0301 /A
2632 0304 /D
2633 0000 /
2634 2634 /
2635 0302 /
2636 0301 /
2637 0304 /
2638 0000 /
2639 2641 /
2640 7402 /
2641 5641 /RESTART HERE OR RTRN1
2642 2563 /
2643 2563 /
2644 2563 /
2645 2563 /
2646 2563 /
2647 2563 /
2648 2563 /
2649 2563 /
2650 2563 /
2651 2563 /
2652 2563 /
2653 2563 /
2654 2563 /
2655 2563 /
2656 2563 /
2657 2563 /
2658 2563 /
2659 2563 /
2660 2563 /
2661 2563 /
2662 2563 /
2663 2563 /
2664 2563 /
2665 2563 /
2666 2563 /
2667 2563 /
2668 2563 /
2669 2563 /
2670 2563 /
2671 2563 /
2672 2563 /
2673 2563 /
2674 2563 /
2675 2563 /
2676 2563 /
2677 2563 /
2678 2563 /
2679 2563 /
2680 2563 /
2681 2563 /
2682 2563 /
2683 2563 /
2684 2563 /
2685 2563 /
2686 2563 /
2687 2563 /
2688 2563 /
2689 2563 /
2690 2563 /
2691 2563 /
2692 2563 /
2693 2563 /
2694 2563 /
2695 2563 /
2696 2563 /
2697 2563 /
2698 2563 /
2699 2563 /
2700 2563 /

```

```

2645 0000
2646 4531
2647 4547
2650 1270
2651 3012
2652 1412
2653 7454
2654 5257
2655 4536
2656 5252
2657 6336
2660 6031
2661 5265
2662 6036
2663 4536
2664 5645
2665 7604
2666 3033
2667 5260

2670 2670
2671 0323
2672 0305
2673 0324
2674 0325
2675 0320
2676 0240
2677 0323
2700 0322
2701 0000

2702 0000
2703 4531
2704 4316
2705 3074
2706 2074
2707 5306
2710 1014
2711 1125
2712 3313
2713 6201
2714 7200
2715 5702

/
/
/ *WAIT HERE TO SETUP SR, TYPE CARRIAGE RETURN
/ AFTER SETTING SR,
/
SETSR, 0
JMS I XFILD /RESTORE DATA FIELD
JMS I XCRLF /CR, LF
TAD STSR
OCA 12
TAD I 12 /PRINT SETUP SR
SNA /DONE IF 0
JMP ,+3
JMS I XPERR
JMP PSTSR
KRB
KSF ,+4
JMP ,+4
KRB
JMS I XFERR
JMP I SETSR
LAS
DCA MCWA
JMP WTCR

/ STSR,
323 /S
305 /E
324 /T
325 /U
320 /P
240 /S
323 /R
522
0

/ STALL,
0
JMS I XFILD
JMS GENRAN /GET ANOTHER
DCA LOOP /18.5 MS MAX,
ISZ LOOP
JMP ,+1
TAD DATFLO
TAD KCOF
DCA ,+1
COF 00
CLA
JMP I STALL /EXIT

```

2716 2700  
 2717 1354  
 2720 7741  
 2721 1342  
 2722 7640  
 2723 5333  
 2724 1355  
 2725 3342  
 2726 1341  
 2727 7104  
 2730 7432  
 2731 1150  
 2732 3341  
 2733 1742  
 2734 1341  
 2735 3742  
 2736 1742  
 2737 2342  
 2740 5716

GENRAN, 2  
 TAD RANTAB  
 CIA  
 TAD RANDEX  
 SZA CLA  
 JMP RANTAD-1  
 TAD TBLRAN  
 JCA RANDEX  
 TAD RANCON  
 CLL RAL  
 SZL  
 TAD K1  
 JCA RANCON  
 TAD I RANDEX  
 RANTAD,  
 JCA I RANCON  
 TAD I RANDEX  
 ISZ RANDEX  
 JMP I GENRAN

2741 1234  
 2742 2753  
 2743 4321  
 2744 1416  
 2745 5363  
 2746 6060  
 2747 3035  
 2750 2572  
 2751 3237  
 2752 0214  
 2753 0000  
 2754 2753  
 2755 2743  
 2756 0177

RANCON, 1234  
 RANDEX, RANTBL+10  
 RANTBL, 4321  
 1416  
 5363  
 6060  
 3035  
 2572  
 3237  
 0214  
 0  
 RANTAB, 1-1  
 TBLRAN, RANTBL  
 K177, 177

```

3000 /ROUTINE TO DETERMINE FIELD FOR RELOCATION
3001 /
3002 /*3000
3003 /
3004 /CMOVE, JMS I XFILD /SET DF TO CURRENT FIELD
3005 7600
3006 TAD ERTBL
3007 DCA ERWRD /SETUP ERROR TABLE POINTER
3008 TAD FIRST1 /FIRST TESTED FIELD
3009 CIA
3010 TAD LAST1 /LAST TESTED FIELD
3011 SNA CLA /DON'T MOVE IF EQUAL
3012 JMP I XRTN /START OVER
3013 RAR
3014 SZL /FIRST MOVE IF A SKIP
3015 JMP I XTMV /SETUP FOR NEXT MOVE
3016 IAC /SET BIT 11
3017 DCA FLAGS
3018
3019 TAD LAST1 /LAST TO TEST = 1ST MOVE
3020 DCA INSFLD /NEW CURRENT FIELD
3021 TAD INSFLD
3022 TAD M10 /SUBTRACT 1 FROM NEW CURRENT
3023 DCA NXLOC /NXLOC=DEST'N FOR NEXT TIME
3024 RIF
3025 CIA
3026 TAD INSFLD
3027 SNA CLA /IS NEXT SAME AS CURRENT
3028 JMP SUB1 /YES, TRY NEXT LOWER FIELD
3029
3030

```

/CHECK FOR ERROR IN NEW FIELD  
/

3031 2022 CKERR, ISZ ERWRD /POINTER+1  
 3032 1201 TAD CMOVE+1  
 3033 7041 CIA  
 3034 1422 TAD I ERWRD /NO ERRORS RECORDED IF 0  
 3035 7650 SNA CLA /INITIALIZE MOVE  
 3036 5310 JMP STMV

3037 1422 CVXT, TAD I ERWRD  
 3040 7041 CIA  
 3041 1016 TAD INSFLD /ERROR IN NEW FIELD IF 0  
 3042 7650 SNA CLA  
 3043 5253 JMP EQUAL  
 3044 1022 TAD ERWRD  
 3045 7041 CIA  
 3046 1021 TAD ENTL /ENTBL=ERWRD+10  
 3047 7650 SNA CLA /TABLE DONE IF 0  
 3050 5310 JMP STMV /INITIALIZE MOVE  
 3051 2022 ISZ ERWRD /POINTER+1  
 3052 5237 JMP CNXT

3053 1422 /EQUAL, TAD I ERWRD /GET ERROR FIELD  
 3054 7041 CIA  
 3055 1123 TAD FIRST1  
 3056 7650 SNA CLA /DON'T MOVE IF = TO FIRST  
 3057 5530 JMP I XRTN /START OVER  
 3060 1422 TAD I ERWRD  
 3061 7650 SNA CLA /IS IT FIELD 0?  
 3062 5266 JMP SUB1 /YES  
 3063 1016 TAD INSFLD /CURRENT NEXT  
 3064 1141 TAD M10 /SUBTRACT 1 FROM DF  
 3065 3151 DCA VXLOC

3066 1020 /SUB1, TAD ERTBL /RESTORE TABLE POINTER  
 3067 3022 DCA ERWRD  
 3070 1151 TAD NXLOC  
 3071 7041 CIA  
 3072 1016 TAD INSFLD  
 3073 7650 SNA CLA /NEXT = CURRENT NEXT IF 0  
 3074 5253 JMP EQUAL /NEW CURRENT FIELD  
 3075 1151 TAD NXLOC  
 3076 3016 DCA INSFLD  
 3077 1016 TAD INSFLD  
 3100 7041 CIA  
 3101 1123 TAD FIRST1 /IS IT = LOWEST FIELD  
 3102 7650 SNA CLA /YES  
 3103 5231 JMP CKERR /CURRENT NEW FIELD  
 3104 1016 TAD INSFLD /SUBTRACT 1 FROM DF  
 3105 1141 TAD M10 /NEXT FIELD LOWER  
 3106 3151 DCA VXLOC

4/24/68 13:41.0

JMP CKERR

3107 5231

```

3110 7200 CLA
3111 1020 TAD ERTBL
3112 3022 DCA ERWRD
3113 6224 RIF
3114 3723 DCA I XSRCE
3115 1723 TAD I XSRCE
3116 7041 CIA
3117 1016 TAD INSFLD
3120 7650 SNA CLA
3121 5530 JMP I XRTN
3122 5724 JMP I XMVE

```

/RESTORE TABLE POINTER

```

/DON'T MOVE IF EQUAL
/START OVER
/GO MOVE

```

/ STMV,

```

/ XSRCE,
  XMVE,
  XTMV,
/

```

```

3123 3327 SOURCE
3124 3307 MOVE
3125 3200 NXTMV

```

3200	SPED				
3201	7600				
3202	6P24	RIF			
3203	3327	DCA SOURCE	/CURRENT FIELD		
3204	2722	ISZ ERWRD	/POINTER +1		
3205	1270	TAD NXTMV			
3206	7041	CIA			
3207	1422	TAD I ERWRD			
3208	7650	SNA CLA	/NO ERRORS RECORDED IF 0		
3209	5225	JMP STNXT	/INITIALIZE MOVE		
3210	1422	TAD I ERWRD			
3211	7041	CIA			
3212	1151	TAD NXLOC	/ERROR IN NEW FIELD IF 0		
3213	7650	SNA CLA	/TRY NEXT LOWER FIELD		
3214	5255	JMP SUR2			
3215	1222	TAD ERWRD			
3216	7041	CIA			
3217	1021	TAD ENTBL	/DONE WITH TABLE IF 0		
3218	7650	SNA CLA	/INITIALIZE MOVE		
3219	5225	JMP STNXT	/POINTER +1		
3220	2022	ISZ ERWRD			
3221	5211	JMP CKVXT			
3222					
3223					
3224					
3225	1120	TAD ENTBL			
3226	3322	DCA ERWRD	/RESTORE TABLE POINTER		
3227	1151	TAD NXLOC	/NEXT LOWER FIELD		
3228	7041	CIA			
3229	1016	TAD INSFLD			
3230	7650	SNA CLA	/NEXT=CURRENT IF 0		
3231	5242	JMP CKNT			
3232	1151	TAD NXLOC			
3233	7041	CIA			
3234	1123	TAD FIRST1			
3235	7640	SZA CLA	/NEXT = LOWEST IF 0		
3236	5247	JMP STNX			
3237	5302	JMP MVRK	/MOVE TO LOWEST TEST FIELD		
3238	1151	TAD NXLOC			
3239	7041	CIA			
3240	1123	TAD FIRST1			
3241	7650	SNA CLA	/NEXT = LOWEST IF 0		
3242	5275	JMP NXTHI	/SETUP TO MOVE TO HIGHEST		
3243	1151	TAD NXLOC	/NEXT LOWER FIELD		
3244	3016	DCA INSFLD	/IS NOW CURRENT FIELD		
3245	1016	TAD INSFLD			
3246	1141	TAD M1Z	/SUBTRACT 1 FROM NEW		
3247	3151	DCA NXLOC	/NEW NEXT LOWER FIELD		
3248	5307	JMP MOVE	/GO MOVE		



3255	TAD ERTBL		
3256	DCA ERWRD		/RESTORE TABLE POINTER
3257	TAD NXLOC		/NEXT LOWER FIELD
3260	SNA		/=FIELD 0 IF 0
3261	JMP I XRTN		/START OVER CAN'T MOVE
3262	TAD M1P		/SUBTRACT 1
3263	DCA NXLOC		/NOW = 2 FIELDS LOWER
3264	TAD NXLOC		
3265	CIA		
3266	TAD INSFLD		/CURRENT FIELD
3267	SZA CLA		/ARE THEY EQUAL
3270	JMP CHNXT		/NO
3271	TAD NXLOC		/YES
3272	SNA		/DOES IT = FIELD 0
3273	JMP CHNXT		/YES
3274	JMP SUR2*5		/NO
/			
3275	TAD LAST1		/VERY LAST TO TEST
3276	DCA NXLOC		/MAKE IT NEXT FIELD
3277	TAD LAST1		
3300	DCA INSFLD		
3301	JMP CHNXT		
/			
3302	TAD NXLOC		
3303	DCA INSFLD		
3304	RIF		
3305	DCA SOURCE		
3306	DCA FLAGS		/CLEAR BIT 11

```

/ROUTINE TO RELOCATE 4K FIELDS
/
MOVE,
TAD KCDF
TAD SOURCE
DCA SOURCE
TAD KCDF
TAD INSFLO
DCA DESTN
TAD SOURCE
CIA
TAD DESTN
SNA CLA
JMP I XRTN
CMA
DCA 10
CMA
DCA 11
DCA LOOP
SOURCE, 2
DESTN, 2
DCA I 11
ISZ LOOP
JMP SOURCE
TAD KCIF
TAD INSFLO
DCA ,+1
CIF 00
JMP I XRTN
/6201
/CURRENT FIELD
/SOURCE NOW = CDF N
/6201
/NEW FIELD
/DESTN NOW = CDF N
/SOURCE COUNT
/DESTINATION COUNT
/4K COUNTER
/WILL = CDF N
/TAKE FROM HERE
/PUT IN HERE
/DONE 4K WHEN SKIP
/KEEP MOVING
/6202
/NEW FIELD
/CHANGE TO NEW FIELD
/EXIT TO RTN1 IN
/NEW FIELD

```

3307 1125  
3310 1527  
3311 3327  
3312 1125  
3313 1016  
3314 3331  
3315 1327  
3316 7041  
3317 1331  
3320 7650  
3321 5530  
3322 7040  
3323 3010  
3324 7040  
3325 3011  
3326 3074  
3327 0000  
3330 1410  
3331 0200  
3332 3411  
3333 2074  
3334 5327  
3335 1126  
3336 1016  
3337 3340  
3340 6202  
3341 5530

THERE ARE NO ERRORS

## SYMBOL TABLE

ALW	0220
ALOK	2273
BAD	2163
BADD	2634
REGIN	0200
RSPACE	2600
CRANK	1627
CDF	6271
CDAM1	0760
CFLD	1722
CHAR	0134
CHXT	3273
CIF	6272
CKRnk	1600
CKERR	3031
CKNT	3242
CKNXT	3211
CLOP1	1252
CLOP2	1327
CLOP3	1425
CLOP4	1462
CMOVE	3000
CNXT	3237
COUNT	0072
CRLF	2154
DATAFLD	0014
DESTN	3331
ENTBL	0021
EPRNT	2054
EQUAL	3053
EREXT	2046
ERROR	2020
ERTBL	0020
ERWRD	0022
EXAM1	0241
EXAM2	0246
EXAM3	0253
EXAM4	0260
EXHDR	2563
EXIT	0153
EXT1	0322
EXT1C	0343
EXT2	0423
EXT2C	0445
EXT3	0475
EXT3C	0517
EXT4	0623
EXT4C	0647
FIELD	1646
FILD	2532
FIRST1	0123
FLAGS	0015
FLCNT	0073

## SYMBOL TABLE

FLDN	2444
FLPAD	2217
GENRAN	2716
GOOD	2555
GOOD	2164
HALT	2641
INSPFD	2216
JMS1	2112
JMS2	2111
JMS3	2112
JMS4	2113
JMS5	2114
KCOF	2125
KCIF	2126
KEYIN	2323
KRXT	2335
KXT1	2345
KXT1C	2346
KXT2	2452
KXT2C	2451
KXT3	2522
KXT3C	2523
KXT4	2645
KXT4C	2646
K1	2152
K1K	2236
K1A	2234
K100	2242
K177	2756
K20	2042
K200	2237
K207	2167
K212	2171
K215	2143
K240	2172
K254	2353
K260	2142
K261	2052
K262	2053
K263	2054
K264	2055
K277	2146
K370	2145
K377	2144
K40	2241
K400	2036
K7	2133
K740	2235
LAST	2166
LASTX	2154
LAST1	2124
LEGAL	2314
LOCAT	2422

## SYMBOL TABLE

LOOP	2074
MCWA	2033
MEMAOR	2122
MOVE	3307
MVRK	3302
M1A	0141
M14	1140
M2A	2056
M4	2060
M40	2057
M5	2061
NXLOC	0151
NXTBANK	1624
NXTHI	3275
NXTMV	3200
OCADR	2165
OKAS	2256
OTLDR	2541
OVFR	1706
PBAD	2605
PFLD	2500
PGOOD	2524
PHDR	2474
PLIM	2452
PLCT	2412
POADR	2512
POSITN	2131
PRERR	2146
PRGAM	2417
PROCTL	2126
PSTSR	2652
PTSTN	2617
QUERY	2344
RANCON	2741
RANDEX	2742
RANTAB	2754
RANTAD	2734
RANTBL	2743
RCHKA	1000
RCHK1	1037
RCHK1C	1054
RCHK2	1071
RCHK2C	1106
RCHK3	1123
RCHK3C	1140
RCHK4	1222
RCHK4C	1217
RDALL	2736
RDF	6214
RDL0P	2753
RD1	1245
RD2	1322
RD3	1420

## SYMBOL TABLE

RD4	1455
RIF	6224
RLOPA	1211
RSTRT1	2211
RTVI	2213
RXIT	1020
SAVIF	1623
SETSF	2645
SETU1	2220
SUMTS	2237
SOURCE	3327
SPING	2115
STALL	2722
STMV	3110
STNX	3247
STNXT	3225
STSR	2672
SURI	3266
SUR2	3255
SW1	2111
SW2	2042
SW3	2033
TBLRAN	2755
TDW20	0126
TDW42	0107
TLIMT	2446
TNUM	2262
TSTL	2457
TSTN	2626
TSY1	2320
TSY1C	0324
TST2	2400
TST2C	2425
TST3	2452
TST3C	2477
TST4	2600
TST4C	0625
WRLOP	1710
WTR	2660
W0011	2652
W0112	2724
W1021	2721
W1122	0667
XBANK	2263
XCFL	1244
XCHKA	1236
XCHK1	2076
XCHK1C	2277
XCHK2	2122
XCHK2C	2121
XCHK3	2122
XCHK3C	2123
XCHK4	2174

## SYMBOL TABLE

XCHK4C	0105
XCRLF	0147
XFILD	0131
XHDR	0135
XHLT	0127
XIT1	0320
XIT1C	0341
XIT2	0421
XIT2C	0443
XIT3	0473
XIT3C	0515
XIT4	0621
XIT4C	0643
XKBK	0070
XLAT	2352
XLMTS	0043
XLOPA	1237
XLOPB	1240
XLOPC	1241
XLOPD	1242
XLOPE	1243
XMOVE	0050
XMVE	3124
XPERR	0136
XPHDR	2644
XPING	0137
XPRER	0132
XRALL	0075
XR01	0115
XR02	0116
XR03	0117
XR04	0120
XRROR	0121
XRTN	0130
XSALL	0152
XSETU	0051
XSRCE	3123
XSTSR	0277
XTBANK	0071
XLLIM	2351
XTMV	3125
XTST1	0044
XTST2	0045
XTST3	0046
XTST4	0047
X0011	0064
X0110	0066
X1001	0067
X1100	0065

SYMBOL TABLE

DATAFLD	0014
FLAGS	0015
INSFLD	0016
FLOAD	0017
ERTBL	0020
ENTBL	0021
ERRRD	0022
NCWA	0033
K10	0034
K740	0035
K402	0036
K200	0037
K100	0040
K40	0041
K20	0042
XLMTS	0043
XTST1	0044
XTST2	0045
XTST3	0046
XTST4	0047
XMOVE	0050
XSETU	0051
K261	0052
K262	0053
K263	0054
K264	0055
M20	0056
M40	0057
M4	0060
M5	0061
INUM	0062
XBANK	0063
X0011	0064
X1100	0065
X0110	0066
X1001	0067
XKRK	0070
XTRK	0071
COUNT	0072
FLCNT	0073
LOOP	0074
XRALL	0075
XCHK1	0076
XCHK1C	0077
XCHK2	0100
XCHK2C	0101
XCHK3	0102
XCHK3C	0103
XCHK4	0104
XCHK4C	0105
TDM20	0106
TDM40	0107
JMS1	0110



## SYMBOL TABLE

JMS2	0111
JMS3	0112
JMS4	0113
JMS5	0114
XRD1	0115
XRD2	0116
XRD3	0117
XRD4	0120
XRROR	0121
MEMADR	0122
FIRST1	0123
LAST1	0124
KCDF	0125
KCIF	0126
XHLT	0127
XRTN	0130
XFILE	0131
XPRER	0132
K7	0133
CHAR	0134
XHDR	0135
XPERR	0136
XPING	0137
M14	0140
M10	0141
K260	0142
K215	0143
K377	0144
K370	0145
K277	0146
XCRLF	0147
K1	0150
NXLOC	0151
XCALL	0152
EXIT	0153
LASTX	0154
BEGIN	0200
RSTRT1	0211
RTN1	0213
ALAW	0220
EXAM1	0241
EXAM2	0246
EXAM3	0253
EXAM4	0260
XSTSR	0277
YST1	0300
XIT1	0320
EXT1	0322
YST1C	0324
XIT1C	0341
EXT1C	0343
KXT1	0345
KXT1C	0346

## SYMBOL TABLE

TST2	0480
XIT2	0421
EXT2	0423
TST2C	0425
XIT2C	0443
EXT2C	0445
KXT2	0450
KXT2C	0451
TST3	0452
XIT3	0473
EXT3	0475
TST3C	0477
XIT3C	0515
EXT3C	0517
KXT3	0522
KXT3C	0523
TST4	0620
XIT4	0621
EXT4	0623
TST4C	0625
XIT4C	0643
KXT4	0645
KXT4C	0646
EXT4C	0647
W0011	0652
W1100	0667
W0110	0724
W1001	0721
RDALL	0736
RULOP	0753
CDON1	0760
RCHKA	1020
RLOPA	1011
RXIT	1020
KRXT	1035
KIK	1036
RCHK1	1237
RCHK1C	1054
RCHK2	1071
RCHK2C	1106
RCHK3	1123
RCHK3C	1140
RCHK4	1220
RCHK4C	1217
XCHKA	1236
XLOPA	1237
XLOPR	1240
XLOPC	1241
XLOPD	1242
XLOPE	1243
XCFL	1244
RD1	1245
CLOP1	1252

## SYMBOL TABLE

RD2	1322
CLOP2	1327
RD3	1400
CLOP3	1405
RD4	1455
CLOP4	1462
CKRINK	1600
CHANK	1607
SAVIF	1623
NXTBANK	1624
FIELD	1646
OVER	1706
WRLOP	1710
CFLD	1722
ERROR	2000
SW2	2033
SW1	2042
ERFXT	2046
EPRNT	2054
SW0	2111
SPING	2115
PROCTL	2126
POSITN	2131
PRFRR	2146
CRLF	2154
RAO	2163
GOOD	2164
OCADR	2165
LAST	2166
K207	2167
K240	2170
K212	2171
SETU1	2200
SLMTS	2207
OKAS	2256
ALOK	2273
KEYIN	2303
LEGAL	2314
QUERY	2344
XTLIM	2351
XLCAT	2352
K254	2353
LOCAT	2400
PLOCT	2412
PRGAM	2417
FLDN	2444
TLIMT	2446
PLIMT	2452
TSTL	2457
PHDR	2474
PFILD	2500
POCDR	2512
PG000	2524

## SYMBOL TABLE

FILD	2532
OTLDR	2541
GOND	2555
EXHDR	2563
BSPCE	2600
PBAD	2605
PTSTN	2617
TSTN	2626
RADD	2634
HALT	2641
XPHDR	2644
SETSR	2645
PSTSR	2652
WTR	2660
STSR	2670
STALL	2702
GENRAN	2716
RANTAD	2734
RANCON	2741
RANDEX	2742
RANTBL	2743
RANTAB	2754
TBLRAN	2755
K177	2756
CMOVE	3000
CKERR	3031
CVXT	3037
EQUAL	3053
SUR1	3066
STMV	3110
XSRCE	3123
XMVE	3124
XTMV	3125
NXTMV	3200
CHNXT	3203
CKNXT	3211
STNXT	3225
CKNT	3242
STNX	3247
SUR2	3255
NXTHI	3275
MVRK	3302
MOVE	3307
SOURCE	3327
DESTN	3331
CDF	6201
CIF	6202
RDF	6214
RIF	6224

MAINDEC EVALUATION REQUEST

After sufficient familiarization with the operation and documentation of this MAINDEC, please indicate your assessment of the following areas and return this form to Digital Equipment Corporation.

IDENTIFICATION: MAINDEC NO. \_\_\_\_\_ Program Title \_\_\_\_\_

USAGE: Used by: Field Service  Production  Other \_\_\_\_\_

Frequency of Usage: Daily  Weekly  Monthly

SUGGESTIONS FOR IMPROVEMENT

1. Are the program loading and operating instructions: clear? , incomplete? , difficult to follow?
2. Do the error reports and program documentation provide sufficient diagnostic information. in all cases? , in most cases? , in very few cases? . Suggestions for improvement:

3. Is the program effective in isolating malfunctions: in all cases? , in most cases? , in very few cases? . Would additional Scope loops or Switch Register control be helpful? \_\_\_\_\_  
Suggestions for improvement:

4. Does the program ever fail to detect malfunctions exposed by other software? \_\_\_\_\_  
Were Margins used? \_\_\_\_\_ Please describe malfunction in detail:

5. Does the program ever report non-existent malfunctions? \_\_\_\_\_  
Please indicate erroneous report and any pertinent operating conditions:

6. Does this MAINDEC ever expose malfunctions in the Central Processor or other peripheral units not detected by the appropriate MAINDEC? \_\_\_\_\_  
Please describe malfunction and MAINDEC(S) used:

7. Does the document provide a general understanding of the functional programming requirements of the system? Good , Fair , None . Would a general description of programming requirements increase the effectiveness of this MAINDEC? \_\_\_\_\_

Remarks:

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