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IDENTIFICATION

PRODUCT CODE: AC-E733I-MC
PRODUCT NAME: CXDRCIO DR11-C MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

DRC IS A IOMOD THAT EXERCISES UP TO SIXTEEN DR11-C'S. THE MODULE USES THE MAINTENANCE MODE TO CHECK DATA TRANSFERS TO AND FROM THE DR11-C. IT TRANSMITS AND RECEIVES 64 WORST-CASE 16 BIT WORDS AND ALSO TESTS THE ABILITY OF THE DR11C TO GENERATE BOTH A-REQUEST AND B-REQUEST INTERRUPTS. IT WILL DROP ITSELF IF RUN IN AN XXDP CHAIN WHEN THE MANUEL INTERVENTION BIT IS NOT SET.(BIT 0 IN LOC.52

2. REQUIREMENTS

HARDWARE: ONE DR11-C WITH A MAINTENANCE CABLE

STORAGE:: DRC REQUIRES:

1. DECIMAL WORDS: 308
2. OCTAL WORDS: 0464
3. OCTAL BYTES: 1150

3. PASS DEFINITION

ONE PASS OF THE DRC MODULE CONSISTS OF TRANSMITTING AND RECEIVING 64 WORDS AND GENERATING ONE A-REQUEST AND ONE B-REQUEST INTERRUPT.

4. EXECUTION TIME

ONE PASS OF DRC RUNNING ALONE ON A PDP11/03 PROCESSOR TAKES APPROXIMATELY THIRTY SECONDS

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 167770, VECTOR: 1, BR1: 5, DEVCNT: 1

REQUIRED PARAMETERS:

AT CONFIGURATION TIME USER MUST SUPPLY THE LOWEST VECTOR OF THE DR11-C'S.

6. DEVICE/OPTION SET-UP

CONNECT THE MAINTENANCE CABLE TO TIE OUTPUT BACK TO INPUT

7. MODULE OPERATION

TEST SEQUENCE:

- A. SET UP VECTORS AND ADDRESS POINTER
- B. OUTPUT TEST DATA TO OUTPUT BUFFER
- C. COMPARE OUTPUT BUFFER WITH TEST DATA-REPORT ANY DATA ERROR
- D. COMPARE INPUT BUFFER WITH TEST DATA-REPORT ANY DATA ERROR
- E. IF NOT 64 TRANSFERS, BUBBLE TEST DATA2 AND REPEAT B-D
- F. IF 64 TRANSFERS GENERATED AND TEST A/B INTERRUPTS
- G. IF NO INTERRUPT-DO NOT REPORT END PASS
- H. IF INTERRUPT-REPORT END PASS RESTART AT A

IF DEVICE FAILS TO GENERATE INTERRUPT A MESSAGE WILL BE PRINTED.

8. OPERATION OPTIONS

NONE

9. NON-STANDARD PRINTOUTS

"DEVICE FAILED TO INTERRUPT" MESSAGE IF INTERFACE FAILS TO INTERRUPT ON EITHER REQUEST BIT.


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241 000406 016722 177400      MOV     R1,(R2)+          ;SET B PRIORITY
242 000411 000764          BR      25                ;GO ADJUST ADDRESS POINTER
243 000414 016700 177606      SETUP1: MOV    SELECT,R0   ;COPY SELECTION PARAMETER
244 000420 016705 177362      MOV    ADDR,R5          ;RESET POINTER TO FIRST ADDRESS
245 000424 005725          TST    (R5)+           ;POINT TO DATA BUFFER WORD
246 000428 017767 177777 177602 NEXT:   MOV    R1,CBIT         ;FIRST RESTATE WANTS "1" INTO LSB
247 000434 005004          CLR    R4              ;FLAG REGISTER (DENOTES SWITCH FROM ALLOW PATTERN TO ALL
248 000436 016701 177566      MOV    ALLOW,R1        ;SET UP INITIAL DATA PATTERN
249 000442 016703 177566      MOV    BUBBLE,R3       ;SET UP ALTERNATE DATA PATTERN
250 000444 006200          RSR                    ;ISOLATE A SELECTION FLAG
251 000450 103404          RCS                    ;IF SELECTED GO CHECK DATA
252 000452 001532          BEQ   ENPS            ;IF NO MORE SELECTED, CALL FOR END OF PASS
253 000454 162705 000010      SETUP2: SUB   #10,R5    ;POINT TO THE NEXT DEVICE'S BUFFER WORD
254 000460 000762          BR     NEXT           ;GO PROCESS NEXT DEVICE
255
256 ;*****
257 ;CHECK DR11-C DATA TRANSFER CAPABILITY
258 SEND VERIFY AND CHECK (THROUGH MAINTAINANCE CABLE) ALTERNATING WORDS
259 OF 177777 AND 0-BUBBLING-THROUGH-1'S THEN ALTERNATING 0 AND 0-
260 BUBBLING-THROUGH-1'S. TOTAL OF 64 BIT-PATTERNS SENT AND TESTED IN
261 EACH PASS.
262 000462 010115      DRACT1: MOV   R1,(R5)    ;MOVE DATA TO OUTPUT BUFFER
263 000464 020115      CMP   R1,(R5)          ;CHECK DATA
264 000466 011413      BEQ   R1,2(P5)        ;BRANCH IF DATA GOOD
265 000470 010567 177410      MOV   R5,WASADR       ;BAD DATA ADDRESS
266 000474 010167 177406      MOV   R1,ASB          ;MOVE "SHOULD BE"
267 000500 011567 177404      MOV   #11567,AWAS     ;MOVE "WAS"
268 000504 104404 000000 000000 000000 000000 000000 000000
269 ;*****
270 000510 000417      OR    2S              ;NEXT DATA
271 000512 020165 000002 1S:   CMP   #1,2(P5)        ;CHECK RECEIVED DATA
272 000516 001414      BEQ   2S              ;BRANCH IF DATA GOOD
273 000520 010567 177360      MOV   R5,WASADR       ;BAD DATA ADDRESS
274 000524 020167 000002 177352 ADD   #2,WASADR        ;MAKE IT THE CORRECT ADDRESS
275 000532 010167 177350      MOV   #11567,AWAS     ;MOVE "SHOULD BE"
276 000536 016567 000002 177344      MOV   2(R5),AWAS      ;MOVE "WAS"
277 ;*****
278 000544 104404 000000 000000 000000 000000 000000 000000
279 ;*****
280 ;THIS SECTION FINDS AND LOADS INTO R1 THE NEXT PATTERN IN THE WORST-CASE
281 ;RUS TEST SEQUENCE, AND DECIDES WHEN TO END THE TEST.
282
283 284 000550 020167 177454      2S:   CMP   R1,ALLOW      ;SWITCH TO BUBBLE PATTERN
285 000554 001413      BEQ   4S              ;IF STRAIGHT I/O PATTERN
286 000556 020167 177450      CMP   R1,ALLOFF      ;IS NON
287 000560 001414      BEQ   4S              ;IS NON
288 000564 005704          TST   R4              ;IN USE
289 000566 001403      BEQ   3S              ;SWITCH TO STRAIGHT I/O: DECIDE WHICH
290 000570 016701 177436      MOV   ALLOFF,R1      ;FLAG RESET, SO IT'S ALL-ON PATTERN
291 000574 001414      BEQ   4S              ;FLAG SET, IT'S ALL-OFF PATTERN
292 000576 016701 177426      3S:   MOV   DRACT1,R1     ;REPEAT TEST WITH NEW WORD
293 000602 000727      BR    DRACT1         ;ALL-ON PATTERN
294 000604 010301 177424      4S:   MOV   R3,R1         ;REPEAT TEST SEQUENCE
295 000606 001414      BR    CBIT           ;PUT IN DATA BUFFER
296 000612 006103      ROL   R3              ;LOAD C BIT MSB OF CBIT (LAST MSB OF R3)
297 ;SHIFT 0 THROUGH WORD, TO LEFT

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297 000614 103403          BCS   5S              ;DO WE SET OR RESET NEXT CRIT?
298 000616 005067 177414      CLR   CBIT           ;C WAS 0, SO CRIT RESET
299 000622 000403          BR    6S              ;CONTINUE
300 000624 177777 177404 5S:   MOV   #1,CBIT        ;C WAS 1, SO CRIT SET
301 000632 026701 177376 6S:   CMP   BUBBLE,R1     ;IS 0 BACK TO LSB WHERE IT STARTED?
302 000636 001311      BNE   DRACT1         ;IF NOT, CONTINUE
303 000640 005704          TST   R4              ;DO HAS BUBBLED ALL AROUND. ARE WE FINISHED?
304 000642 100404          BMT   INTST         ;YES (FLAG SET); TEST INTERRUPTS
305 000644 005304          DEC   R4              ;NO
306 000646 016701 177360      MOV   ALLOFF,R1     ;REPEAT WHOLE THING, WITH ALL-OFF PATTERN INSTEAD
307 000652 000703          BR    DRACT1         ;RETURN TO TEST LOOP
308
309
310
311
312 ;CHECK INTERRUPTS ON DR11-C
313
314
315 000654 005015      INTST: CLR   (R5)        ;CLEAR OUTPUT & INPUT(VIA CABLE) BUFFERS
316 000656 012705      MOV   #2,R4          ;BREAK LOOP COUNTER
317 000662 005067 177336      CLR   #1,R4          ;FLAG: BOTH INTERRUPTS IN
318 000666 005045      CLR   -(R5)         ;CLEAR CONTROL REGISTER
319 000670 012715 000003      MOV   #3,(R5)        ;SET MAINTENANCE BITS
320 000674 010567 177174      MOV   R5,SVRS        ;SAVE R5 BEFORE INTERRUPT COMES
321 000700 052715 000040      BIS   #40,(R5)       ;ENABLE A INTERRUPT
322 000704
323 000706 104407 000000 000000 000000 000000 000000 000000
324 000710 104407 000000 000000 000000 000000 000000 000000
325 000714 005767 177304      BREAKS,BEGIN        ;TEMPORARY RETURN TO MONITOR...
326 000720 001005      BREAKS,BEGIN        ;THEN CONTINUE AT NEXT INSTRUCTION
327 000724 001367      TST   R4              ;IF FLAG IS CLEAR TO INTERRUPT YET
328 000726 001005      BNE   RESET         ;IF FLAG SET, GO START NEXT DEVICE
329 000728 005304          DEC   TIMER          ;REDUCE COUNT. IF NOT EXCEEDED, BREAK AGAIN
330 000734 104403 000000 000754 000000 000000 000000 000000
331 000736 005723      MSGNS,REGIN,HUNG   ;ASCII MESSAGE CALL WITH COMMON HEADER
332 000738 000646      TST   (R5)+         ;RESTORE R5 VALUE
333 000740 104413 000000 000000 000000 000000 000000 000000
334 000744 000167 177342      BR    SETUP2        ;GO PROCESS NEXT DEVICE
335
336
337
338 000750 001012 000000 000000 000000 000000 000000 000000
339 000754 177777      CHAIN: CHAINM       ;MONITOR SHALL TEST END OF PASS
340 000756 001114 177777      HUNG:  FAIL          ;
341 000758 177777
342
343 ;INPUT/OUTPUT SERVICE ROUTINES
344
345
346
347
348 000760 042777 000100 177106      DRACTA: BIC   #100,@SVRS5 ;DISABLE A INTERRUPT IMMEDIATELY, THROUGH SAVED R5
349 000766 005267 177232          INC   FLAG           ;RETURN TO BREAK LOOP
350 000772 000002          RTI                    ;
351 000774 042777 000040 177072      DRACTB: BIC   #40,@SVRS5 ;DISABLE B INTERRUPT IMMEDIATELY, THROUGH SAVED R5
352 001002 052777 000100 177064      BIS   #100,@SVRS5  ;ENABLE A INTERRUPT NOW

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353 001010 000002 RTI ;RETURN
354
355
356 001012 051104 020103 040503 CHAINM: .ASCIZ 'DRC CANNOT BE RUN IN THIS CHAIN, MANUAL INTERVENTION NOT ALLOWED.'
357 001020 047116 052117 041040
358 001026 020105 052522 020116
359 001034 047111 052040 044510
360 001042 020123 044103 044501
361 001050 026116 046440 047101
362 001056 040525 020114 047111
363 001064 042524 053122 047105
364 001072 044524 047117 047040
365 001100 052117 040440 046114
366 001106 053517 042105 000056
367 001114 042504 044526 042503 FAIL: .ASCIZ 'DEVICE FAILED TO INTERRUPT'
368 001122 043040 044501 042514
369 001130 020104 047524 044440
370 001136 052116 051105 052522
371 001144 052120 000000
372
373 .EVEN
      .END
  
```

```

ACSR 000102R 174#
ADDR 000006R 140#
ADDR22 = 001000 196#
ALLOFF 000232R 286# 290# 306#
ALLON 000230R 195# 248# 284# 292#
ASB 000106R 178# 265* 275*
ASTAT 000104R 176#
AWAS 000108R 179#
BEGIN 000000R 137# 266* 276*
BIT0 000001 192# 220# 221# 225# 268# 278# 323# 324# 329# 333#
BIT1 000002 192#
BIT10 002000 192#
BIT11 004000 192#
BIT12 010000 192#
BIT13 020000 192#
BIT14 040000 192#
BIT15 100000 192#
BIT2 000004 192#
BIT3 000010 192#
BIT4 000020 192#
BIT5 000040 192#
BIT6 000100 192#
BIT7 000200 192#
BIT8 000400 192#
BIT9 001000 192#
BREAKS = 104407 192# 323# 324#
BR1 000012R 142# 239# 241#
BR2 000013R 143#
BTODS = 104421 192#
BUBBLE 000234R 197# 249# 301#
CBIT 000235R 198# 246* 295* 298* 300*
CDATAS = 104412 192#
CHAIN 000750R 220# 338#
CHAINM 001012R 338# 356#
CONFIG 000056R 152#
CSRA 000100R 172#
DATCKS = 104411 192#
DATES = 104404 192# 268# 278#
DRACTA 000760R 238# 348#
DRACTB 000774R 240# 351#
DRACT1 000462R 251# 261# 291# 293# 302# 307#
DROP 000322R 224# 228#
DVID1 000014R 144# 222#
END1S = 104413 192# 333#
ENDS = 104410 192# 221# 225#
ENPS 000740R 252# 332#
ERRWP 000106R 177#
EXITS = 104400 192#
FAIL 001114R 340# 367#
FLAG 000224R 193# 317* 325# 349*
GTPAS = 104415 192#
GWBUPS = 104414 192#
HRDCNT 000044R 157#
HRDERS = 104405 192#
HRDPAS 000050R 159#
HUNG 000754R 329# 340#
  
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