

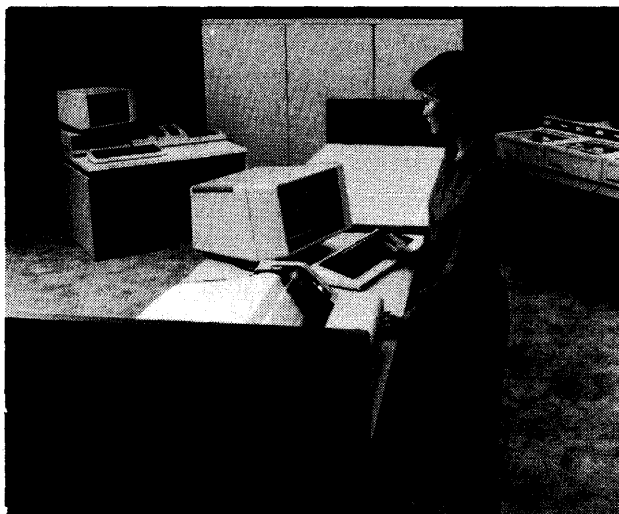
# Unisys (Sperry) 1100/70

## MANAGEMENT SUMMARY

**UPDATE:** *The greatest change to the 1100/70 Series during the past year was the acquisition of Sperry Corporation by Burroughs Corporation. In November 1986 the two companies announced that the new combined corporation would be called "Unisys" and would be headquartered in Blue Bell, PA, not in Detroit. As far as we could determine by press time, the names "Burroughs" and "Sperry" will no longer be used.*

Although the 1100/70 Series processors have not been enhanced for some time, the basic systems now carry lower price tags. The new prices are primarily a result of a change in packaging. The systems are now configured without a cache/disk control. In addition, all dual-processor and multiprocessor systems are now configured by adding appropriate upgrade options to a single-processor system. This move has eliminated the confusion between the 1100/70 dual-processor systems and the 1100/72 multiprocessor systems, both of which had two processors.

All 1100/70 systems are based on the same 116-nanosecond central processor. The 1100/71 Model B1 is the entry-level system, which seems to have the same specifications as the 1100/71 Model C1. According to the vendor, however, the Model C1 offers 1.2 times the performance of the Model B1. The 1100/71 Model E1 adds an 8KB buffer to the basic system and can also be upgraded to a dual-processor system. The 1100/71 Model H1 has a 32KB buffer and can be upgraded to a multiprocessor system with two, three, or four processors. The C1, E1, and H1 models use the standard 1100 instruction set, while the C2, E2, and H2 models use the Extended Instruction Set. ▶



*The 1100/70 Series of mainframe computers is available in an entry-level model that has a single central processor with 2MB of main memory expandable to 16MB (1100/71 B1) and a multiprocessor version that has four central processors, 64K bytes of buffer storage, and 4MB of main memory expandable to 32MB (1100/74 H2).*

The 1100/70 is a family of medium- to large-scale computer systems that features a multiple-microprocessor implementation of the 1100 Series architecture. Both uni-processor and multiprocessor configurations are available.

**MODELS:** 1100/71 B1, C1, C2, E1, E2, H1, and H2; 1100/72, 1100/73, and 1100/74 H1 and H2.

**CONFIGURATION:** From 1 to 4 CPUs, 512K to 8192K words (2MB to 32MB) of main memory, 1 to 4 IOUs, and 1 to 8 consoles.

**COMPETITION:** Control Data Cyber 180 Models 810 and 830, Honeywell DPS 8, IBM 4361 and 4381; and NCR V-8500, V-8600, and V-8800.

**PRICE:** Purchase prices for basic Processing Complexes range from \$139,875 to \$743,790.

## CHARACTERISTICS

**MANUFACTURER:** Unisys, P.O. Box 500, Blue Bell, PA 19424. Telephone (215) 542-4011. In Canada: Sperry, Inc., 55 City Center Drive, Mississauga, Ontario LR3 2Z1.

**MODELS:** 1100/71 B1, C1, C2, E1, E2, H1, and H2 single-processor systems; 1100/72 E1, E2, H1, and H2 dual-processor systems; 1100/73 and 1100/74 H1 and H2 multiprocessor systems.

## DATA FORMATS

**BASIC UNIT:** A 36-bit word. In main storage, each word location includes four additional parity bits.

**FIXED-POINT OPERANDS:** One 36-bit single precision word. Addition and subtraction can also be performed upon 2-word (72-bit) double precision operands and upon 18-bit half-words and 12-bit third-words; the leftmost bit holds the sign in each case. Moreover, partial words of 6, 9, 12, or 18 bits can be transferred into and out of the arithmetic and control registers. The C2, E2, and H2 processor models can perform decimal addition and subtraction operations on 9-bit bytes, packed 4 to a word.

**FLOATING-POINT OPERANDS:** One word, consisting of 27-bit-plus-sign fraction and 8-bit exponent for single precision; or two words, consisting of 60-bit-plus-sign fraction and 11-bit exponent for double precision. The range for single precision is from 10 to the 38th power to 10 to the minus 38th power with 8-digit precision; for double precision, the range is 10 to the 307th power to 10 to the minus 308th power with 18-digit precision. The sign is the most significant bit in single precision (bit 35) and double precision (bit 71). Negative floating-point numbers are represented by the ones complement of the entire corresponding positive floating-point number. Single precision negative exponents are biased by 128, while double precision negative exponents are biased by 1024.

**INSTRUCTIONS:** One word, consisting of 6-bit Function Code, 4-bit Partial-Word or Immediate-Operand Designa-▶

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TABLE 1. SYSTEM COMPARISON

MODEL	1100/71 B1	1100/71 C1, C2	1100/71 E1, E2	1100/71 H1, H2
<b>SYSTEM CHARACTERISTICS</b>				
Date announced	June 1983	June 1983	June 1983	June 1983
Date first delivered	June 1983	June 1983	June 1983	June 1983
Field upgradable to	Model C1	Models E1, E2, H1, H2	Models H1, H2	1100/72
Relative performance*	1.0	1.2 (C1), 1.6 (C2)	1.9 to 4.2	2.8 (H1), 3.3 (H2)
Number of processors	1	1	1 or 2	1
Cycle time, nanoseconds	116	116	116	116
Word size, bits	36	36	36	36
Operating systems	1100 OS	1100 OS	1100 OS	1100 OS
<b>MAIN MEMORY</b>				
Type	64K-bit CMOS	64K-bit CMOS	64K-bit CMOS	64K-bit CMOS
Minimum capacity, bytes	2M	2M	2M per CPU	2M
Maximum capacity, bytes	16M	16M	16M per CPU	16M
Increment size	2MB	2MB	2MB	2MB
Cycle time, nanoseconds	580	580	580	580
<b>BUFFER STORAGE</b>				
Minimum capacity, bytes	Not applicable	Not applicable	8K per CPU	32K
Maximum capacity, bytes	—	—	8K per CPU	32K
Increment size	—	—	—	—
<b>INPUT/OUTPUT CONTROL</b>				
Number of channels:				
Byte multiplexer	Not available	Not available	Not available	Not available
Block multiplexer	3 to 5	3 to 5	3 to 5	3 to 5
Word	4 to 12	4 to 12	4 to 12	4 to 12
Other	—	—	—	—

\*Relative performance ratings based on an 1100/71 Model B1 equaling 1.0.

➤ All single-processor 1100/70 Series systems support from 2 to 16 megabytes of main memory. Multiprocessor systems have from 4 to 32 megabytes of memory, which is shared by the processors.

Also available is the Attached Virtual Processor (AVP) for users upgrading from a Series 90/60 or 90/80 to an 1100/70 system. The AVP provides concurrent execution of applications written for the Series 90 Operating System and applications written for the 1100 Series Operating System.

The 1100/70 systems support a variety of peripheral devices, including the 8450, 8470, 8480, 8481, and 8490 Disk Subsystems; an optical disk subsystem; and the Uniservo 22, 24, 26, 28, 30, 32, 34, 36, 36 II, and 40 Magnetic Tape Subsystems. The new Uniservo 40 is an 18-track cartridge tape subsystem, similar to IBM's 3480. A number of impact printers with speeds of 760 to 2000 lines per minute are available, as is the 0777 Laser Printer. The 1100/70 also supports the Shared System, which is based on Britton-Lee's relational data base machine.

The 1100 Operating System is the standard operating system for all members of the 1100 Series, and furnishes comprehensive supervisory and control facilities for three distinct modes of multiprogrammed operation: batch, interactive, and realtime (or communications). Operating facilities include dynamic storage allocation, reentrancy, multiprocessing, dynamic reconfiguration, automatic recovery, multilevel prioritization, and system optimization.

Software facilities that operate under the control of the 1100 Operating System include processors for the Cobol, Fortran, Algol, Basic, Pascal, PL/1, APL, RPG, and Assem- ➤

➤ tor, 4-bit Control Register Designator, 4-bit Index Register Designator, 1-bit Index Modification Designator, 1-bit Indirect Address Designator, and 16-bit Address Field.

**INTERNAL CODE:** Unisys (Sperry) communications terminals and other I/O units can employ a 6-bit Fielddata code or standard ASCII code. The 1100 processors are not code-sensitive and can manipulate data in 6-bit, 9-bit, 12-bit, or 18-bit codes.

### MAIN MEMORY

Both internal and external main memory are available for the 1100/70 systems. The internal memory, located in the central processor cabinet, has a minimum capacity of 512K words (2MB). A free-standing Main Storage Unit (MSU), which is required with the 1100/73 and 1100/74 systems, has a minimum capacity of 1024K words (4MB).

Storage protection is provided through Bank Descriptor Registers (BDRs) that define the upper and lower boundaries of both the instruction areas and data areas that may be referenced by the currently active user program. Any attempt to reference an address beyond these limits causes a guard mode interrupt. The setting of a bit in the Designator Register determines whether the protection is against write operations; read, write, or jump operations; or whether no protection exists. In the first case, the operating system is in privileged mode. Under this mode, privileged programs such as realtime programs or executive controlled subroutines may enter nonalterable (reentrant) subroutines for reading or jumping only. In the second case, the operating system is in user mode. In the third case, the BDRs are loaded but ignored since the operating system is in OPEN mode. Registers BDR0 and BDR1 correspond to I-bank (instruction word) address ranges and Registers BDR2 and BDR3 correspond to D-bank (data word or operand) address ranges.

**STORAGE TYPE:** Complementary metal oxide (CMOS) using 64K-bit chips. ➤

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TABLE 1. SYSTEM COMPARISON (Continued)

MODEL	1100/72 H1, H2	1100/73 H1, H2	1100/74 H1, H2
<b>SYSTEM CHARACTERISTICS</b>			
Date announced	June 1983	January 1984	January 1984
Date first delivered	June 1983	March 1984	March 1984
Field upgradable to	1100/73 H1, H2	1100/74 H1, H2	—
Relative performance*	5.2 (H1), 6.4 (H2)	7.6 (H1), 9.3 (H2)	9.9 (H1), 11.9 (H2)
Number of processors	2	3	4
Cycle time, nanoseconds	116	116	116
Word size, bits	36	36	36
Operating systems	1100 OS	1100 OS	1100 OS
<b>MAIN MEMORY</b>			
Type	64K-bit CMOS	64K-bit CMOS	64K-bit CMOS
Minimum capacity, bytes	4M	4M	4M
Maximum capacity, bytes	32M	32M	32M
Increment size	2MB	2MB to 4MB	2MB to 4MB
Cycle time, nanoseconds	580	580	580
<b>BUFFER STORAGE</b>			
Minimum capacity, bytes	32K per CPU	32K per CPU	32K per CPU
Maximum capacity, bytes	32K per CPU	32K per CPU	32K per CPU
Increment size	—	—	—
<b>INPUT/OUTPUT CONTROL</b>			
Number of channels:			
Byte multiplexer	Not available	Not available	Not available
Block multiplexer	3 to 5	3 to 5	3 to 5
Word	4 to 12	4 to 12	4 to 12
Other	—	—	—

\*Relative performance ratings based on an 1100/71 Model B1 equaling 1.0.

▷ bly languages, plus a variety of utility routines and applications packages.

Within the realm of data communications, four Distributed Communications Processors (DCPs) are available to serve as front-end processors, network processors, or remote concentrators. The DCPs support UDLC, bisynchronous, synchronous, and asynchronous transmission. Depending on the model, the DCPs can accommodate from 8 to 1,016 communications lines.

The Distributed Communication Architecture (DCA) can support a broad range of host processors and terminal attachments, including other manufacturers' equipment. According to the vendor, continued compatibility of present and future products will be ensured by specifying interfaces and functions of all components and providing guidelines for the building of communications networks.

### COMPETITIVE POSITION

Traditionally, Sperry Corporation concentrated much of its marketing effort on two key computer markets—manufacturing and energy. Its new partner, Burroughs, has always been strong in the banking and financial markets. Now that the two companies have joined to form Unisys, it will be interesting to see what strategy they adopt. They may continue to look to financial institutions for future Burroughs A Series customers, and aim the Sperry 1100 Series at manufacturing and scientific markets. On the other hand, they may find that the merger offers new opportunities for both product lines.

Only time will tell whether the decision to drop the Burroughs and Sperry names was a wise one. Both companies ▷

▶ **CAPACITY:** See Table 1 for the capacities of individual models.

**CYCLE TIME:** Read/write cycle time of 580 nanoseconds; 625 nanosecond access to corrected read data; and 928-nanosecond partial write cycle. Single and partial word writes are available. In dual processor and multiprocessor systems, storage modules may be interleaved under control of the System Support Processor (SSP) software.

**CHECKING:** The Main Storage Unit (MSU) contains circuitry for single-bit error detection and correction and detection of double-bit errors. Multiples of double-bit errors and some odd multiples of double-bit errors are also detected. Memory errors are detected using a 7-bit hamming code generated for all read and write operations.

A parity bit with each half-word is checked whenever storage is referenced for I/O transfers via the two IOU interfaces. The MSU also detects single-bit address errors and out of bounds addresses.

**RESERVED STORAGE:** The low end of memory is reserved for storing the processor state during interrupts. The processor state consists of the program status, addressing status, and interrupt status. Interrupt routines and the general register stack are also located in the low end of memory.

### CENTRAL PROCESSORS

All models of the 1100/70 series employ the same basic 116-nanosecond CPU, which is a multifunctional implementation of the 1100 Series architecture. The processor contains two microexecution units that concurrently execute parts of the same microinstruction (see ARM below). Complete execution of every microinstruction requires four cycles. Speed is enhanced by overlapping execution of microinstructions. To further increase performance, microprocessor functions are generated using a phantom branching technique in which one of two functions is selected for execution in each microprocessor, one cycle after microaddress selection. ▶

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► had established a reputation in the computer industry. They are now conducting a massive advertising campaign to bring the name "Unisys" to the attention of the public. The choice of name has left many industry observers yawning, but the name "IBM" probably wouldn't excite them, either, if they'd never heard it before.

Be that as it may, the 1100/70 systems, like other members of the word-oriented 1100 Series, are well suited to general purpose scientific and business data processing. According to the vendor, the 1100/71 B1 is equivalent in performance to the IBM 4361 Model Group 5, and the 1100/72 H2 with the Extended Instruction Set option can compete successfully against the dual-processor IBM 4381 Model Q3.

As a contender in the scientific and engineering realm, Control Data Corporation poses a realistic challenge to the 1100 Series. Control Data's Cyber 180 Model 810A and 830A uniprocessors offer more main memory (8MB to 64MB) than the 1100/70 systems, and also have a faster cycle time (50 nanoseconds). However, the difference in cycle time can be minimized through the use of the high-speed buffer storage available on the 1100/70. The CDC Cyber 180 Model 830A dual processor competes with the dual-processor versions of the 1100/70 E and H Models. Again, Control Data offers more main memory. What's more, CDC offers its basic Model 830A dual-processor complex for about \$120,000 less than the comparable 1100/70 E model.

In the area of scientific/engineering applications, CDC has the edge through the use of its CyberPlus Multi-Parallel Processor, which can be attached to any CDC 800 Series computer. The CyberPlus offers increased throughput by use of a ring architecture. While the top-of-the-line 1100/90 systems can increase throughput through the use of the Integrated Scientific Processor, there is no comparable product available for the 1100/70 Series.

General-purpose business computer systems that compete with the 1100/70 systems include the Honeywell DPS 8 and the NCR V-8500, V-8600, and V-8800 Systems.

### ADVANTAGES AND RESTRICTIONS

Users have a growth path both within the 1100/70 family and throughout the 1100 Series product line. Beginning with the entry-level 1100/71 Model B1, a user can increase performance by a factor of 12 by migrating to the high-end 1100/74 Model H2. As the user's processing needs expand, a migration to the top-of-the-line 1100/94 offers a 55-fold performance increase over the 1100/71 B1.

All 1100 Series systems use the proven 1100 Operating System, so a high degree of program and data compatibility is maintained throughout the product line. This compatibility was extended downward with the introduction of the 2200 Series "Midframe", which uses 1100 Series architecture and the 1100 Series Operating System. The vendor has also announced a number of software products that connect 1100 Series and 2200 Series systems with the 5000 Series and 7000 Series minicomputer systems. ►

► The concept of *availability, reliability, and maintainability (ARM)* was an important consideration in the design of the 1100/70 series processor, according to the vendor. To implement ARM, duplicates were provided for the microinstruction units, executing the same function on the same data in the duplicate unit and comparing the results at the end of each cycle. Similarly, the shifter, logic function section, and control store address generator are also duplicated. The 1100/70 also includes a hardware instruction retry mechanism that allows the system to recover from most transient faults, transparent to the operating environment.

The hardware monitor feature enables an 1100/70 series processor to collect system profile performance data on hardware and software. Sampling of data can be initiated by software or operator request. The signals are sampled every 475 microseconds and collected by the System Support Processor (SSP) every 25 seconds for storage in the system log for later report generation.

The 1100/70 series processor also includes an interprocessor interrupt interface that allows operational control by the operating system to permit a CPU to interrupt another CPU or to be interrupted by another CPU in a multiprocessor environment.

The 1100/70 series processor contains a *General Register Stack (GRS)* that consists of 36-bit integrated circuit registers with a basic cycle time of 116 nanoseconds. The GRS includes 128 program addressable control registers with some overlap of function and some areas guard mode protected (e.g., the executive system of the operating system). The GRS includes an unassigned nonindexing register; 15 index registers; 4 registers that can be used for either indexing or accumulation; 16 accumulators; and the processor state control registers consisting of 2 pointer registers, the Executive Bank Descriptor Table Pointer and the User Bank Descriptor Table Pointer. There are 11 interrupt status words, including the Immediate Storage Check Designator Register and Guard Mode Designator Register, a quantum timer, Bank Descriptor Table Indexes, and jump history stack; 16 special registers, including the Realtime Clock (guard mode protected), Repeat Count Register, Mask Register, and user registers R3 through R15; 16 special guard mode protected executive registers, such as Executive Registers R0 and R3 through R15; Repeat Count Register and Mask Register; a guard mode protected (executive) nonindexing register; 15 executive index registers; 4 executive registers for indexing or accumulation; and 16 executive accumulators.

The Realtime Clock is initially loaded by the program and decremented once every 200 microseconds. The Repeat Count Register controls repeated operations such as block transfer and search instructions. The Mask Register is used with the search command in determining which portions of words are to be compared in repeated masked search operations. The Jump History Stack holds the recent 24-bit absolute addresses of jump instructions. The Quantum Timer, once loaded with an initial value, is decremented every 116 nanoseconds of actual CPU use, provided that a bit in the Designator Register is set. The Bank Descriptor Registers are described in the section entitled Storage Protection. The Designator Register generally determines functioning characteristics of the CPU.

The Breakpoint Register is employed with the address breakpoint mechanism. It allows an interrupt to be initiated when an equality comparison is made between the absolute address in the register or an operand address. The Breakpoint Register is operational on all instruction addresses, read/write references to main memory, and I/O references to main memory.

The 1100/70 series has an *address range* of 16 million words. Both indirect and direct addressing are possible. ►

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TABLE 2. MASS STORAGE

MODEL	FH-432/FH-1782 Drums	8450	8470	8480	8481	8490
Cabinets per subsystem	1 to 8	1 to 32	1 to 32	1 to 8	1 to 4	1 to 4
Disk packs/HDA's per cabinet	—	1 HDA	1 HDA	4 HDAs	4 HDAs	4 to 8 HDAs
Capacity*	1.17MB/9.4MB	243MB	403MB	1.6GB	2.5GB	2.5GB to 5.1GB
Tracks/segments per drive unit	384/1,536	16,800	20,160	80,000	67,328	—
Average seek time, msec.	—	14.7	14.7	14.7	16	18
Average access time, msec.	4.3/17.0	23	23	23	29.9	26.3
Average rotational delay, msec.	—	8.3	8.3	8.3	13.9	8.3
Data transfer rate	1.08M bytes/sec.	1.26M bytes/sec.	2.09M bytes/sec.	2.09M bytes/sec.	1.8M bytes/sec.	1.8M bytes/sec.
Controller model	5012	5040	5056	5056	5057	5090
Comments	Models FH-432 and FH-1782 can be intermixed on the same system	Can be configured as part of a cache/disk subsystem	Can be configured as part of a cache/disk subsystem	Can be configured as part of a cache/disk subsystem	Can be configured as part of a cache/disk subsystem	Can be configured as part of a cache/disk subsystem

\*Capacity based on 112-word records

➤ Availability, reliability, and maintainability (ARM) features include duplicate microexecution units that allow parallel execution of instructions, as well as duplicates of the shifter, logic function section, and control store address generator. Also included is an instruction retry mechanism that allows the system to recover from most transient faults, transparent to the operating system. TRACE, the Total Remote Assistance Center, provides remote hardware maintenance via telephone lines.

Maintenance and support have always been Sperry strong points. With the third-party maintenance service offered under Customcare, users with systems consisting of multiple vendors' equipment can avail themselves of the cost and continuity advantages of dealing with one service supplier.

#### USER REACTION

Datapro's 1986 survey of general-purpose computer users yielded 57 responses from Sperry 1100/70 Series users. The survey respondents represented a variety of industries, although nearly one-third of the users were in manufacturing.

More than 84 percent of the respondents had converted from another system to the 1100/70 system. Fifty-six percent of those users had converted from another Sperry system, primarily from 90 Series systems. The other users had converted from an IBM, Digital Equipment, Burroughs, or Honeywell system.

Memory capacity on the installed systems ranged from 2MB to 32MB, with 33 percent reporting a capacity of 8MB to 16MB. Disk storage varied from 1.2GB to over 10.0GB with the majority of respondents using between 1.2GB and 4.8GB. More than 50 percent of the survey respondents were using more than 60 local workstations, while nearly 30 percent had installed more than 60 remote workstations.

The majority of users surveyed indicated that they were planning expansions to their present systems in 1986. These expansions included the acquisition of additional hardware (61 percent), additional software from the manu- ➤

➤ Indirect addressing is possible to any desired number of levels, with full indexing capabilities at each level. Operand addresses can be modified by the contents of any of 19 index registers. If desired, the contents of the index register can be automatically incremented by any specific value each time the register is referenced.

The 1100/70 instruction set is almost identical with that of the other 1100 Series systems in order to maintain compatibility. To utilize the full capabilities of the 1100/70 system, additional privileged instructions are included, and an optional Extended Instruction Set (EIS) is also available.

The 1100/70 series has 161 standard instructions. Most instructions specify the address of one operand in main storage and one of the accumulators. Complete binary arithmetic facilities are provided for single precision fixed-point and both single and double precision floating-point operands. Addition and subtraction can also be performed on double precision fixed-point operands and on 18-bit half-words and 12-bit third-words. Also included are extensive facilities for testing, shifting, searching, and logical operations.

EIS includes bit string instructions for moving, comparing, and translating character or byte fields; decimal arithmetic and edit instructions; and instructions for converting between ASCII, decimal, and binary notation. According to the vendor, gains realized by the use of EIS can be expected to be in the range of 25 to 35 percent for heavy Cobol/DMS batch type environments.

There are 24 priority level interrupts available in the 1100/70 series processors. Priority levels 0 through 10 are internal interrupts, which can neither be locked out nor deferred. The remainder are deferrable interrupts that can be locked out. All external interrupts are presented to each CPU in the system. Therefore, an interlocked synchronization mechanism is provided to ensure that only one CPU actually accepts the interrupt request.

In addition to the CPU and main memory, the 1100/70 Model E and H processing complexes include the Storage Interface Unit (SIU), which houses the buffer storage. Buffer capacity is 2,048 words (8K bytes) per CPU in E models and 8,192 words (32K bytes) per CPU in H models. A read request results in a serial retrieval of a four-word block from the MSU—the requested word and three adjacent words. Subsequent read references to the same or adjacent words in the block are presented at SIU speed with no further reference to the MSU required. The words in the buffer are divided into 512 sets. Each set contains four 4-word blocks. The SIU employs a Paired Least Recently Used (PLRU) algorithm to control aging and replacement of ➤

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► **facturer (49 percent), proprietary software (42 percent), and expanded data communications facilities (65 percent).**

As part of the survey, users were asked to rate their Sperry equipment from excellent to poor in several categories. A weighted average was then calculated based on the total number of responses. A summary of these ratings is included in the following table.

	Excellent	Good	Fair	Poor	WA*
Ease of operation	17	30	10	0	3.12
Reliability of mainframe	31	24	2	0	3.51
Reliability of peripherals	21	31	4	1	3.26
Maintenance service:					
Responsiveness	31	17	9	0	3.39
Effectiveness	23	22	12	0	3.19
Technical support:					
Troubleshooting	10	27	17	3	2.77
Education	4	29	19	5	2.56
Documentation	3	22	26	6	2.39
Manufacturers software:					
Operating system	21	30	5	0	3.29
Compiler & assemblers	23	31	3	0	3.35
Application programs	6	31	10	4	2.76
Ease of programming	9	40	7	0	3.04
Ease of conversion	4	25	15	11	2.40
Overall satisfaction	9	42	6	0	3.05

\*Weighted Average based on a scale of 4.0 for Excellent.

The 1100/70 systems earned slightly higher marks for the reliability of the mainframe and peripherals in 1986 than in 1985, but slightly lower marks for overall satisfaction and maintenance service. In other questions, the respondents to our 1986 survey gave Sperry high marks for the relative ease of expanding their systems and the timeliness of the installation of the hardware and software. Sperry scored lower on cross system terminal and peripheral compatibility, power/energy utilization, and software support.

We also asked the users if they would recommend their systems to other users. Nearly 88 percent of the 1100/70 users said that they would, and 7 percent were undecided. □

► **data blocks within each set. In case of buffer malfunction, the affected blocks are automatically bypassed.**

**SPECIAL FEATURES:** The *System Support Processor (SSP)* provides partitioning, system control, maintenance, and console management functions. The SSP is a standalone desk-sized unit that interfaces, via the Support Controller, to the CPU complex and its component parts, including the CPU, IOU, MSU, and SIU. A basic configuration for the SSP includes CRT/keyboard/printer console, 128K bytes of addressable storage, a console interface, diskette drive interface, remote maintenance interface, and central complex interface.

The partitioning function provides the ability to assign individual central complex units of a system to either one of two independent smaller systems, or to isolate a unit from either application for off-line concurrent maintenance. Partitioning is supported via partitioning panel displays. The SSP also defines special system protection modes such as realtime and maintenance modes.

The partitioning function also indicates the operational status of each central complex unit. These status conditions

are available to system software for configuration control. The ability to control the partitioning of subsystems is also provided.

Two partitioning features are built into the IOU. One feature controls shared peripheral interface units on word channels, and the other controls the byte channel transfer switch for subsystems connected to a block multiplexer channel. The SSP provides control of system functions such as clocks and timers, stop jump control, initial load path, and auto recovery through system operator panel displays on the console. An optional System Performance Monitor is also under SSP control.

The SSP acts as a primary maintenance tool through functions such as control storage loading, fault corrections, scan/set data comparisons, error logging, and a remote maintenance capability. The SSP also acts as the communications link between the 1100/70 and the system console(s).

The *Attached Virtual Processor (AVP)* is a special-purpose processor that provides a migration path from the byte-oriented Series 90/60 and 90/80 systems to the word-oriented 1100/70 systems. The AVP can be attached to any processor model in the 1100/70 family except Model B1. Performance is comparable to the 90/80-3. The AVP provides concurrent execution of applications written for the VS/9 operating system and applications written for the 1100 Series operating system.

The AVP incorporates system features found in both Series 90 and 1100/70 processors. On the VS/9 side, the hardware includes a CPU with a logic bus structure and microcode control similar to the 90/80 family. In addition, the reliability features of the 90/80, such as parity checking, control store, and duplicate adders, have been maintained and applied to the AVP. The 1100/70 System Support Processor (SSP) is also included, and provides partitioning, system control, maintenance, and console management functions.

The system's main memory ranges from 512K words (2 megabytes) to 1024K words (4 megabytes). An 8K word (32K-byte) cache unit provides buffer storage of instructions and data between the 1100 Main Storage Unit (MSU) and the AVP CPU. The cache is designed to improve CPU performance while reducing the number of requests into the MSU.

I/O operations are handled by the AVP using the Attached Processor Control Software (APCS). The AVP can accommodate VS/9 random access data files via direct, logical, or local attachment. The Direct Attachment feature permits disk subsystems of the Series 90 systems to attach directly to the 1100/70 block multiplexer channel. Files written for VS/9 can be run on the AVP without change. The Logical Attachment facility permits the VS/9 user to utilize current technology random access devices, such as the 8470 disk drive, that are not included in standard VS/9 configurations. The third access mode, Local Attachment, permits devices available for VS/9 operation, but not for OS 1100, to be run on the AVP. The devices are attached to an optional block multiplexer channel.

**PHYSICAL SPECIFICATIONS:** The 1100/70 central complex cabinet is 30 inches deep, 78 inches wide, and 64 inches high. The cabinet weighs approximately 1,500 pounds. Power requirements for the basic CPU complex cabinet is 7kVA, 60 Hz. A motor alternator is not required. Cooling required by the CPU complex is less than 1,500 cubic feet per minute forced air, supplied from room air or false floor. Heat dissipation is reportedly less than 24,000 Btu per hour. Recommended temperature for the typical system is 75 degrees Fahrenheit with a relative humidity of 50 percent noncondensing. ►

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TABLE 3. INPUT/OUTPUT UNITS

Magnetic Tape Units	Number of Tracks	Recording Density, Bits/Inch	Encoding	Tape Speed, Inches/Sec.	Transfer Rate, Bytes/Sec.
Uniservo 22	9	800	NRZI	75	60,000
	9	1600	PE	75	120,000
Uniservo 24	9	800	NRZI	125	100,000
	9	1600	PE	125	200,000
Uniservo 26	9	1600	PE	75	120,000
	9	6250	GCR	75	470,000
Uniservo 28	9	1600	PE	125	200,000
	9	6250	GCR	125	780,000
Uniservo 30	7	200	NRZI	200	40,000
	7	556	NRZI	200	111,000
	7	800	NRZI	200	160,000
	9	800	NRZI	200	160,000
	9	1600	PE	200	320,000
Uniservo 32	9	1600	PE	75	120,000
	9	6250	GCR	75	470,000
Uniservo 34	9	1600	PE	125	200,000
	9	6250	GCR	125	780,000
Uniservo 36	9	1600	PE	200	320,000
	9	6250	GCR	200	1,250,000
Uniservo 40	18	37,871	—	79	1,500,000
Printers	Printing Speed	Print Positions	Horizontal Spacing, Chars./Inch	Vertical Spacing, Lines/Inch	Form Size, Inches
0770-00	800 lpm	132	10	6 or 8	3.5 to 22 wide, 24 long
0770-02	1400 lpm	132	10	6 or 8	Same
0770-04	2000 lpm	132	10	6 or 8	Same
0770-06	2000 lpm	136	10	6 or 8	4 to 20 wide, 8 to 22 long
0776-00	760 lpm	136	10	6 or 8	4.0 to 18.75 wide, 24 long
0776-02	900 lpm	136	10	6 or 8	Same
0776-04	1200 lpm	136	10	6 or 8	Same
0777 Laser Printer	10,500 to 21,000 lpm	136, 164, or 204	10, 12, or 15	6, 8, or 12	6.5 to 15.8 wide, 7 to 14 long

## ► CONFIGURATION RULES

The 1100/70 systems are available in seven basic processor models. The entry-level B model and the C models do not include buffer storage. The E models include 2K words (8K bytes) of buffer storage, while the H models include 8K words (32K bytes) of buffer storage. Models B1, C1, E1, and H1 include the standard 1100 Series instruction set. Models C2, E2, and H2 include the Extended Instruction Set (EIS), which is designed to enhance the performance of high-level languages and system software.

All 1100/71 single-processor systems include the central processor, 512K words (2 megabytes) of main memory, one Input/Output Unit (IOU), one System Support Processor (SSP), and a system console with printer. Main memory is expandable in 512K-word increments to a maximum of 4096K words (16 megabytes). A second IOU and up to three auxiliary consoles can be added.

System upgrades are achieved by adding appropriate components to the basic 1100/71 unit processor complex. The models C1, E1, and H1 can be upgraded to models C2, E2, or H2 with the addition of EIS. In addition, the B model can be upgraded to C models, the C models can be upgraded to E models, and the E models can be upgraded to H models.

The 1100/71 Model E1 and E2 can be upgraded to 1100/72 dual-processor systems by adding a second CPU with 512K words of main memory. Main memory is expandable in 512K-word increments to a maximum of 4096K words. A second IOU and up to five auxiliary consoles can be added.

The 1100/72 Model H1 and H2 are fully redundant multi-processor systems that include two central processors, 1024K words of main memory, two IOUs, two SSPs, two system consoles with printers, and a maintenance console. Each processor can have up to 4096K words of main memory for a total system capacity of 8192K words (32 megabytes). ►

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► Two additional IOUs can be added, one per processor. The 1100/72 systems support up to six auxiliary consoles.

The 1100/73 and 1100/74 multiprocessor systems consist of additional central processor complexes that can be added to the 1100/72 H Models. The 1100/73 system comprises three Model H1 or H2 processors with a total of 24K words of buffer storage, two or three IOUs, and two SSPs. The 1100/74 processor complex includes four Model H1 or H2 CPUs with a total of 32K words of buffer storage, two or four IOUs, and two SSPs. Both systems support 1024K words to 8192K words of main memory. In addition, both systems support up to six auxiliary consoles. A multiprocessor system requires two consoles and one maintenance CRT/keyboard. The maintenance CRT/keyboard connects to one of the auxiliary console interfaces on each System Support Processor.

Minimum peripheral equipment required to complete an 1100/70 series system includes an 0776 Printer Subsystem, a disk subsystem with one 5057 Control Unit and one disk drive, a magnetic tape subsystem with one control unit and two Uniservo 22 or 24 Magnetic Tape Units, and a Telcon system with one DCP.

As an alternative, a minimum peripheral system would include an 0770 Printer Subsystem, a disk subsystem with one control unit and one disk drive, and a magnetic tape subsystem with one control unit and two Uniservo 30, 32, 34, or 36 Magnetic Tape Units.

No maximum peripheral restrictions are placed on the 1100/70 series configurations other than channel considerations (see Input/Output Control). However, some peripheral subsystems used on earlier Sperry 1100 Series systems can only be configured with present software support. The company will not enhance any of the existing 1100 Series software for these subsystems.

### INPUT/OUTPUT CONTROL

All models in the 1100/70 family contain one Input/Output Unit (IOU). The IOU consists of a central control module (CCM) and up to six channel modules. The CCM provides independent control paths to up to two CPUs and up to two SSPs and data paths to/from up to two MSUs and the channel modules. The CCM processes all I/O instructions, passes control information to the channel modules, controls main storage requests, updates control words and format status words, and generates all interrupt requests.

Each channel module consists of either one block multiplexer channel or four word channels. The basic IOU contains one word channel module and one block multiplexer channel. A fully configured IOU can consist of 2 block multiplexer channels and 12 word channels, 3 block multiplexer channels and 8 word channels, or 5 block multiplexer channels and 4 word channels.

Individual word channels operate in one of three modes: 36-bit Internally Specified Index (ISI), 18-bit Externally Specified Index (ESI), or 9-bit ESI. The ISI mode word channel has one subchannel assignment. The ESI mode word channel has up to 64 subchannels, while the block multiplexer channel has up to 128 subchannels for concurrent operation. Each IOU can support up to 1,024 subchannels. One subchannel is reserved for the status table, leaving 1,023 for use by the system.

The maximum block multiplexer channel data rate is 1.67 million bytes per second. The aggregate output data rate for a word channel module (four channels) operating in ISI mode is 2.76 million bytes per second. The aggregate input data rate for a word channel module is 3.2 million bytes per second in ISI mode.

One input or output operation on each I/O channel can occur simultaneously with computation in each processor. Moreover, the Externally Specified Index (ESI) mode permits multiple remote communications devices to transmit data to and from main storage in multiplexed fashion over a single I/O channel. All installed processors and IOUs can operate simultaneously and independently, with interference occurring only when two or more of these units simultaneously attempt to access the same storage module.

The microinstruction execution units utilize overlap execution techniques, with one new microinstruction starting each cycle.

### MASS STORAGE

Disk subsystems supported on the 1100/70 series are listed in Table 2.

The *Cache/Disk System* is available in three versions: the original 8450/8470/8480 Cache/Disk System, the 8481 Cache/Disk System, and the 8490 Cache/Disk System. The first two are essentially the same, except for the disk drives supported.

The Cache/Disk Systems are hierarchical mass storage systems that provide a level of memory between the 1100/70 processor and the appropriate disk drives. The 8450/8470/8480 Cache/Disk System consists of one or two 5057-XX Cache/Disk or Solidstate Disk (SSD) Processors, up to four 7053 Storage Units, and up to sixteen 8450, 8470, or 8480 disk units. The 8481 Cache/Disk System consists of two 5057-XX Cache/Disk or SSD Processors, up to four 7053 Storage Units, and up to four 8481 disk units.

The 5057 Cache/Disk Processor controls all data access functions, including indexing, searching, buffering, storage management, staging and destaging of data to and from disk, and error recovery. The 7053 Storage Unit contains 917,504 words (4 megabytes) of semiconductor memory. It can be configured as cache memory, as a solidstate disk, or both.

In Cache/Disk mode, data is automatically transferred from the disk to the 7053 Storage Unit. The host computer accesses data as if it were stored on the disks. A separate indexing feature, the Segment Descriptor Table (SDT), is required in one of the 7053 units. The SDT contains a list of disk addresses that point to cache storage areas containing duplicates of data in recently referenced disk space. When an index find occurs, data transmission between the cache and the host CPU begins in about one millisecond. If an index miss occurs, the 5057 processor issues a seek to disk and disconnects for other activity.

In Solidstate Disk (SSD) mode, the 5057 SSD Processor manages storage consisting of up to four 7053 cache storage units to be used exclusively in the solidstate disk mode. No caching or disk attachment is provided. In SSD mode, the 7053 is directly addressed by the host processor. The access time in this mode is approximately 0.2 millisecond. In both Cache/Disk and SSD modes, the response time is improved by eliminating the seek and latency time required by the disk drives.

The 8490 Cache/Disk System performs in a similar manner to the systems described above, but it does not use the 7053 storage unit. The basic 8490 Cache/Disk System is available with two 5090 control units, eight disk modules providing 5 gigabytes of storage, and a cache/disk capability. An SSD unit and controller and a standard disk subsystem are also available.

Also offered is the 5071/8652 *Optical Disk System*, which includes a 2.6-gigabyte optical disk unit and a controller. An ►



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TABLE 4. TERMINALS

MODEL	UTS 10	UTS 20	UTS 40
<b>DISPLAY PARAMETERS</b>			
Max. chars./screen	1,920	1,920	1,920
Screen size (lines x chars.)	24 X 80	24 X 80	24 X 80
Symbol formation	7 X 11 dot matrix	7 X 11 dot matrix	7 X 11 dot matrix
Character phosphor	P31 green	P31 green	P31 green
Total colors/no. simult. displayed	—	—	—
<b>KEYBOARD PARAMETERS</b>			
Style	Typewriter	Typewriter	Typewriter
Character/code set	128 ASCII	96 ASCII	96 ASCII
Detachable	Standard	Standard	Standard
Program function keys	12 std.	22 std.	22 std.
<b>OTHER FEATURES</b>			
Buffer capacity	1,920 characters	4,000 characters	4,000 characters
Tilt/swivel	Optional	Optional	Optional
Graphics capability	—	—	—
<b>TERMINAL INTERFACE</b>	EIA RS-232-C, CCITT V.24, Current Loop (using TTY protocol)	EIA RS-232-C, CCITT V.24, (using UNISCOPE protocol)	EIA RS-232-C, CCITT V.24, (using UNISCOPE protocol)

► optical disk library provides an automatic disk changer (jukebox style) that supports up to 32 disk cartridges. The optical disk system provides automated electronic storage of millions of documents.

The *Shared System* is based on Britton-Lee's relational data base machine. The system is designed to offload the 1100 Series mainframe and provide faster access to information. The basic Shared System includes a data base processor with 2 megabytes of memory, an 1100 host interface, a data base administrator's workstation, and 600 megabytes of disk storage. The system can be expanded to include 6 megabytes of memory, 2 host interfaces, 15 workstations, and 5 gigabytes of disk storage. Interfaces are available to permit the 1100 Series processor to communicate with an IBM mainframe or with Unisys (Sperry) personal computers.

#### INPUT/OUTPUT UNITS

For magnetic tape units and printers supported by the 1100/70 systems, please refer to Table 3.

#### TERMINALS

See Table 4 for terminals that can be connected to the 1100/70 systems.

#### COMMUNICATIONS

DCP/Telcon is an intelligent communications system that provides basic hardware, software, and peripherals for users with large communications networks. The system can operate as a front-end processor for 1100/70 and other 1100 Series host processors, as a network nodal processor, or as a remote concentrator. As such, it provides networks that support realtime, time-sharing, remote job entry, and message switching applications. The major components of Telcon are the Distributed Communications Processor (DCP) and the Telcon network software. Multiple DCPs can be combined to form a node of high throughput and processing capability.

Four DCP models are available: the DCP/10A, DCP/15, DCP/20, and DCP/40. The entry-level DCP/10A includes a processor, 512K bytes of memory, and communications line modules. The DCP/10A supports 8 half- or full-duplex communications lines.

The DCP/15, announced in October 1986, will supersede the DCP/10A. The DCP/15 includes a processor, from 2 to

4 megabytes of memory, integrated diskette, integrated hard disk, and communications line modules. It supports up to 52 full-duplex communications lines.

The DCP/20 system consists of a processor with 512K to 2048K bytes of memory, one to three I/O processors, and communications line modules. The main processor performs both generalized communications processing and input/output processing; the I/O processors perform input/output processing only.

Each DCP/20 I/O processor provides programmed control for up to 16 data paths, which can be a combination of serial lines to remote equipment, channels to peripheral devices, or channels to on-site host Series 1100 or Series 90 processors. Each operational port on the I/O processors requires one line module, which provides an interface to a line and performs various communications functions such as control character recognition and line timing. DCP/20 accommodates asynchronous, synchronous, and wideband transmission at up to 64K bits per second. It supports Universal Data Link Control (UDLC) as well as character-oriented communications protocols.

The DCP/40 system includes a processor with 512K to 4096K bytes, expandable in 512K-byte increments. A maximum DCP/40 may include up to 16 I/O processors, each of which provides program control for up to 16 communications channels. Each can handle a mixture of remote lines, parallel interfaces, and host channel connections. Each I/O processor is programmed separately using a set of over 60 macroinstructions and each handles, in addition to data transmission and receipt, remote terminal polling, error checking and recovery, dynamic buffer allocation, reporting of line status, and recording of error and traffic statistics.

The increased memory permits larger and more complex user applications to be included in a single DCP. In addition, the DCP/40 may front-end either 1100 Series or 90 Series mainframes, and supports up to 1,016 half- or full-duplex communications lines.

The DCPs are modular hardware systems that can be tailored to meet the needs of a broad range of users. The network software, Telcon, like the hardware, is also modularly structured and readily tailored by the user. A repertoire of over 285 instructions is available to the user for the generation, assembly, and loading of message handling routines.

The Telcon-controlled system performs all message control operations. As users access the system (network), predeter- ►

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► mined routing paths are followed, or alternate routes are selected using predefined table search routines should established paths become unavailable. Specialized I/O controllers (frequently microcoded modules) handle specific functions, including terminal interfaces and line, trunk, or channel control.

Software and firmware terminal handlers in the DCPs are available for most standard Unisys (Sperry) terminal devices, as well as for terminals from other vendors, including Teletype and IBM 3270 and 2780/3780 batch. Other software modules handle particular line protocols, such as the UDLC trunk lines, or access links to/from X.25 packet switching services.

DCP message switching can be achieved through user-coded applications which use the message routing facilities inherent in the Telcon software. Message routing among terminals, host systems, and network-resident applications is achieved either through user definition in the network generation process or by a dynamic selection through network management services.

If multiple DCPs are configured in the network, each is assigned both specific and network-common responsibilities. For example, all messages remain the responsibility of the originating DCP until accepted by another DCP or end user. Under normal conditions, main memory is used to maintain message queues and buffers, with disk storage used for overflow. Terminal and line handlers are placed as close to the terminals or gateway links as possible, usually in the nearest DCP. This philosophy permits as much of the network as possible to consist of high-speed trunk lines, and allows the low-speed lines running a variety of different terminal protocols, character codes, transmission speeds, and modes to be minimized.

In addition to off-loading the host, the DCP affords a degree of network reliability and resiliency to the user. The stand-alone capability of a single network DCP may permit continued message acceptance and storage of data during periods of temporary inaccessibility to a given host or terminal. Similarly, multiple DCPs may be redundantly configured to maximize network uptime or to increase network throughput. The user is free to mix and match all of the communications processors and subsystems thus far discussed into an efficient communications network. Cost may be a limiting factor in providing increased sophistication.

The Telcon operating system supports local disk and magnetic tape storage for their respective DCPs. This support permits functions including store and forward message switching, logging, journalization, file management, and monitoring.

Peripherals available for the DCPs include hard disk subsystems, diskette subsystems, magnetic tape subsystems, and printers. The DCPs require an operator console, which can be a UTS 20 terminal, an SVT-1121 terminal, or a UTS 400 terminal attached to a communications line.

### SOFTWARE

**OPERATING SYSTEM:** All 1100 Series systems utilize the *1100 Operating System*, which supports batch, transaction, realtime, and interactive processing in multiprogramming, multiprocessing, and distributed processing environments. The heart of the 1100 Operating System is the Executive, which supports user program processing.

Batch processing jobs can be submitted either locally or remotely. A scheduling routine selects the runs to be initiated in accordance with user-assigned priorities and deadlines.

The demand processing facilities of the 1100 Operating System permit interactive use of the system by multiple users at remote terminals. By means of the Executive Control Language, demand-mode users can compile and execute programs, use library facilities, and communicate with the computer center and with other terminals. (More comprehensive facilities for interactive operations are provided by the Interactive Processing Facility, Conversational Time-Sharing, and High-Volume Time-Sharing systems, described later in this report.)

Realtime and communications programs, which are subject to specific time constraints, receive top-priority handling by the 1100 Operating System. Realtime programs receive privileged access to system resources, such as central processors, memory, and input/output channels, and have a priority higher than any other processing except for Executive interrupt processing. Interrupt processing routines can be defined for each realtime communications line; they execute at a higher priority than all other processing. Communications control facilities for transaction processing are provided by the Communications Management System and the Transaction Interface Package, described later in this report.

Multiprocessing is handled as a logical extension of the 1100 Executive's multiprogramming capabilities. The system maintains a list of processor activities currently waiting to be performed. Each processor inspects this list, selects a task, and executes it. One processor can interlock the others while referencing critical areas of common data, and various other techniques are employed to guard against interprocessor interference.

A number of *system management tools* are available for Executive system management, upgrading, and testing. These include the Customer On-site Maintenance and Installation System (COMUS), the Quota system, and Fault Location by Interpretive Testing (FLIT).

- COMUS facilitates the installation and maintenance of the Executive software and program products. COMUS provides a high-level interface that directs an automatic system generation process. COMUS also supports an interface for installing all software into the system libraries. Augmenting COMUS is the Symbolic Stream Generator (SSG). Directions and models for building the desired stream images are conveyed to SSG through a skeleton program. The resulting symbolic output streams can be placed in a user-specified file, printed, and dynamically added for execution after SSG terminates. SSG also helps to maintain symbolic input files that may be printed, corrected, and updated for later use.
- The Quota System enables 1100 Series installations to control the use of system resources by both batch and demand users. Each installation can establish account and individual limits through user identification codes for use of system resources. With the Quota System, installations can prevent users from requesting the use of system resources beyond an account budget or a preassigned limit, control the number of concurrent demand and batch runs executing in the system, and define limits to be applied to resources available to demand and/or batch jobs at specified times.
- Fault Location by Interpretive Testing (FLIT) provides the capability to execute and diagnose the Executive while running as a normal user program under Executive control. Thus, a new version of the Executive or a planned new configuration can be studied and tested in a "virtual" environment prior to its use as the production Executive system. FLIT may also be used to debug programs. ►

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► In addition, the 1100 Executive can dynamically monitor and record system activity. The Software Instrumentation Package (SIP) provides a tool for system throughput and response optimization.

A number of *system processors* are available, including the Site Administration Package (SIMAN), Checkpoint/Restart, Memory Allocation Processor, Post-Mortem Dump Processor (PMD), Element Processor (ELT), Procedure Definition Processor (PDP), File and Program Utility Processor (FURPUR), and Data Processor.

- SIMAN provides a single interface for the site administrator to define users' quota limits, Terminal Security System (TSS) data, and system security data. TSS permits each installation to establish a file of valid remote system users through user identification codes, passwords, and other pertinent information. SIMAN allows installation passwords to be changed dynamically and enables users to be selected as masters or submasters to allow delegation of authority in creating and updating identifications and passwords in the TSS file. Each installation can define the action to be taken in the event of an attempted security violation. SIMAN also is a security control processor that is used to create and maintain a user security profile data base, which is then used to control user access to files and certain privileged functions.
- Checkpoint/Restart snapshots a run or program and creates a checkpoint that may be used for restarting at a later time if, desired.
- The Memory Allocation Processor provides for the collection and interconnection of relocatable elements produced by the compilers to produce an executable program.
- The Postmortem Dump Processor is a user debugging aid that produces edited dumps of the contents of main storage if the program terminates abnormally. Optionally, a dump can be produced when a program terminates normally.
- The Element Processor is used to insert symbolic, relocatable, absolute, or omnibus elements into a program file from images in the runstream.
- The Procedure Definition Processor processes symbolic elements that may contain Assembler, Fortran, or Cobol procedures and produces entries in the table of contents of a program file.
- The File and Program Utility Processor consists of a set of file maintenance routines that provide for the management and manipulation of cataloged or temporary files containing data or programs.
- Data Processor provides data handling capabilities at the file level.

**PROGRAMMING LANGUAGES:** The 1100/70 supports Cobol, Fortran, APL, Pascal, Algol, Basic, PL/1, and RPG. Also available is the Macro general-purpose processor, which extends host languages through its ability to process character strings.

**DATA BASE MANAGEMENT:** *DMS 1100* is a comprehensive data base management system developed under the guiding principles of the CODASYL Data Base Task Group. It is designed to satisfy the need for standardized data management techniques that provide: 1) separation of the data definition and data manipulation functions, 2) an acceptable degree of data independence, 3) data base protection and integrity, and 4) alternate data access methods. DMS has four principal components: a Data Description Language, a Data Manipulation Language, a Data Management Routine, and a Data Recognition Utility.

The Data Description Language is a standalone language whose record descriptions are compatible with those of Cobol. The Data Manipulation Language consists of commands embedded in Cobol, Fortran, and PL/1 to allow these host languages to manipulate the data base via DMS 1100. The Data Management Routine, the key operational component of DMS 1100, maintains the data base and preserves its integrity. The Data Reorganization Utility provides for optimization of the physical placement of records within an existing data base without the need for tailored unload and reload programs.

*QLP 1100* is an English-language inquiry system that allows inquiries to be made to data bases generated under DMS 1100. QLP 1100 has the ability to access standard data files and incorporates extended reporting capabilities. It uses a command language designed around a simplified English syntax and requires a minimum knowledge of the DMS 1100 data base structure. QLP can operate either in demand or batch mode, although the primary mode is interactive. Its two major component modules, the Scan Parser, which analyzes incoming commands, and the Task Translator, which accesses the data base, are both reentrant. Through the use of the QLP command languages, users can inquire into the data base, update records, add new records, or delete records. QLP 1100 uses a Subschema Data Definition Language (QLPSDDL) similar to the DMS 1100 DDL. Access to the data base via QLP is regulated by the Data Base Administrator through use of SDDL. QLP also provides a report writer and procedural facilities.

**DATA MANAGEMENT:** The *Universal Data System (UDS 1100)* is designed to provide 1100 Series users with a single unified data subsystem that furnishes the data management services for all components of the 1100 Operating System. UDS 1100 components include the UDS 1100 Control, Data Management System (DMS 1100), Processor Common Input/Output System (PCIOS), Relational Data Management System (RDMS 1100), Data Dictionary System (DDS 1100), Define File Processor (DFP), Integrated Recovery Utility (IRU), and File Administration System (FAS).

The UDS 1100 Control is the on-line data manager of the UDS system, which provides a complete range of data structures, utility programs, and support programs. UDS 1100 Control integrates these different programs and manages the movement of data between data models. It also centralizes functions such as audit trails and administration.

PCIOS is designed to ensure compatible data file formats. It supports sequential, indexed sequential, and multikeyed sequential access methods for APL, PL/1, ASCII Cobol, ASCII Fortran, RPG, Sort, and QLP.

RDMS 1100 provides definition and access for both host language programming and end-user interface software. Relational data bases are defined by the data manipulation language used for retrieval and updating of data. The Relational Transformation Language provides relational views of other data bases, such as DMS 1100.

DDS 1100 provides a means for the centralized description, location, and control of the various elements within a user data base environment. DDS 1100 consists of a data base of information, called the meta-data base, about the entities in the user data base environment, as well as a set of processors that access the meta-data base for the purpose of creating, updating, and reporting information.

The Define File Processor provides a data file description external to the program processing the file. Using DFP, programs written in Fortran, Cobol, PL/1, APL, and RPG are file-format-independent and can share common files. ►

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► The Integrated Recovery Utility provides the user with English-language commands to initiate a variety of integrity features and capabilities. IRU can be used to control user access to selected TIP or TIP/DMS files or to provide partial file access. It can also be used to compare complete or partial records between files.

The File Administration System is a functional successor to the Secure file administration processor. FAS provides extensive file handling and control within an Interactive Processing Facility system environment. FAS includes capabilities for mass storage file backup, archiving, and reporting. It also provides for the administration of hierarchical files and directories.

The Information Management System (IMS) 1100 is an interactive transaction processing system compatible with the IMS 90 used on the Sperry 90 Series computers. It provides defined record management and access to both data and conventional files.

The Remote Processing System (RPS) 1100 is an interactive data management and file processing system that provides access to system resources by a nonprogramming-oriented user interface through a Uniscope 100 or Uniscope 200 CRT display terminal. RPS 1100 data base files are created and maintained under DMS 1100, and the system interfaces with TIP for transaction interfacing and control. RPS 1100 provides a set of generalized system functions which can be invoked by the user via the terminal. These include commands to ENTER, BUILD, DESTROY, or FORM a file; to process a file through SEARCH, MATCH, or SORT; to build an INDEX structure to line item data and data fields for faster access; to perform computations on specified fields; and to request printing of reports in user-specified formats. RPS 1100 provides tutorial assistance to end users by displaying a choice of functions for user selection and utilizing "fill-in-the blanks" techniques to permit users to enter commands.

**DATA COMMUNICATIONS:** The 1100 Operating System supports two communications processing packages: the Communications Management System (CMS 1100) and the Processor Common Communication System (PCCS 1100), as well as the Distributed Communications Architecture (DCA).

The Communications Management System is the communications network interface for all 1100 Series processors to a DCA-based DCP/Telcon network. It has been separated from the 1100 system generation process, thus allowing the entire terminal network configuration to be generated, checked, and corrected without generating a full system. CMS has cognizance of all terminals in an 1100 Series computer network. It acts as the communications "front-end" to the Transaction Interface Package (TIP), and handles polling, parity checking, data blocking, data packing and unpacking, message envelope formatting, message acknowledgement, message queueing, and other message control procedures. The message queue can be maintained in main and/or auxiliary storage; this common data pool is then accessed by the Transaction Interface Package. A Protocol function determines what the current activity on each circuit should be in terms of overall system loading, availability of facilities, user-specified priorities, type of circuit or device, and activity response level from the terminal.

CMS handles the standard Unisys (Sperry) terminals, as well as "alien" terminal devices. For alien devices the user must supply a skeletal communications control routine that interfaces into the device-control master service routine of CMS. Typical main storage residence requirements for CMS are 10K to 12K words.

The Processor Common Communication System provides a means by which application programs developed in high-level languages such as Cobol and PL/1 can utilize the Series 1100 communications system. Programs using PCCS 1100 can communicate with other communication programs, terminal users, remote batch systems, and certain host computers.

The Distributed Communications Architecture (DCA) describes the currently-available communications hardware and software components through which networking of Unisys (Sperry) processors and terminal devices is achieved.

Whether network control is host-dependent or host-independent, there are still certain hardware components and subsystems required to implement a DCA network. Inherently, a DCA node or host must contain several software components that provide it with the network interface.

The capability of completely separating communications management from applications processing is a key characteristic of DCA. The off-loading of communications processing permits the host or hosts to concentrate their energies on applications processing, their primary function. Another characteristic of DCA is its ready acceptance of other vendors' terminals, processors, and networks.

An extensive library of modular network management applications is available. User programming for tailored communications functions (such as message switching) is also fully supported.

A minimal DCA network requires a DCA host with a communications subsystem. The host may be either an 1100 mainframe running under the 1100 operating system, or a 90 Series CPU, Model 60 or 80, running the VS/9 operating system.

A DCA terminal is generally one for which a standard terminal handling module is available. In DCA, each terminal might be operating with different character codes (ASCII, EBCDIC), transmission modes (start/stop asynchronous, character synchronous), or terminal protocols (U100, IBM 2780). It is the responsibility of the DCP closest to the terminal to translate its data format into a common trunk language—typically UDLC.

UDLC is a bit-oriented, synchronous protocol designed for full-duplex operation. Devices connected by UDLC trunks can utilize either switched or nonswitched, voice grade or digital lines. UDLC, like its SDLCL, HDLC, and ADCCP predecessors, uses bit sequences for control codes rather than whole characters. (Hence the nomenclature "bit-oriented.") This characteristic permits much more control information to be contained in the same or smaller amount of message space.

**PROGRAM DEVELOPMENT:** *Mapper 1100* is a realtime report processing system for multiple terminal systems. Data is collected and updated via the CRT display units in free-form or prescribed report formats. Functions such as record and page display, update, search, sort, and report generation can be developed into saved programs for on-line application development. A forms generation capability allows implementation of data bases and related report processing and generating services without applications programming.

*ADVISE 1100* provides a set of easy-to-use tools for data definition, data interaction, and application development. ADVISE 1100 furnishes the query, update, and application development interface to RDMS 1100, so that users can design and access relational data bases. ►

## Unisys (Sperry) 1100/70

► **CTS 1100** is a modular software system that provides users at remote terminals with a human-machine interface. The system consists of the CTS Control module, interactive syntax analyzers for Basic, Fortran, and Cobol, and access to the compilers for Basic, Fortran, Cobol, Algol, and APL. CTS provides the user with a simplified command language editor. Although still available, CTS has effectively been superseded by the newer Interactive Processing Facility, which is described below.

The *Interactive Processing Facility* supports both batch and time-sharing operations. It provides a user interface to the system through a procedural command language and an English-language response language. IPF 1100 is designed for ease of use by users with little or no data processing background, as well as by computer professionals. Functional capabilities can be expanded by adding user-developed program modules or by modifying or adding commands. IPF 1100 includes data management capabilities, security features, and session control capabilities.

IPF consists of eight separately priced modules. The IPF Command Language is the primary interface for using IPF. It is based on CODASYL specifications. The development of command language subroutines and macros is accomplished through the IPF Procedures module. The Distributed Data Processing module supports file transfers and job submissions from 1100 Series to 1100 Series systems. The Edit 1100 module is an input and update editor that provides access to a variety of file formats, works in an easy-to-use full-screen mode, and can be used from a terminal or called from a program. The User Assistance module manages responses to the terminal user, as well as HELP and explanation processing.

**SX 1100** is a Unix System V-based 1100 OS application program designed to provide a set of software development tools for applications developers as well as for the execution of standard applications. It features debugging tools, on-line documentation, a file management system, access to 1100 OS demand processing, and the ability to access and write 1100 OS formatted files.

The *Programmer's Advanced Debugging System (PADS 1100)* is a language-independent debugging tool. PADS was designed primarily for debugging programs written in high-level languages such as Cobol, Fortran, and PL/1, but it may also be used for programs written in Assembler.

**UTILITIES:** The 1100 Operating System supports a number of utility packages, including CULL, Sort/Merge, Log Analyzer, Performance Analysis Routines, and the On-Line System Activity Monitor.

**CULL** produces an alphabetically sorted, cross-referenced listing of all symbols in a specified set of symbolic elements. Each symbol processed by CULL can contain up to 12 alphanumeric characters plus the dollar sign. An interactive version, IACULL, is also available.

The *Sort/Merge* package provides three sort options and a standard merge option. The sort options are record sort, selection sort, and tag sort. Up to 26 files can be merged, and up to 40 keys can be specified.

The *Log Analyzer (LA)* is designed to assist the user in monitoring the resource utilization of an 1100 Series system. The *Performance Analysis Routines (PAR)* package is a reporting system for data collected by the Software Instrumentation Package embedded in the operating system. The *On-Line System Activity Monitor (OSAM)* provides an on-line, realtime display of system activity. OSAM can be used in conjunction with LA and PAR.

**OTHER SOFTWARE:** The *Transaction Interface Package (TIP)* serves as the "middleman" between the 1100 Operat-

ing System and the user's application programs in a transaction-oriented on-line data processing system. TIP's functions are stimulated by the incoming transaction messages stored in the common data pool maintained by CMS. The TIP transaction scanner, Transcan, analyzes each message, determines which application program is required to process it, and arranges for the Executive to load and execute that program. One application program can also call another application program via TIP, through program action based on data parameters. The application programs can be written in Cobol, Fortran, Assembly Language, or PL/1 and can be reentrant. TIP's features include on-line debugging aids, a batch-mode checkout capability, interprogram protection facilities, and comprehensive system recovery provisions. User-written routines can be accommodated by TIP to perform installation-specified functions such as prioritizing messages and other special message manipulation. The integrated recovery feature supports synchronized recovery of the communications messages and data base updates in a transaction processing environment. Once an input message is received, the requested transaction will be executed regardless of any component failure.

The *Display Processing System (DPS) 1100* provides for screen handling and the management of display-oriented transactions in an on-line environment. The system operates in conjunction with the Transaction Interface Package or the Conversational Time-Sharing System. DPS 1100 includes an interactive screen generator and a screen handler. Additional functions are provided for data editing and validation, applying passwords to screens or separate fields of screens, and controlling access to multipage screens.

A number of application packages are provided for the 1100/70, including the following: Unis 1100, a manufacturing package that includes bill of materials processor, inventory control, and planning and scheduling modules; the Unidis distribution system with freight waybill, wholesale, transportation equipment, and message switching packages; the Unifacs 1100 financial system; Sufics 1100 (Sperry Univac Financial Integrated Control System 1100); the MSA 1100 accounting system; and ICES (Integrated Civil Engineering System).

## PRICING AND SUPPORT

**POLICY:** The 1100/70 is available for purchase or a one-year or five-year lease. All software except the operating system is unbundled. The vendor also offers a seven-year lease to state and local governments and to educational institutions. Educational institutions are eligible for an additional 10 percent discount. The discount does not apply to maintenance service charges.

**SUPPORT:** On-site operating system support can be obtained for a flat monthly fee. Support is available for some unbundled software at a separate monthly charge.

The standard use and service agreements allow unlimited use of the equipment (exclusive of the time required for remedial and preventive maintenance). There are no extra-charge charges. The basic maintenance charge covers maintenance of the equipment for nine consecutive hours a day between the hours of 7 a.m. and 6 p.m., Monday through Friday. Extended periods of maintenance are available at premium rates. The premiums for additional coverage are a percentage of the base maintenance rate and are as follows:

## HOURS OF COVERAGE

	4	8	9	10	12	16	18	20	24
Monday through Friday	—	—	100	105	110	115	120	125	130
Saturday	5	8	9	—	11	15	—	14	15
Sunday and Holidays	7	10	12	—	14	16	—	18	20

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► Maintenance service performed outside the contracted maintenance period is subject to the following rates:

	Monday through Friday (\$)	Saturday, Sunday and Holidays (\$)
Min. charge per call	228	258
Each addl. hour	114	129

Users who elect not to contract for maintenance pay the same rates on a per-call basis.

The Total Remote Assistance Center (TRACE) is a remote hardware maintenance facility located in Roseville, Minnesota. TRACE is available to 1100/70 system customers via a dedicated WATS number 24 hours per day and seven days per week. Via TRACE, a user's system may be monitored and controlled using on-site and remote library testing programs. TRACE also provides support for a wide range of terminals connected to dial-up lines. Various data files in Roseville contain information on approved hardware changes, references to solutions for problems encountered with diagnostic test software in field use, and operating system enhancements and problems. Other files contain a history of how the system should operate properly, and can be utilized for comparison purposes during diagnostic testing.

**EDUCATION:** A variety of courses, both self-study courses and lecture courses, are offered. Instruction is available for both hardware and software systems. Lecture course held at a Unisys (Sperry) site range in price from about \$100 to \$2,800 per student. Group rates are available.

**TYPICAL CONFIGURATIONS:** The following systems illustrate some of the configurations that are possible within the 1100/70 family. All necessary hardware and control units are included in the indicated prices, but software is not included.

### 1100/71 MODEL B1:

3145-23 Processor Complex with 512K words of main memory, one IOU, one SSP, and one system console with printer	\$ 139,875
One 5056-83 Disk Controller	43,750
Two 8470-99 Disk Drives (806MB)	54,720
One 5058-00 Uniservo 22 Subsystem with control unit and 2 tape drives	71,040
One 0770-00 Printer (800 lpm)	56,304
One Print Cartridge	462
One 1986-67 DCP/15	15,125
One SVT-1121 Console for DCP	895
One F1946-02 DCP Host Interface	4,000

**TOTAL PURCHASE PRICE: \$ 386,171**

### 1100/71 MODEL H2:

3145-90 Processor Complex with 512K words of main memory, 2K words of buffer storage, one IOU, one SSP, one maintenance console, and one system console with printer	\$ 305,895
Seven 2004-96 MSU Expansions, for a system total of 4M words of memory	170,240
One 5057-35 Disk Controller	48,125
Two 8481-00 Disk Drives (5GB)	159,280
One 5042-91 Uniservo 36 II Subsystem with control unit and 2 tape drives	81,690
Four Uniservo 36 II Tape Drives	100,000
One 0770-06 Printer (2000 lpm)	60,000
One Print Cartridge	462
One 0776-00 Printer (760 lpm)	36,570
One Print Cartridge	1,270
One 1986-61 DCP/15 with 4MB memory and 20MB hard disk	25,320
One SVT-1121 Console for DCP	895
One F1946-02 DCP Host Interface	4,000
One F3837-99 Multiline Async Line Module	2,250

**TOTAL PURCHASE PRICE: \$ 995,997**

### 1100/74 MODEL H2 MULTIPROCESSOR:

3145-64 Processor Complex with 2 CPUs with 8K words of buffer storage each, 1M words of main memory, 2 IOUs, 2 SSPs, 1 maintenance console, and 2 system consoles with printers	\$ 743,790
Two 3145-78 Processor Expansions; adds 3rd and 4th CPUs	427,040
One 7049-92 MSU Cabinet w/o memory	132,000
One 7049-91 MSU Cabinet with 1M words of memory	180,640
Six 2004-98 Memory Expansions (6M words) for system total of 8M words	291,840
One 5057-35 Disk Controller	48,125
Three 8481-00 Disk Drives (7.5GB)	238,920
One 5042-91 Uniservo 36 II Subsystem with control unit and 2 tape drives	81,690
Six Uniservo 36 II Tape Drives	150,000
Two 0770-06 Printers (2000 lpm)	120,000
Two Print Cartridges	924
One 1986-61 DCP/15 with 4MB memory and 20MB hard disk	25,320
Two SVT-1121 Consoles for DCP	1,790
One F1946-02 DCP Host Interface	4,000
One F3837-99 Multiline Async Line Module	2,250

**TOTAL PURCHASE PRICE: \$2,448,329**

## EQUIPMENT PRICES

		Monthly Charges*			
		Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)
<b>PROCESSORS</b>					
3145-23	1100/71 Model B1 Entry-Level Processing Complex; includes CPU with standard instruction set, 512K words of main memory, IOU with one block multiplexer channel and one word channel module (4 channels), System Support Processor (SSP), and system console with printer	139,875	715	3,245	2,863

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.

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		Monthly Charges*			
		Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)
<b>► PROCESSORS (Continued)</b>					
3145-99	1100/71 Model C1 Standard Processing Complex; includes CPU with standard instruction set, 512K words of main memory, IOU with one block multiplexer channel and one word channel module (4 channels), SSP, and system console with printer	186,595	1,065	5,160	4,398
3145-96	1100/71 Model C2 Standard Processing Complex; same as Model C1, but with extended instruction set	214,995	1,190	6,210	5,238
3145-75	1100/71 Model E1 Medium Performance Processing Complex; includes CPU with standard instruction set, 512K words of main storage, 2K words of buffer storage, integrated multiprocessor capability, IOU as in Model C1, SSP, maintenance console, and system console with printer	232,495	1,415	6,935	5,818
3145-72	1100/71 Model E2 Medium Performance Processing Complex; same as Model E1, but with extended instruction set	260,895	1,540	7,985	6,658
3145-93	1100/71 Model H1 High Performance Processing Complex; same as Model E1, but with 8K words of buffer storage	277,495	1,765	8,575	7,133
3145-90	1100/71 Model H2 High Performance Processing Complex; same as Model H1, but with extended instruction set	305,895	1,890	9,625	7,973
3145-67	1100/72 Model H1 High Performance Multiprocessor Complex; to be used when initially ordering an 1100/73 or 1100/74; includes two CPUs with 8K words of buffer storage each, one Memory Storage Unit with 1024K words of main memory, two IOUs, two SSPs, two system consoles, and one maintenance console	686,990	3,830	20,462	16,941
3145-64	1100/72 Model H2 High Performance Multiprocessor Complex; same as 3145-67, but with Extended Instruction Set	743,790	4,080	22,562	18,621
3145-79	Model H1 Processor Expansion; provides an H1 processor for expanding an 1100/72 H1 to an 1100/73 H1 or for expanding an 1100/73 H1 to an 1100/74 H1; includes one processor with 8K-word buffer; external main memory must be ordered separately	185,120	1,730	5,835	4,700
3145-78	Same as 3145-79, but for H2 systems; includes Extended Instruction Set	213,520	1,855	6,885	5,540
3064-99	Attached Virtual Processor (AVP); includes CPU with 512K to 1024K words of main memory and 32K-byte buffer; main storage interface unit; interprocessor interface; one byte multiplexer channel; one externally specified index attachment; VS/9 facility; AVP console; and Attached Processor Control Software (not available for B1 processors)	132,040	455	2,669	2,357
F3627-00	AVP Block Multiplexer Channel	16,430	60	390	310
<b>SYSTEM UPGRADES</b>					
2017-97	Model B1 to C1 Upgrade	46,720	350	1,915	1,535
F2917-99	Model 1 up 2 Upgrade; makes Model C1, E1, or H1 into the equivalent of Model C2, E2, or H2 with the addition of the Extended Instruction Set	28,400	125	1,050	840
1952-86	Model Upgrade; upgrades Model C1 to E1 or C2 to E2	45,900	350	1,775	1,420
1952-84	Model Upgrade; upgrades Model C1 to H1 or C2 to H2	90,900	700	3,415	2,735
1952-82	Model Upgrade; upgrades Model E1 to H1 or E2 to H2	45,900	350	1,775	1,420
3145-55	Model E1 Uniprocessor to Dual Processor Upgrade; upgrades 1100/71 E1 by adding second E1 processor with 2K-word buffer and 512K-word memory	139,745	1,215	4,225	3,653
3145-54	Model E2 Uniprocessor to Dual Processor Upgrade; same as 3145-55, but adds E2 to E2	168,145	1,340	5,275	4,493
3145-53	Model H1 Uniprocessor to Dual Processor Upgrade; upgrades 1100/71 H1 by adding second H1 processor with 8K-word buffer and 512K-word memory	184,745	1,565	5,865	4,968
3145-52	Model H2 Uniprocessor to Dual processor Upgrade; same as 3145-53 but adds H2 to H2	213,145	1,690	6,915	5,808
<b>PROCESSOR OPTIONS</b>					
F2869-00	Performance Monitor; provides scannable buffered counters within a processing complex to allow the SSP to collect selected performance parameters	32,151	23	803	637
F2688-01	IOU Expansion; provides space for up to two word channel modules and one block multiplexer channel; one per processor complex; mutually exclusive with F2916-00 and F3751-00	8,722	23	271	216
F2916-01	IOU Expansion; provides space for up to two block multiplexer channels and one word channel module; one per processor complex; mutually exclusive with F2688	8,722	23	271	216
F3751-00	IOU Expansion; provides space for four additional block multiplexer channels; one per processor complex; mutually exclusive with F2688 and F2916	16,500	75	472	367
2003-97	I/O Expansion for 1100/73 and 1100/74 systems; includes interfacing logic for two CPU/IOU interfaces, two MSUs, support controller interface, one block multiplexer channel and one word channel module	45,000	180	1,395	1,120
F2684-01	Word Channel Module; provides four additional independent word channel interfaces; for use with F2688 and F2916	17,425	105	515	410
F2690-01	Block Multiplexer Channel; provides interface for up to eight byte-oriented control units; for use with F2688, F2916, or F3751	13,867	70	433	343

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.

## Unisys (Sperry) 1100/70

	Monthly Charges*			
	Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)

► **PROCESSOR OPTIONS (Continued)**

F3716-98	Channel Interconnection; provides Hyperchannel connection capability on block multiplexer channels	2,400	16	53	42
F2867-00	Shared Peripheral Interface (SPI) Control; provides capability to control up to six word control units, each with up to four SPI interfaces	16,105	23	404	321
F2904-00	Byte Channel Transfer Switch Control; provides capability to control one fully configured 4x8 byte channel transfer switch; maximum of 2 F2867-00 or F2904-00 per processing complex	16,105	23	404	321
2521-00	Channel Transfer Switch for block multiplexer channels; freestanding cabinet contains operator controls for manual switching of 4 subsystem strings, a primary module with 2x1 switch, and power and space for 4x8 switching	19,781	81	489	365
2521-02	Channel Transfer Switch for remote operation	19,781	81	489	365
F2600-00	Primary Module Expansion; adds a switch for one subsystem string; maximum of 3 per 2521-00 or F2601-00; maximum of one per F2601-01	586	—	14	11
F2601-00	Additional Primary Module; adds a second 2x1 primary module and operator control for switching up to 4 subsystem strings	10,476	44	273	194
F2601-02	Additional Primary Module for remote operation	10,476	44	273	194
F2601-01	Secondary Module; for applications requiring 2-by switching capability when up to 4 switchable strings can be configured among independent 2-by switches; may be expanded by one F2600-00	10,476	44	273	194
F2601-03	Secondary Module; same as F2601-01 but for remote operation	10,476	44	273	194
F2602-00	Secondary Module; expands primary module from 2x1 to 4x1; two maximum	7,127	34	185	133
F2602-01	Secondary Module; same as F2602-00 but for remote operation	7,127	34	185	133
F2603-00	Secondary Module; allows expansion of 4-by switching by one subsystem string; maximum of 3 with each F2602-00; requires F2600-00	586	—	14	11
F2604-00	DC Power Redundancy; adds back-up DC supplies for hot-standby dynamic power redundancy	2,680	13	69	49
3660-64	Additional Console Complex; attaches to SSP; includes CRT console with keyboard, 200-cps bidirectional printer, and console table; maximum of two per SSP	28,627	129	714	565
3660-62	Auxiliary Console; includes CRT console with keyboard and console table; maximum of two per processor complex	11,574	70	288	227

**MEMORY**

2004-96	Memory Storage Unit Expansion; provides an additional 512K words of memory; maximum of 4096K words for a unit processor and 8192K words for a dual processor or multiprocessor	24,320	50	855	685
7049-92	Main Storage Unit Cabinet; houses from 1024K to 4096K words of external memory for 1100/73, 1100/74, 1100/72 AVP, or 1100/73 AVP systems	132,000	300	3,312	2,675
7049-91	Main Storage Unit Cabinet Expansion; provides second cabinet for external memory for 1100/73 or 1100/74; includes 1024K words, expandable to a maximum of 4096K words	180,640	400	5,022	4,045
2004-98	Storage Expansion; provides 1024K words of external memory for 7049 MSU, which is prerequisite	48,640	100	1,710	1,370

**MASS STORAGE**

5012-99	FH-432/FH-1782 Drum Control; controls one to eight 6016-00 or 6015-00 drums in any combination	102,720	555	2,247	1,605
F0929-00	Write Lockout Feature for 5012-99 drum control	1,392	5	32	22
F0930-99	Shared Peripheral Interface for 5012-99 drum control; multiprocessor application only	22,608	52	495	355
6016-00	FH-432 Drum; 256K words	52,848	229	1,271	825
6015-00	FH-1782 Drum; 2048K words	146,064	638	3,512	2,280
F0786-01	Dual Channel Feature for 6016-00 drum	3,024	31	72	47
F0767-00	Dual Channel Feature for 6015-00 drum	3,024	35	69	47
5039-91	8433 Control; supports up to eight 8433 (or 8430) disk drives; minimum of two drives per subsystem	27,000	408	1,577	769
F2047-00	Drive Expansion Feature; provides for a maximum of sixteen 8433/8430 drives to be attached to the 5039-91	5,760	56	211	137
8433-00	8433 Disk Drive; removable disk media; minimum of two drives per system	13,680	258	798	389
F1223-00	Disk Pack for the 8433; 154MB	1,820	—	66	43
F2021-00	Dual Access Feature; provides dual access and simultaneous read/read, read/write, write/read, or write/write on any two 8433/8430 drives	1,630	5	59	39
5056-83	8470/8480 Disk Control; provides control for up to eight 8470 disk drives	43,750	258	1,255	930
F2994-00	Four-Channel Capability for 5056-83	6,472	37	270	180
F3192-00	8433 Attachment; allows up to eight 8433 (or 8430) drives on 5056 control unit; up to 3 are allowed	9,840	58	305	226

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.



## Unisys (Sperry) 1100/70

		Monthly Charges*			
		Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)
<b>► MASS STORAGE (Continued)</b>					
F3192-02	8470/8480 Attachment; allows up to 8 additional 8470 or two 8480 drives on single control unit; up to 3 are allowed	3,200	21	105	78
F2837-00	Power Control Expansion; required on control unit when over 16 drives are configured	6,575	56	222	144
8470-99	8470 Disk Drive; 403MB of storage	27,360	119	809	599
F2718-00	8470 Dual Access Feature; provides dual access and simultaneous read/write, read/read, write/read, and write/write	1,920	17	57	42
8480-97	8480 Disk Storage Unit; contains 4 spindles with a total capacity of 1.6GB (limited availability)	76,500	475	1,932	1,607
8480-99	8480 Disk Storage Expansion; provides 4 additional spindles; includes dual access feature	83,700	497	2,113	1,761
F2718-02	8480 Dual Access Feature; provides dual access and simultaneous read/write, read/read, write/read, and write/write	7,200	22	181	154
5057-75	8450/8470/8480 Cache/Disk Processor; manages up to four 7053 Cache Storage Units plus up to eight 8450 disk units and up to eight 8470 or two 8480 disk units; connects via word channel	52,960	355	2,207	1,471
5057-77	Disk Control Processor; controls up to eight 8450 disk units and up to eight 8470 or two 8480 disk units; can be upgraded to 5057-75 Cache/Disk Processor	48,125	355	2,005	1,337
5057-87	Solid-State Disk (SSD) Processor; manages up to four 7053 Cache Storage Units used only in solid-state disk mode	41,715	355	1,738	1,159
F3948-97	Processor Upgrade; converts 5057-77 Processor to 5057-75 Cache/Disk Processor	4,835	—	202	134
F4025-98	SSD Upgrade; converts 7057-87 SSD Processor to 5057-75 Cache/Disk Processor	11,245	—	469	312
F3567-00	8450 Capability Expansion; permits sixteen 8450 drives on cache/disk control; precludes attachment of 8470 drives	9,345	55	290	215
F3568-00	8470/8480 Capability Expansion; permits sixteen 8470 drives or four 8480 drives on cache/disk control; precludes 8450 drives	9,345	55	290	215
F2994-00	Four-Channel Capability; expands channel interface capability to 4 word channels; one required per control	6,472	37	270	180
8450-99	8450 Disk Storage Unit; provides 2 disk drives, each with a capacity of 216MB	49,950	346	2,439	1,583
5057-31	8481 Cache/Disk Processor; manages up to four 7053 Cache Storage Units and up to four 8481 disk units; cache and SSD can be intermixed; connects to word channel	52,960	355	2,207	1,471
5057-29	8481 Dual Cache/Disk Processor; same as 5057-31, but provides 2 processors in a single cabinet for dual access control; connects to 2 word channels	105,920	710	4,414	2,942
5057-23	8481 Dual Cache/Disk Processor; same as 5057-29, but connects to 2 block multiplexer channels with FIPS compatibility	105,920	710	4,414	2,942
5057-35	8481 Disk Control Processor; controls up to four 8481 disk units; connects via word channel; can be expanded to 5057-31 Cache/Disk Processor	48,125	355	2,005	1,337
5057-33	8481 Dual Disk Control Processor; same as 5057-35, but includes 2 processors for dual access control; can be expanded to 5057-29 Dual Cache/Disk Processor	96,250	710	4,010	2,674
5057-25	8481 Dual Disk Control Processor; same as 5057-33, but connects via 2 block multiplexer channels with FIPS compatibility; can be expanded to 7057-23 Dual Cache/Disk Processor	96,250	710	4,010	2,674
5057-39	Solid-State Disk (SSD) Processor; manages up to four 7053 Cache Storage Units used only in solid-state disk mode; connects to word channel	41,715	355	1,738	1,159
5057-37	Dual SSD Processor; provides 2 processors in one cabinet for dual access control	83,430	710	3,476	2,318
5057-27	Dual SSD Processor; same as 5057-37, but connects to 2 block multiplexer channels with FIPS compatibility	83,430	710	3,476	2,318
F4025-96	Cache/Disk Capability; converts a 5057-39 SSD Processor to a 5057-31 Cache/Disk Processor	11,245	—	469	312
F4025-95	Cache/Disk Capability; converts a 5057-37 SSD Processor to a 5057-29 Cache/Disk Processor	22,490	—	938	624
F4025-94	Cache/Disk Capability; converts a 5057-27 SSD Processor to a 5057-23 Cache/Disk Processor	22,490	—	938	624
F3948-95	Cache/Disk Capability; upgrades a 5057-35 Disk Control Processor to a 5057-31 Cache/Disk Processor	4,835	—	202	134
F3948-94	Cache/Disk Capability; upgrades a 5057-33 Disk Control Processor to a 5057-29 Cache/Disk Processor	9,670	—	404	268
F3948-93	Cache/Disk Capability; upgrades a 5057-25 Disk Control Processor to a 5057-23 Cache/Disk Processor	9,670	—	404	268
8481-00	8481 Disk Storage Unit; contains 4 spindles with a total capacity of 2.5GB	79,640	235	3,318	2,212
8481-99	8481 Disk Storage Unit; same as 8481-00, except it includes dual access and simultaneous read/write, read/read, write/read, and write/write capability	86,840	235	3,618	2,412
F2718-04	Dual Access Feature; provides dual access capability for 8481-00 disk unit	7,200	—	300	200
7053-97	First Cache Storage Unit; provides 917,504 words of RAM; functions as SSD or cache/disk; requires Segment Descriptor Table for operation in cache/disk mode	72,000	469	2,130	1,600
7053-96	Cache Storage Expansion Unit; provides additional 917,504 words of RAM; up to 3 can be added	72,000	469	2,130	1,600

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.

### Unisys (Sperry) 1100/70

		Monthly Charges*			
		Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)
▶	<b>MASS STORAGE (Continued)</b>				
F3117-02	Segment Descriptor Table; provides 64K words of RAM for cache memory index for 7053	8,200	30	275	250
K3351-00	7053 Memory Expansion; 917,504 words of RAM	36,000	185	1,065	800
F3118-00	7053-97 Dual Access Feature	4,416	16	138	123
F3118-01	7053-96 Dual Access Feature	4,416	16	138	123
5090-99	8490 Dual Solidstate Disk (SSD) Controllers; 2 control units with an 8MB SSD module; can be expanded up to 256MB of SSD storage; requires 7059 uninterruptible power supply (UPS)	166,210	592	6,926	4,617
5090-97	SSD Subsystem; same as 5090-99, except it includes 7059 UPS	194,610	734	8,109	5,405
5090-95	8490 Disk Subsystem; includes two 5090 controllers and 5GB of disk storage; can be expanded to a cache/disk system	296,500	730	12,354	8,236
5090-93	8490 Disk Subsystem; includes two 5090 controllers and 2.5GB of disk storage; can be expanded to 3.75GB or 5GB	224,500	650	9,354	6,236
5090-91	8490 Cache/Disk Subsystem; same as 5090-95, except it also includes cache/disk capability with 8MB of memory	366,500	1,100	15,271	10,180
5090-89	8490 Cache/Disk Subsystem; same as 5090-93, except it also includes cache/disk capability with 8MB of memory	294,500	1,050	12,271	8,180
8490-99	First 5GB Disk Expansion Unit; for 5090-95 or 5090-91; can be used in 5090-93 or 5090-89 if they are first expanded to 5GB	150,000	400	6,250	4,166
8490-97	Disk Expansion Unit; same as 8490-99, except it provides second or third 5GB expansion unit	150,000	400	6,250	4,166
8490-95	First 2.5GB Disk Expansion Unit; for 5090-95 or 5090-91; can be used in 5090-93 or 5090-89 if they are first expanded to 5GB	90,000	310	3,750	2,500
8490-93	Disk Expansion Unit; provides second or third 2.5GB expansion unit	90,000	310	3,750	2,500
F4976-99	Disk Expansion, 2.5GB; for 5090 or 8490 cabinet with only 2.5GB installed	90,000	310	3,750	2,500
F4976-98	Disk Expansion, 1.25GB; for partially populated cabinets	54,000	160	2,250	1,500
F4979-99	Four-Channel Expansion; expands the 4 word channels in a 5090 subsystem to 8 channels	12,945	45	540	360
F4983-99	Disk to Cache Upgrade; provides cache capability to disk-only units; includes 8MB of memory, expandable to 32MB	70,000	400	2,916	1,944
F4984-99	SSD Capability; adds SSD to disk-only units; includes 8MB of memory, expandable to 40MB	72,800	280	3,033	2,022
F4983-98	Disk/SSD to Cache Upgrade; converts F4984-99 SSD module, which has been added to a disk-only system, to a cache/disk module	10,000	120	416	277
F4984-98	Second SSD Module; requires F4984-99; can also add SSD capability to a cache/disk subsystem or add a second SSD module to an SSD-only system	72,800	280	3,033	2,022
F4982-99	Memory Expansion, 8MB; for cache or SSD module	60,000	80	2,500	1,666
F4985-99	Data Save Feature; provides 256MB of data save capability for SSD module; requires freestanding UPS	16,710	42	696	464
7059-99	Freestanding Uninterruptible Power System (UPS); provides 15 minutes of power for the dual control units, data save unit, and SSD memory	44,500	142	1,854	1,236
5071-00	Optical Disk Control; provides control for up to 4 optical string controllers in optical disk cabinet	36,000	135	1,500	1,000
F0487-00	Optical Disk Control Expansion; provides second control module, housed in 5071-00 cabinet	26,700	102	1,113	742
F0486-00	Dual Channel Interface for 5071-00	3,960	17	165	110
F0486-01	Dual Channel Interface for F0487-00	3,960	17	165	110
8652-99	Optical Disk Unit; includes optical disk string control, 2.6GB optical disk drive, optical disk cartridge, operator panel, power supply, and space for up to 3 additional optical disk units	50,860	210	2,119	1,413
F0488-00	Optical Disk Drive Expansion; provides additional 2.6GB optical disk drive	25,560	106	1,065	710
F0489-00	String Switch; provides capability to switch data path of 8652-99 string between control modules of 5071-00 and F0487-00; housed in 8652-99	4,680	20	195	130
8654-99	Optical Disk Library; includes optical string control, optical library disk drive, optical disk cartridge, operator panel, power supply, and automatic disk changer for 32 disk cartridges	110,860	390	4,619	3,079
F0488-02	Optical Disk Library Expansion; provides an additional optical library disk drive; maximum of one per 8654	25,560	106	1,065	710
F0489-02	String Switch for Optical Library Unit; provides capability to switch data path of 8654 between control modules of 5071 and F0487; housed in 8654	4,680	20	195	130
F4761-00	Optical Disk Cartridge; 2.6GB	460	—	—	—
5070-97	Shared System; a relational data base system consisting of a processor, a data accelerator, 2MB of main storage, block multiplexer interface, RS-232 interface, disk controller, 8457 disk cabinet with 2 drives, console with printer, and a data base administrator's terminal with printer	250,000	1,200	—	—
F4554-00	Main Storage Expansion, 1MB; maximum of 4	9,000	65	—	—
F4556-01	Block Multiplexer Interface Expansion; provides for attachment of a second 1100 Series or IBM host block multiplexer channel	17,300	60	—	—
F4619-01	RS-232 Interface Expansion; provides for attachment of up to 8 additional RS-232 Sperry Personal Computers (or equivalent) to the Shared System	8,000	60	—	—

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.

## Unisys (Sperry) 1100/70

		Monthly Charges*			
		Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)
<b>MASS STORAGE (Continued)</b>					
F4557-00	Disk Control Expansion; provides additional control for up to four 8457 disk drives; maximum of 3	10,200	65	—	—
F4555-00	Mirrored Disk Support; provides support for data duplication on separate disk drives	12,000	75	—	—
8457-99	Disk Subsystem; includes cabinet with two 344MB disk drives; one included in basic Shared System; up to 3 more cabinets can be added	30,000	190	—	—
F4552-99	8457 Disk Expansion; provides 2 additional 344MB disk drives for 8457-99 cabinet	26,000	190	—	—
8457-98	Disk Subsystem; includes cabinet with four 344MB disk drives; maximum of 4 per Shared System	56,000	380	—	—
8457-97	Disk Subsystem; includes cabinet with two 512MB disk drives; up to 3 more cabinets can be added	34,000	190	—	—
F4814-99	8457 Disk Expansion; provides 2 additional 512MB disk drives	30,000	190	—	—
<b>MAGNETIC TAPE UNITS</b>					
5058-00	Uniservo 22 Subsystem; includes 2 Uniservo 22 tape drives and control for up to 8 Uniservo 22 or Uniservo 24 drives	71,040	411	2,235	1,659
5058-02	Uniservo 22 Magnetic Tape Drives; includes 2 dual-density PE/NRZI drives; 1600/800 bpi, 9-track, 75 ips	47,040	267	1,386	1,029
5058-06	Uniservo 24 Subsystem; includes 2 Uniservo 24 tape drives and control for up to 8 Uniservo 24 or Uniservo 22 drives	78,720	455	2,466	1,827
5058-08	Uniservo 24 Magnetic Tape Drives; includes 2 dual-density PE/NRZI drives; 1600/800 bpi, 9-track, 125 ips	54,720	311	1,617	1,197
F0825-00	Dual Channel Feature; provides nonsimultaneous operation on 2 channels of one processor or one channel on each of 2 processors	4,272	34	110	89
F2627-00	Translation Feature; translation is ASCII/EBCDIC, Fielddata/EBCDIC, or Fielddata/ASCII	1,728	15	52	36
F2627-01	Second Translation Feature	1,728	15	52	36
F3820-00	Dual Access Feature	2,016	16	56	44
5055-99	Uniservo 26/28 Control; controls up to 8 Uniservo 26 and 28 tape units; also controls Uniservo 22 and 24 tape units with F2451-00 installed	22,700	140	635	470
F2451-00	Adds 9-track NRZI to 5055-99	3,170	16	82	63
F3738-00	Dual Channel Feature for the 5055-99; provides nonsimultaneous access to the control from 2 block multiplexer channels	1,000	4	34	25
F3739-00	Translation Feature; ASCII to/from EBCDIC	3,600	18	94	72
0884-00	Uniservo 26 Magnetic Tape Unit; dual-density GCR/PE, 6250/1600 bpi, 9-track, 75 ips	22,000	180	595	440
0884-02	Uniservo 28 Magnetic Tape Unit; dual-density GCR/PE, 6250/1600 bpi, 9-track, 125 ips	24,750	190	675	500
F3737-00	Dual Access Feature	900	5	27	20
0876-97	Uniservo 22 Magnetic Tape Unit; attaches to 5055 control unit with F2451-00 installed	19,190	110	525	389
0876-93	Uniservo 24 Magnetic Tape Unit; attaches to 5055 control unit with F2451-00 installed	21,215	121	694	532
5042-00	Uniservo 30 Control for up to 8 9-track, dual-density (GCR/PE) Uniservo 30, 32, 34, or 36 drives	36,214	399	1,290	953
F2131-00	Adds 9-track NRZI to 5042-00; prerequisite for Uniservo 30 drives and all 7-track NRZI features	3,171	26	88	66
F2585-00	Translation Feature for 9-track drives on 5042 control; translation is in both directions involving ASCII/EBCDIC, Fielddata/EBCDIC, and Fielddata/ASCII	1,785	15	49	38
F2585-01	Second 9-track Translator; F2585-00 required	1,785	15	49	38
F2584-99	Adds 7-track NRZI to 5042-00; includes ASCII to BCD translator and data conversion	1,617	13	44	34
F2584-98	Translator is ASCII to fielddata	1,617	13	44	34
F2584-97	Translator is fielddata to BCD	1,617	13	44	34
F2135-00	Dual Channel Feature for the 5042-00; provides nonsimultaneous access to the control from 2 block multiplexer channels; not software supported	4,185	44	138	104
F2137-00	Drive Expansion Feature for the 5042-00; provides for up to 16 Uniservo 30, 32, 34, or 36 drives to be attached to the 5042-00	668	5	23	16
0872-00	Uniservo 30 Magnetic Tape Drives; 9-track, dual-density, PE/NRZI, 1600/800 bpi, 200 ips	27,300	251	903	631
0872-02	Uniservo 30 Magnetic Tape Drive; 7-track, NRZI, 800/556/200 bpi, 200 ips	27,300	251	903	631
F2123-00	Conversion Feature; converts 0872-02 to 0872-00	3,287	—	91	68
0873-00	Uniservo 32 Magnetic Tape Drive; 9-track, dual-density, GCR/PE, 6250/1600 bpi, 75 ips	24,800	227	839	573
0873-02	Uniservo 34 Magnetic Tape Drive; 9-track, dual-density, GCR/PE, 6250/1600 bpi, 125 ips	28,300	261	962	654
F2125-00	Conversion Feature; converts 0873-00 to 0873-02	3,675	34	129	85
0874-00	Uniservo 36 Magnetic Tape Drive; 9-track, dual-density, GCR/PE, 6250/1600 bpi, 200 ips	29,500	279	1,031	700
5042-91	Uniservo 36-II Subsystem; includes cabinet with control unit and one Uniservo 36-II tape drive, plus a second cabinet with one tape drive and power supply for both cabinets	81,690	957	3,404	2,269

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.

Unisys (Sperry) 1100/70

	Monthly Charges*			
	Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)

▶ **MAGNETIC TAPE UNITS (Continued)**

F4849-00	Dual Channel Feature; provides nonsimultaneous access to the control from 2 block multiplexer channels	4,408	44	187	122
F4847-00	Dual Access 8 Feature; provides dual access capability for up to 8 tape drives; requires 2 control units	5,990	25	250	166
F4848-00	Dual Access 16 Feature; provides dual access capability for up to 16 tape drives; requires 2 control units	5,990	25	250	166
F4850-00	9-Track Translation Feature; translates data from ASCII to EBCDIC and from EBCDIC to ASCII	1,785	15	74	50
0874-22	Uniservo 36-II Magnetic Tape Drive; 9-track, dual-density, GCR/PE, 6250/1600 bpi, 200 ips	25,000	279	1,042	694
5073-99	Uniservo 40 Cartridge Tape Control; controls up to 4 Uniservo 40 Cartridge Tape Units	65,430	360	2,726	1,818
F4855-XX	Channel Attachment; adds second, third, or fourth block multiplexer channel interface	5,785	20	241	161
F4854-00	Dual Access Feature	4,045	—	169	112
0899-99	Uniservo 40 Cartridge Tape Unit; contains 2 tape drives (18-track, 37,871 bytes/inch)	47,108	225	1,963	1,309

**PRINTERS**

0770-00	Line Printer and Control; 800 lpm with 48 character set	56,304	372	1,300	1,041
0770-02	1400 lpm	64,896	487	1,498	1,196
0770-04	2000 lpm	86,686	742	3,187	2,074
F1533-00	160 Print Positions for 0770 Series Printers	4,416	26	102	82
F1534-00	Expanded Character Set Control; required for other than 48-character print cartridges	2,880	5	66	53
F2230-00	Printer Upgrade; 0770-00 to 0770-02	8,592	116	198	155
F2230-01	Printer Upgrade; 0770-00 to 0770-04	30,382	249	1,159	559
F2230-02	Printer Upgrade; 0770-02 to 0770-04	21,790	133	961	404
F2822-00	Dynamic Advance Control; reduces slew rate by 50 percent to optimize stacking of light forms	300	—	8	7

Print Cartridges for 0770 Printers:

F1536-00	48-character Alphanumeric Business/Commercial	462	—	24	19
F1536-01	48-character Alphanumeric Scientific	462	—	24	19
F1536-03	48-character Alphanumeric for United Kingdom	462	—	24	19
F1536-06	48-character ANSI standard OCR-A	462	—	24	19
F1537-00	94-character ASCII Graphic (ANSI X3.4-1968)	462	—	24	19
F1537-03	68-character ISO Universal OCR-B	462	—	24	19
F1537-04	68-character OCR H-14 Universal	462	—	24	19
F1537-05	58-character Cobol/Fortran/Business	462	—	24	19
F1537-06	177-character International	462	—	24	19
F1537-09	24-character Numeric	462	—	24	19
F1537-11	68-character Universal OCR-A	462	—	24	19
F1537-12	68-character Universal ECMA-11 OCR-B	462	—	24	19
F1537-13	68-character Universal Univac 77L OCR-B	462	—	24	19
F1537-14	63-character Modified Fortran	462	—	24	19
F1537-15	63-character Modified ASCII	462	—	24	19
F1537-19	384-character American Library Association	462	—	24	19
F1537-21	128-character OCR-A	462	—	24	19
F1537-23	94-character Optimized ASCII	462	—	24	19
F1537-24	68-character Optimized IOS Universal OCR-B	462	—	24	19

(Cartridges are also available for languages other than English)

0770-06	0770 II Line Printer and Control; 2000 lpm with 48-character set	60,000	685	2,500	1,667
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Print Cartridges for 0770 II Printer:

F4836-00	48-character Alphanumeric Business/Commercial	462	—	—	—
F4836-01	48-character Alphanumeric Scientific	462	—	—	—
F4836-06	48-character OCR-A Alphanumeric	462	—	—	—
F4837-00	94-character ASCII	462	—	—	—
F4837-03	68-character ISO Universal OCR-B	462	—	—	—
F4837-04	68-character OCR H-14 Universal	462	—	—	—
F4837-05	58-character Cobol/Fortran/Business	462	—	—	—
F4837-06	177-character International	462	—	—	—
F4837-11	68-character ISO Universal OCR-A	462	—	—	—
F4837-12	68-character Universal ECMA-11 OCR-B	462	—	—	—
F1537-13	68-character Universal IBM 407	462	—	—	—
F4837-14	63-character Modified Fortran	462	—	—	—
F4837-15	63-character Modified ASCII	462	—	—	—
F4837-19	162-character American Library Association	462	—	—	—

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.

## Unisys (Sperry) 1100/70

	Monthly Charges*			
	Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)

▶ **PRINTERS (Continued)**

F4837-21	73-character OCR-A	462	—	—	—
F4837-22	63-character Alphanumeric				
F4837-23	94-character Optimized ASCII	462	—	—	—
F4837-24	63-character Optimized IOS Universal OCR-B (Cartridges are also available for languages other than English)	462	—	—	—
0776-00	Line Printer and Control; 760 lpm with 48-character set	36,570	284	1,006	803
0776-02	Line Printer and Control; 900 lpm	41,340	340	1,134	907
0776-04	Line Printer and Control; 1200 lpm	48,000	388	1,431	1,145
F2217-00	Printer Upgrade; 0776-00 to 0776-02	4,770	56	128	104
F2245-00	Expanded Character Set Control; required for character sets with more than 64 characters	1,910	5	50	40

## Print Cartridges for 0776 Series Printers:

F2216-00	48-character Alphanumeric Business/Commercial	1,270	—	34	26
F2216-01	48-character Alphanumeric Scientific	1,270	—	34	26
F2216-07	24-character Numeric	1,270	—	34	26
F2216-08	63-character Modified Fortran	1,270	—	34	26
F2216-09	63-character Modified ASCII	1,270	—	34	26
F2216-10	48-character OCR-A	1,270	—	34	26
F2215-00	94-character ASCII	1,270	—	34	26
F2215-03	68-character ISO Universal OCR-B	1,270	—	34	26
F2215-04	68-character OCR H-14 Universal	1,270	—	34	26
F2215-05	58-character Cobol/Fortran/Business	1,270	—	34	26
F2215-11	68-character Universal OCR-A	1,270	—	34	26
F2215-12	68-character Universal ECMA-11 OCR-B	1,270	—	34	26
F2215-13	68-character Universal Univac 77L	1,270	—	34	26
F2215-20	94-character Optimized ASCII	1,270	—	34	26
F2215-21	68-character Optimized ISO Universal OCR-B	1,270	—	34	26
F2215-23	128-character OCR-A	1,270	—	34	26
0777-97	On-Line Laser Printer, Model I; up to 21,000 lpm; includes forms splicing station and diskette with 15 character sets (limited availability)	270,000	1,026	—	8,283
0777-87	On-Line Laser Printer, Model II; same as 0777-97 but includes 2 diskettes with 15 character sets each	170,700	942	7,115	4,740
F3380-00/ -01	Additional Character Sets; for 0777-97	30	—	—	—
F2874-00	Character Font Expansion; up to 255 characters; for 0777-97	3,640	18	109	82
F3815-00	Character Font Expansion; up to 255 characters; for 0777-87	3,640	18	109	82
F3816-00	Character Font Expansion; up to 1,024 characters; for 0777-87	14,560	84	500	344
F3816-02	Character Font Expansion; up to 3,200 characters; for 0777-87	43,680	254	1,500	1,033
F3816-99	Character Font Upgrade; expands an 0777 printer with 1,024-character font storage to 3,200-character font storage	29,120	170	1,000	689
F3816-98	Character Font Expansion; same as F3816-00, but for field installation only on 0777-97	14,560	84	500	344
F3816-97	Character Font Expansion; same as F3816-02, but for field installation only on 0777-97	43,680	254	1,500	1,033
F3935-XX	Alternate Developer Station; for 0777-97	14,500	26	810	613
F2876-00	Forms Overlay Capability	11,700	34	352	261
F3426-00	Overlay Transparencies	35	—	—	—
1963-00	Burster/Trimmer/Stacker	40,196	181	1,278	900
F3595-00	Forms Counter for 1963-00	1,580	5	40	34
F3598-00	Center Slitter for 1963-00; provides lengthwise separation of forms	900	11	21	18
F3601-00	One-Wide Roll Imprinter; for special printing on forms before bursting; requires 1963-00	1,060	29	25	21
F3601-01	Two-Wide Roll Imprinters; same as F3601-00, but provides two-wide printing	1,520	29	45	38

**PUNCHED CARD EQUIPMENT**

0604-99	Card Punch and Control; 250 cpm	31,968	251	664	536
0716-89	Card Reader and Control; 1000 cpm; comes with code translator; EBCDIC, ASCII, compressed code, or field data code	16,545	179	445	322
F1487-00	51-Column Card Read Feature	1,968	17	45	32
F1487-01	66-Column Card Read Feature	1,968	17	45	32
F1488-00	Validity Check	816	—	18	14
F1498-00	Stacker Feature; permits the alternate filling of stackers one and two when in the stop-on-error mode	528	—	12	8
F1486-00	Translate Mode conversion; from EBCDIC to ASCII	105	—	—	—
F1486-01	Compressed Code to ASCII	105	—	—	—
F1486-02	ASCII to EBCDIC	105	—	—	—
F1486-03	Compressed Code to EBCDIC	105	—	—	—

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.

## Unisys (Sperry) 1100/70

### Monthly Charges\*

Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)
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### ► PUNCHED CARD EQUIPMENT (Continued)

F1486-04	ASCII to Compressed Code	105	—	—	—
F1486-05	EBCDIC to Compressed Code	105	—	—	—
F1486-06	To Fielddata Code	100	—	—	—
F1530-00	Adds a second translator to translate mode under program control	1,104	5	25	18

### TERMINALS

3560-64	UTS 10 Buffered CRT; includes 12-inch screen, RS-232 or current loop interface, expanded keyboard with function keys, program cartridge, and bit serial output peripheral interface	1,720	—	—	—
3660-60	UTS 20 CRT; includes 12-inch screen, 16K RAM, RS-232-C/CCITT V.24 interface, 3270 compatibility, tilt/rotate base, program cartridge, and bit serial output peripheral interface	2,115	31	100	66
3561-80	UTS 40 Programmable CRT; includes 12-inch screen, system RAM, and RS-232-C/CCITT V.24 interface	2,333	43	104	69

### DISTRIBUTED COMMUNICATIONS PROCESSORS

1986-75	Distributed Communications Processor/10A (DCP/10A); includes cabinet with space for additional DCP/10A, processor with 512K bytes of storage, power supply, power distribution, cooling, operator panel, active line indicators, microprograms, multiple device line module, and integrated diskette drive with controller; requires F1946-02 or F1947-03 host interface	14,950	159	623	415
1986-73	DCP/10A; same as 1986-75, except that processor includes 1MB of storage	20,450	229	852	568
1986-71	DCP/10A; same as 1986-75, except it also includes a 10MB rigid disk drive	17,750	210	740	493
1986-69	DCP/10A; same as 1986-75, except it includes a processor with 1MB of storage and a 10MB rigid disk drive	23,250	280	939	646
2005-75	DCP/10A; same as 1986-75, except cabinet is not included	13,716	152	587	386
2005-73	DCP/10A; same as 2005-75, except processor has 1MB of storage	19,216	222	816	539
2005-71	DCP/10A; same as 2005-75, except it also includes integrated 10MB rigid disk drive	16,516	203	704	464
2005-69	DCP/10A; same as 2005-75, except it includes a processor with 1MB of storage and an integrated 10MB rigid disk drive	22,016	273	903	617
F3891-03	Storage Expansion; expands processor storage from 512KB to 1MB	10,400	70	433	289
F4427-00	Storage Expansion; expands processor storage from 1MB to 1.5MB or from 1.5MB to 2MB	10,400	70	433	289
F3895-00	Power Supply Expansion; provides additional power for remote configurations	882	5	26	21
F1947-03	8-Bit Host Interface	4,000	23	105	85
1986-67	Data Communications Processor/15 (DCP/15); includes cabinet with processor, 2MB of memory expandable to 4MB, power supply, power distribution, cooling, operation panel, active line indicators, microprograms, multiple device line module, and integrated diskette drive with controller; requires F1946-02 or F1947-03 host interface and SVT-1121 console; provides space for one 2053-XX DCP/15	15,125	50	630	420
1986-63	DCP/15; same as 1986-67, but also includes 20MB of integrated disk storage	17,125	70	714	476
1986-65	Expanded DCP/15; includes 4MB of memory	23,320	55	972	648
1986-61	Expanded DCP/15; includes 4MB of memory and 20MB of integrated disk storage	25,320	75	1,055	703
2053-99	DCP/15; same as 1986-67, except cabinet is not included	13,915	50	580	387
2053-97	DCP/15; same as 1986-65, except cabinet is not included	22,110	50	921	614
2053-95	DCP/15; same as 1986-63, except cabinet is not included	15,915	70	663	442
2053-93	DCP/15; same as 1986-61, except cabinet is not included	24,110	75	1,005	670
F3895-01	Power Supply Expansion; provides additional power for remote configurations	970	5	40	27
8597-78	Data Communications Processor/20 (DCP/20); includes cabinet, processor with 512KB of storage, power supplies, power distribution, cooling, maintenance panel, operator panel, active line indicators, microprograms, and integrated diskette drive	35,000	229	1,458	972
8597-76	DCP/20; same as 8597-78, except processor includes 1MB of storage	42,110	355	1,755	1,170
2024-96	Storage Expansion; expands processor storage from 512KB to 1MB	12,250	126	510	340
8597-01	Expansion Cabinet; contains processor capable of performing I/O functions only; provides mounting for 8 line modules; maximum of 2 per DCP/20 system	24,000	119	656	525
F2894-00	Line Module Expansion; provides 8 additional line modules for 8597-01	12,000	60	460	250
8596-79	Distributed Communications Processor/40 (DCP/40); preconfigured system including 512KB of main storage, 4.6MB rigid disk subsystem, integrated diskette drive, 1100 Series interface, 8-bit peripheral interface, and active line indicators; accommodates up to 11 communications line modules; requires a UTS 20 or UTS 400 console	102,675	584	2,809	2,225
8596-77	DCP/40; same as 8596-79, except it accommodates up to 27 communications line modules and has 14MB disk; includes second I/O processor	119,651	674	3,324	2,629

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.

## Unisys (Sperry) 1100/70

## Monthly Charges\*

## ▶ DISTRIBUTED COMMUNICATIONS PROCESSORS (Continued)

		Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)
8596-96	DCP/40; includes processor with 512KB of main storage, I/O controller module, first I/O processor, and microprograms; requires an integrated diskette plus an 8441-81 disk subsystem, communications line modules, and an SVT-1121 console	84,245	452	2,305	1,843
K1930-01	Storage Expansion; provides additional 512KB of storage; maximum of 3	15,600	126	410	325
1945-99	Expansion Cabinet; provides power supply and power controller; accommodates up to 4 I/O processors and up to 4MB of main storage; maximum of 3 per DCP/40 system, only one of which can contain storage	27,060	146	740	593
F2942-01	Storage Controller; provides a storage controller and 512KB of storage; mounts in expansion cabinet; can be expanded to 2MB by the addition of up to 3 K1930-01 storage expansion features and expanded up to 4MB with the addition of a 2036-99 and 3 K3930-01 features; maximum of one per system	26,880	145	735	588
2036-99	Storage Controller Expansion; includes 512KB of storage; can be expanded to 2MB with the addition of up to 3 K1930-01 storage expansion features to create the second 2MB of storage in the 1945-99 expansion cabinet	13,950	77	365	290
F1933-00	I/O Processor Controller Module; provides expansion cabinet with first IOP and space for mounting 3 additional IOPs and a storage port expander	14,680	78	399	320
F2941-99	Second IOP Expansion; provides second IOP for 8596-96 or 1945-99; includes power for 2 more IOPs	14,920	81	410	326
F1932-99	Third IOP; mounts in 8596-96 or 1945-99; includes storage port expander	14,185	76	389	310
F1932-98	Fourth IOP	10,635	57	294	231
F1928-00	Operator Station; a freestanding work surface that can be used for the local console	1,200	—	30	25
F1825-05	Active Line Indicator; provides a visual display of line activity on up to 16 communication line modules in a single IOP	960	4	26	21
Features for the DCPs:					
F1936-00	DCP/20-DCP/40 Storage Port Expander; provides a multiplexed interface to a single local storage access port for up to 4 requestors	3,550	19	95	75
F1946-02	1100 Series ISI Interface; provides a full-duplex ISI interface to a word channel; maximum of 1 per DCP/10A or DCP/15 cabinet, 2 per DCP/20 cabinet, or 4 per DCP/40 cabinet	4,000	23	110	89
F1947-02	Host Byte Interface; provides interface to Series 90 byte multiplexer channel or 1100 Series block multiplexer channel; maximum of 1 per DCP/20 cabinet or 2 per DCP/40 cabinet (not available for DCP/10A or DCP/15)	4,000	23	105	85
F1947-03	Host Byte Interface; provides interface to 1100 Series block multiplexer channel; for DCP/10A or DCP/15	4,000	23	105	85
F3878-00	Byte Interface Line Module; provides 8-bit interface to the 8409 disk subsystem	1,900	11	56	45
F1948-01	16-bit Peripheral Interface; provides interface to a peripheral subsystem; allows operation in 8- or 16-bit mode (for DCP/20 and DCP/40)	3,000	16	84	68
F1941-00	Full-Duplex Interface to Asynchronous Data Sets; conforms to EIS RS-232-C and CCITT V.24 and V.28; data set rates up to 2400 bps	960	3	25	20
F1942-00	Full-Duplex Interface to Synchronous Data Sets; conforms to EIA RS-232-C and CCITT V.24 and V.28; data set rates up to 9600 bps	960	3	26	21
F3163-00	Full-Duplex Interface to Synchronous or Asynchronous Modems; conforms to EIA RS-232-C and CCITT V.24 and V.28; operates with Bell DDS up to 9600 bps or at data set rates up to 19,200 bps	1,275	8	35	30
F3163-01	Full-Duplex Interface to Public Data Networks; conforms to CCITT X.21 and X.25; operates at rates up to 19,200 bps	2,500	14	63	50
F3163-04	Full-Duplex Interface to Synchronous Modems; conforms to RS-449; up to 9600 bps	1,920	11	50	40
F3164-00	Full-Duplex Interface to Bell 303 Modem; up to 64K bps	7,200	38	188	150
F3164-01	Full-Duplex Interface to Carrier Facilities; conforms to CCITT V.35; operates with UDLC protocol data formats (64K bps), V.35 facilities (48K bps), and Bell DDS and DSDS facilities (56K bps)	3,745	21	100	80
F3165-01	Multiline Asynchronous Line Module; provides full-duplex interfaces to up to 4 data sets; conforms to RS-232-C and CCITT V.24 and V.28; up to 2400 bps	2,880	14	79	63
F3837-99	Multiline Asynchronous Line Module; provides full-duplex interfaces to up to 4 data sets or direct-connect terminals; conforms to RS-232-C and CCITT V.24 and V.28; up to 9600 bps	2,250	18	94	63
F3837-00	Multiline Synchronous Line Module; provides up to 4 full-duplex interfaces to data sets or direct connect terminals; conforms to RS-232-C and CCITT V.24 and V.28; up to 19.2K bps	2,250	18	94	63
F3835-00	Remote Partitioning Capability; maximum of 1 on DCP/20 or 4 on DCP/40 (not available for DCP/10A or DCP/15)	960	5	25	20
F1945-00	Auto Dialing Line Module; interfaces to Bell 801 Automatic Calling Units or those conforming to CCITT V.24 and V.25	1,005	4	25	20
8590-00	Remote Control Module (RCM); provides the capability to control power on/off and other functions of up to 4 DCP processors; requires RCM Adapter, F3163-00 or F3163-04 and/or 1 or 2 F3556-00 and F3557-00	13,526	61	355	280
F3898-00	Remote Control Adapter for DCP/10A and DCP/15; provides interface between the RCM and the DCP	1,915	11	50	40
F2893-00	Remote Control Adapter for DCP/20	1,824	11	48	38

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.

## Unisys (Sperry) 1100/70

Monthly Charges*			
Purchase Price (\$)	Monthly Maint. (\$)	1-Year Lease (\$)	5-Year Lease (\$)

► **DISTRIBUTED COMMUNICATIONS PROCESSORS (Continued)**

F1937-00	Remote Control Adapter for DCP/40	1,824	11	48	38
2523-00	Line Switch Module (LSM); provides the capability to switch communication lines and/or peripherals from a local or remote source; requires 1 switch feature; up to 6 switch features supported	28,750	112	748	597
1962-00	LSM Auxiliary Cabinet for DCP/20 and DCP/40; provides mounting for up to 10 switch features	6,872	39	197	143
F3557-00	RCM/LSM Microcode	350	1	9	7
F3556-00	RCM/LSM Local Control Interface; provides one loadable line module for the RCM and LSM and one for the DCP	3,600	16	95	75
F3105-00	Modem Expander; enables a second RCM or LSM to share a single RS-232-C modem	1,440	4	38	30
F3109-00	RS-232-C Switch; provides the capability to switch 8 RS-232-C communications lines from one communications controller to another	4,930	22	132	102
F3110-00	CCITT V.35 Switch; up to 8 lines	9,325	43	245	195
F3112-00	RS-449 Switch; up to 4 lines	6,000	27	156	125
F3113-00	16-bit Parallel Interface Switch; up to 4 interfaces (not for DCP/10A)	7,200	33	188	150
F3559-00	Bell 303 Switch; up to 4 lines (not for DCP/10A)	16,800	82	440	350
F1939-00	Integrated Diskette Subsystem for DCP/20 and DCP/40; includes 256KB diskette and controller	1,920	12	53	42
8408-02	Cartridge Disk Control; controls up to 2 F2380 drives (for DCP/20 and DCP/40)	5,564	32	146	109
F2380-04	Fixed/Removable Cartridge Disk Drive; five megabytes fixed, five megabytes removable	17,750	124	461	330
F2187-00	Second I/O Interface for dual F2380 configuration	1,568	9	39	29
8409-99	Disk Subsystem; includes cabinet, control, and one 4.6MB disk drive; requires Byte Interface Line Module, F3878-00 (not for DCP/15)	9,650	82	378	280
8409-97	Disk Subsystem; same as 8409-99, except it includes a 14MB drive	10,746	94	478	354
F3900-00	Disk Drive Expansion; provides a second disk drive with 4.6MB capacity; maximum of one	3,777	54	158	117
F3900-01	Disk Drive Expansion; provides a second disk drive with 14MB capacity; maximum of one	4,207	66	188	139
F4085-00	Disk Drive Expansion; expands the capacity of one 4.6MB drive to 14MB	1,096	12	100	74
F3881-00	Dual Disk Control; provides a second DCP interface	2,000	9	65	50
F4158-01	Integrated Disk Drive for DCP/15; 20MB	2,000	20	83	56
8441-78	8441 Disk Subsystem; 30MB; connects to DCP/15	4,200	28	175	116
F4228-98	Additional 8441-78 Disk Drive	2,710	26	112	75
8441-79	Disk Subsystem; includes cabinet, controller, and 30MB disk drive; connects to DCP/10A	4,200	28	175	116
F4228-98	Disk Expansion; provides additional 30MB disk drive for 8441-79	2,710	26	112	75
8441-81	Disk Subsystem; includes cabinet, controller, and 30MB disk drive; connects to DCP/20 and DCP/40	4,200	28	175	116
F4228-99	Disk Expansion; provides additional 30MB disk drive for 8441-81	2,710	26	112	75
O871-01	Uniservo 10 Magnetic Tape Unit; PE/NRZI, 1600/800 bps, 25 ips (for DCP/20 and DCP/40)	13,962	93	318	239
F2721-00	Uniservo 10 Controller; controls up to 2 drives	10,320	56	284	215
F2879-00	AC Power Switch; provides remote control of second Uniservo 10	1,200	5	32	25
3612-95	SVT-1121 Console; includes 14-inch screen, keyboard, setup menu in 6 languages, full-duplex auxiliary port, security keylock, and power cord	895	10	—	—
O425-93	Data Processing Quality Printer for SVT-1121; 160 cps	1,275	38	55	45
O425-92	High Definition Quality Printer for SVT-1121; 160/40 cps	1,395	44	60	50
O472-99	Bidirectional Printer for SVT-1121; 160 cps in data processing mode, 32 cps in near letter quality mode	695	—	—	—
O445-99	Data Processing Quality Printer; 160 cps; connects to DCP/20 or DCP/40	775	17	35	23
O445-97	High Definition Quality Printer; 160/40 cps; connects to DCP/20 or DCP/40	895	20	49	27
F4224-00	Paper Roll for O445 Printers	45	1	3	2
F4109-00	Forms Tractor	50	1	3	2
F3977-00	Printer Stand for O445 Printers	230	—	10	8

\*Lease charges do not include maintenance.

\*\*Field Installation Charge.



## Unisys (Sperry) 1100/70

## SOFTWARE PRICES

	<b>Single Extended Term Charge* (\$)</b>
<b>► System Processors</b>	
6163-00 Terminal Security System	11,230
6167-00 Sentry Security Control Processor	37,778
6158-00 Quota Input Processor (QUIP)	11,385
6162-00 Checkpoint/Restart	6,883
6133-00 Data Processor	3,260
6203-00 Fault Location of Interpretive Testing (FLIT)	6,003
<b>Language Processors</b>	
6165-00 General Syntax Analyzer	4,950
6172-00 APL 1100	26,341
6171-00 UBasic	6,521
6178-00 UBasic Syntax Analyzer	3,260
6153-00 ASCII Cobol	15,525
6149-00 Cobol Syntax Analyzer (BCOB)	6,521
6154-00 ASCII Fortran	22,770
6150-00 Fortran Syntax Analyzer (BFTN)	6,521
6151-00 PL/1	13,041
6164-00 RPG 1100	6,521
6243-99 RPG II Group	6,728
6160-00 MACRO	6,521
<b>Data Base Management &amp; Data Management</b>	
6292-00 Universal Data System (UDS) 1100 Control	10,350
6700-00 UDS Data Management System (DMS) 1100	54,338
6298-00 UDS Query Language Processor (QLP) 1100	21,994
6298-99 UDS QLP with PCIOS Interface	25,616
6293-00 UDS Relational Data Management System (RDMS) 1100	38,813
6299-00 UDS Data Dictionary System	25,875
6177-00 Define File Processor	3,260
6175-00 Integrated Recovery Utility (IRU)	22,201
6175-01 IRU Version II	23,184
6175-02 IRU Version III	26,082
6291-00 File Administration System (FAS)	7,763
6155-00 Data Management System (DMS) 1100	56,925
6176-00 Data Dictionary	19,820
6152-00 Processor Common Input/Output System (PCIOS)	3,881
6244-00 Information Management System (IMS) 1100	10,091
6157-00 Query Language Processor (QLP) 1100	21,994
6156-00 Remote Processing System (RPS) 1100	13,041
<b>Data Communications</b>	
6169-95 Communications Management System (CMS)	14,231
6169-97 CMS 1100 DCP/40	28,463
6159-00 Processor Common Communications System (PCCS)	6,521
6136-92 DCP/10, DCP/15 Operating System	6,750
6136-95 DCP/20 Operating System	9,000
6136-01 DCP/40 Operating System	16,425
6276-00 BSC 3270 Terminal Handler	6,750
<b>Program Development</b>	
6146-00 Mapper 1100	62,100
8824-00 Mapper Kit Tools	15,000
6290-00 Advanced Information Service (Advise) 1100	15,525
6239-00 Programmers Advanced Debugging System (PADS) 1100	11,385
6170-01 Conversational Time-Sharing System (CTS) 1100	18,113
6147-00 High-Volume Time-Sharing (HVTS)	32,861
6262-00 Interactive Processing Facility (IPF) Command Language	14,231
6260-00 IPF Control	5,170
6263-00 IPF Procedures	18,113
6245-98 Edit 1100	15,008
6264-00 User Assistance	3,881
6261-00 Distributed Data Processing (DDP) 1100	5,175
7623-01 SX 1100; 16 users	12,000
7623-02 SX 1100; 32 users	18,000
7623-03 SX 1100; 64 users	27,000
7623-04 SX 1100; 128 users	39,000

\*License for a 5-year period.

\*\*Monthly charge.

## Unisys (Sperry) 1100/70

Single  
Extended  
Term  
Charge\*  
(\$)

### ► Utilities

6271-00	CULL Processor	1,294
F3859-00	Interactive CULL (IACULL)	1,294
6135-00	Sort/Merge	7,763
6246-00	Log Analyzer	6,210
6161-00	Performance Analysis Routines	15,008
6274-00	On-line System Activity Monitor (OSAM)	7,763

### Miscellaneous Products

6237-00	Display Processing System (DPS) 1100	18,113
F3791-00	Univac Printer Interface Software (UPRINTS); provides interface to 0777 Printer	10,350
6753-00	Percon Control; provides support for peripheral devices such as printers	2,588
F6110-00	Percon 0777; provides support for 0777 laser printer; requires 6753-99	4,050
F6115-00	Percon 0770 and 0776; provides support for 0770 and 0776 printers as an alternative to standard device handlers; requires 6753-99	3,364
F3793-00	Cache Disk Interface Software (CADIS)	20,700
7666-00	Shared System Control	60,000
8759-00	Optical Disk Support System; provides the capability to store data or retrieve data from the 5071 Optical Disk Subsystem	10,800
8760-00	Optical Disk Direct Read Package; includes all the capabilities of 8759-00, plus a callable subroutine that provides for direct/random reads within the previously written portion of an optical disk cartridge	13,800
6173-62	1100/71 Model B1 Local Support Service (LSS)	**400
6173-91	1100/71 Model C1 LSS	**580
6173-90	1100/71 Model C2 LSS	**580
6173-68	1100/71 Model E1 LSS	**800
6173-67	1100/71 Model E2 LSS	**800
6173-89	1100/71 Model H1 LSS	**925
6173-88	1100/71 Model H2 LSS	**925
6173-61	Model E1 Dual Processor LSS	**1,000
6173-60	Model E2 Dual Processor LSS	**1,000
6173-59	Model H1 Dual Processor LSS	**1,150
6173-58	Model H2 Dual Processor LSS	**1,150
6173-87	1100/72 Model H1 Multiprocessor LSS	**1,300
6173-86	1100/72 Model H2 Multiprocessor LSS	**1,300
6173-66	1100/73 Model H1 Multiprocessor LSS	**1,500
6173-65	1100/73 Model H2 Multiprocessor LSS	**1,500
6173-64	1100/74 Model H1 Multiprocessor LSS	**1,620
6173-63	1100/74 Model H2 Multiprocessor LSS	**1,620

\*License for a 5-year period.

\*\*Monthly charge. ■