

Pinout for CPU (Milton-256) - Data Bus (D0-D31)

sysData(0)	24	D0
sysData(1)	26	D1
sysData(2)	32	D2
sysData(3)	34	D3
sysData(4)	36	D4
sysData(5)	101	D5
sysData(6)	103	D6
sysData(7)	106	D7
sysData(8)	108	D8
sysData(9)	111	D9
sysData(10)	113	D10
sysData(11)	116	D11
sysData(12)	118	D12
sysData(13)	120	D13
sysData(14)	163	D14
sysData(15)	167	D15
sysData(16)	169	D16
sysData(17)	171	D17
sysData(18)	173	D18
sysData(19)	175	D19
sysData(20)	178	D20
sysData(21)	180	D21
sysData(22)	182	D22
sysData(23)	185	D23
sysData(24)	215	D24
sysData(25)	217	D25
sysData(26)	221	D26
sysData(27)	229	D27
sysData(28)	231	D28
sysData(29)	233	D29
sysData(30)	236	D30
sysData(31)	239	D31

Pinout for CPU (Milton-256) - Control and Status

memNotCe(0) (6)	240	RAM_L
memNotBe(3) (6)	7	BE3_L
memNotBe(2) (6)	8	BE2_L
memNotBe(1) (6)	9	BE1_L
memNotBe(0) (6)	10	BE0_L
memNotWe (6,7,8)	241	WE_L
irFrmBsy (4)	246	GPIO(8)
serialNotCpEnable (4)	247	GPIO(9)

Pinout for CPU (Milton-256) - I/O and Peripheral

memNotOe (6)	242	OE_L
romNotCe(0) (6)	201	ROM_L
lcdIoCe (11)	244	CS1_L
memNotFlashCe (6)	245	CS0_L
romSda (6)	249	SDA
romIoCe (6)	202	IOCE
romIoWr (6)	203	IOWR
romIoRd (6)	204	IORD
romIoInt (6)	1	IOINT
romIoRdy (6)	4	EXTRDY
bitIoDda (3)	250	DDA
bitIoAint (3)	253	AINT
bitIoDad (3)	251	DAD
bitIoFrame (3)	252	FRAME

Pinout for CPU (Milton-256) - LCD and Display

lcdData(7-0) (5)	lcdData(0) 55	DATA0
	lcdData(1) 54	DATA1
	lcdData(2) 53	DATA2
	lcdData(3) 51	DATA3
	lcdData(4) 45	DATA4
	lcdData(5) 44	DATA5
	lcdData(6) 42	DATA6
	lcdData(7) 41	DATA7
lcdFim (5)	40	FLM_LCD_CS
lcdVdClk (5)	46	VDCLK
lcdLnClk (5)	49	LNCLK
lcdAcMod (5)	39	ACMOD
lcdVccEn (5,9)	37	VCC_EN
clk32KHz (12)	50	LCD_DC_CLK
lcdVeeEn (11)	38	VEE_EN
elBackLightOn (9)	48	DISP_VP_EN

Pinout for CPU (Milton-256) - Memory and System

pcNotCe (7,8)	243	PCE_L
pcNotIrq (7,8)	194	PIRQ_L
pcNotFq (7,8)	195	PFIQ_L
pcNotDreq (7,8)	196	PDREQ_L
pcReady(3)	197	PRDY3
pcReady(2)	198	PRDY2
pcReady(1) (8)	199	PRDY1
pcReady(0) (7)	200	PRDY0

Pinout for Memory (U1)

VSS_IN0	28
VSS_IN1	160
VSS_IN2	218
VSS_IN3	95
VSS_32K	132
VSS_CLK	226
VSS_CORE0	29
VSS_CORE1	96
VSS_CORE2	164
VSS_CORE3	219
VSS_O0	11
VSS_O1	27
VSS_O2	43
VSS_O3	52
VSS_O4	68
VSS_O5	85
VSS_O6	104
VSS_O7	114
VSS_O8	165
VSS_O9	183
VSS_O10	148
VSS_O11	209
VSS_O12	235
VSS_PLL	256

Pinout for Memory (U1) - Address and Data

MA0	12	memAddr(0)
MA1	14	memAddr(1)
MA2	16	memAddr(2)
MA3	18	memAddr(3)
MA4	20	memAddr(4)
MA5	77	memAddr(5)
MA6	79	memAddr(6)
MA7	81	memAddr(7)
MA8	83	memAddr(8)
MA9	86	memAddr(9)
MA10	89	memAddr(10)
MA11	91	memAddr(11)
MA12	93	memAddr(12)
MA13	97	memAddr(13)
MA14	142	memAddr(14)
MA15	144	memAddr(15)
MA16	146	memAddr(16)
MA17	149	memAddr(17)
MA18	151	memAddr(18)
MA19	154	memAddr(19)

Pinout for CPU (Milton-256) - Address Bus (A0-A30)

A0	13	sysAddr(0)
A1	15	sysAddr(1)
A2	17	sysAddr(2)
A3	19	sysAddr(3)
A4	21	sysAddr(4)
A5	78	sysAddr(5)
A6	80	sysAddr(6)
A7	82	sysAddr(7)
A8	84	sysAddr(8)
A9	88	sysAddr(9)
A10	90	sysAddr(10)
A11	92	sysAddr(11)
A12	94	sysAddr(12)
A13	99	sysAddr(13)
A14	143	sysAddr(14)
A15	145	sysAddr(15)
A16	147	sysAddr(16)
A17	150	sysAddr(17)
A18	152	sysAddr(18)
A19	155	sysAddr(19)
A20	156	sysAddr(20)
A21	157	sysAddr(21)
A22	158	sysAddr(22)
A23	159	sysAddr(23)
A24	205	sysAddr(24)
A25	206	sysAddr(25)
A26	207	sysAddr(26)
A27	208	sysAddr(27)
A28	210	sysAddr(28)
A29	212	sysAddr(29)
A30	213	sysAddr(30)

Pinout for CPU (Milton-256) - Control and Status

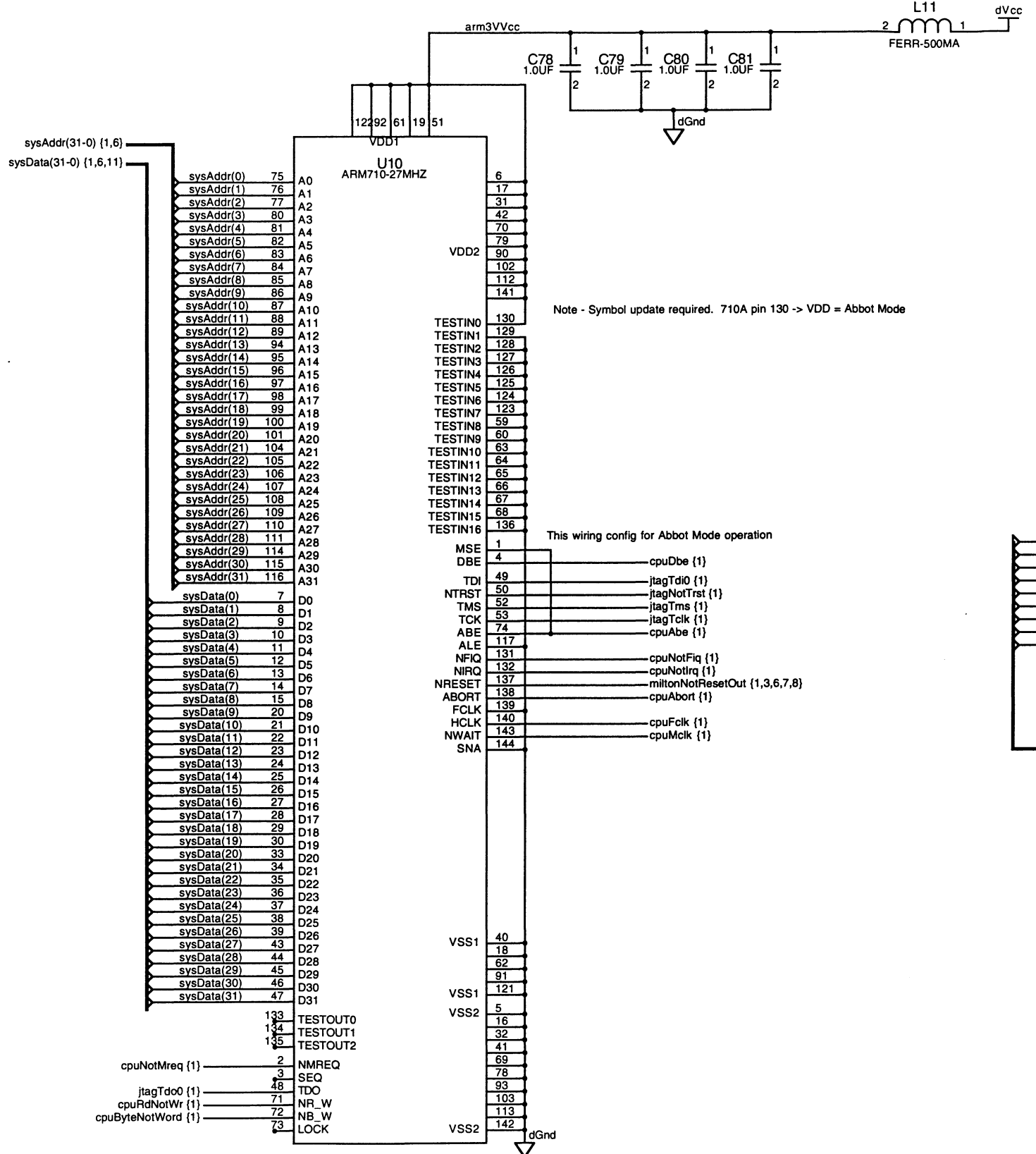
FCLK	223	cpuFclk (2)
MCLK	225	cpuMclk (2)
NMREQ	135	cpuNotMreq (2)
ABORT	186	cpuAbort (2)
NRW	136	cpuRdNotWr (2)
NBW	137	cpuByteNotWord (2)
NIRQ	193	cpuNotIrq (2)
NFIQ	192	cpuNotFq (2)
ABE	187	cpuAbe (2)
DBE	189	cpuDbe (2)
ALE_GPIO(10)	188	miltonGPIO10 (4)
HALT	190	cpuHalt (2)
HALTA	191	cpuHaltAck (2)
SCC_RTS0_L	62	sccNotRts0 (4)
SCC_CTS0_L	73	sccNotCts0 (4)
SCC_DCD	67	sccNotDcd0 (4)
SCC_DTR0_L	65	sccNotDtr0 (4)
SCC_TXD0	59	sccTx0 (4)
SCC_RXD0	59	sccRxd0 (4)
COMDATA	134	irDataNotControl (4)
IR_TXD1	64	irTxd1 (4)
IR_RXD1	58	irRxd1 (4)
SCC_TXD2	69	keybrdTxd2 (4)
SCC_RXD2	60	keybrdRxd2 (4)
SCC_RTS3_L	70	modemNotRts3 (4)
SCC_CTS3_L	74	modemNotCts3 (4)
SCC_DCD3_L	75	modemNotDcd3 (4)
SCC_DTR3_L	71	modemNotDtr3 (4)
SCC_RI3_L	76	modemRi3 (4)
SCC_TXD3	72	modemTxd3 (4)
SCC_RXD3	61	modemRxd3 (4)
ATTN	57	cpuAttention (2,12)
VCC_FAULT_L	2	pwrNotVccFault
BATT_FAULT_L	3	pwrNotBattFault (10,12)
PWR_CTRL	248	pwrPowerEnable (3,4,6,7,8)
SYS_RESET_L	6	miltonNotResetOut (2,3,6,7,8)
RESET_IN	5	miltonNotResetIn
HSCLK	227	rawHsClk
SCLK	255	clkSclk (3,4,6)
CLK32K_IN	130	clk32KHzIn
CLK32K_OUT	131	clk32KHzOut
TCLK	126	jtagTclk (2)
TMS	127	jtagTms (2)
NTRST	128	jtagNotTrst (2)
TDI	133	jtagTdi1
TDO	125	jtagTdo1
GPIO0	141	gpPowerSwIn (4)
GPIO1	140	acAdaptorInstalled (10)
GPIO2	139	pcmS0CardLockSw (7)
GPIO3	124	pcmS1CardLockSw (8)
GPIO4	123	gp5VEnable (11)
GPIO5	122	gp12VEnable (11)
GPIO6	122	gpSerPortSel (4)
GPIO7	121	niCdFastCharge (10)

Pinout for Memory (U1) - Data Bus (MD0-MD31)

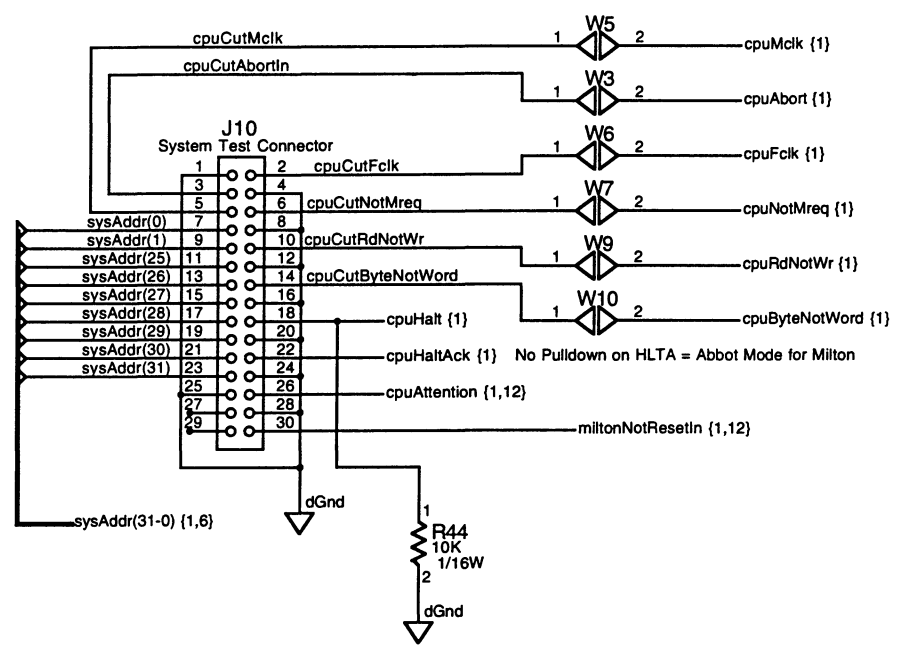
MD0	23	memData(0)
MD1	25	memData(1)
MD2	30	memData(2)
MD3	33	memData(3)
MD4	35	memData(4)
MD5	100	memData(5)
MD6	102	memData(6)
MD7	105	memData(7)
MD8	107	memData(8)
MD9	109	memData(9)
MD10	110	memData(10)
MD11	111	memData(11)
MD12	117	memData(12)
MD13	119	memData(13)
MD14	161	memData(14)
MD15	166	memData(15)
MD16	168	memData(16)
MD17	170	memData(17)
MD18	172	memData(18)
MD19	174	memData(19)
MD20	177	memData(20)
MD21	179	memData(21)
MD22	181	memData(22)
MD23	184	memData(23)
MD24	214	memData(24)
MD25	216	memData(25)
MD26	220	memData(26)
MD27	228	memData(27)
MD28	230	memData(28)
MD29	232	memData(29)
MD30	234	memData(30)
MD31	238	memData(31)

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Note - Symbol update required. 710A pin 130 -> VDD = Abbot Mode



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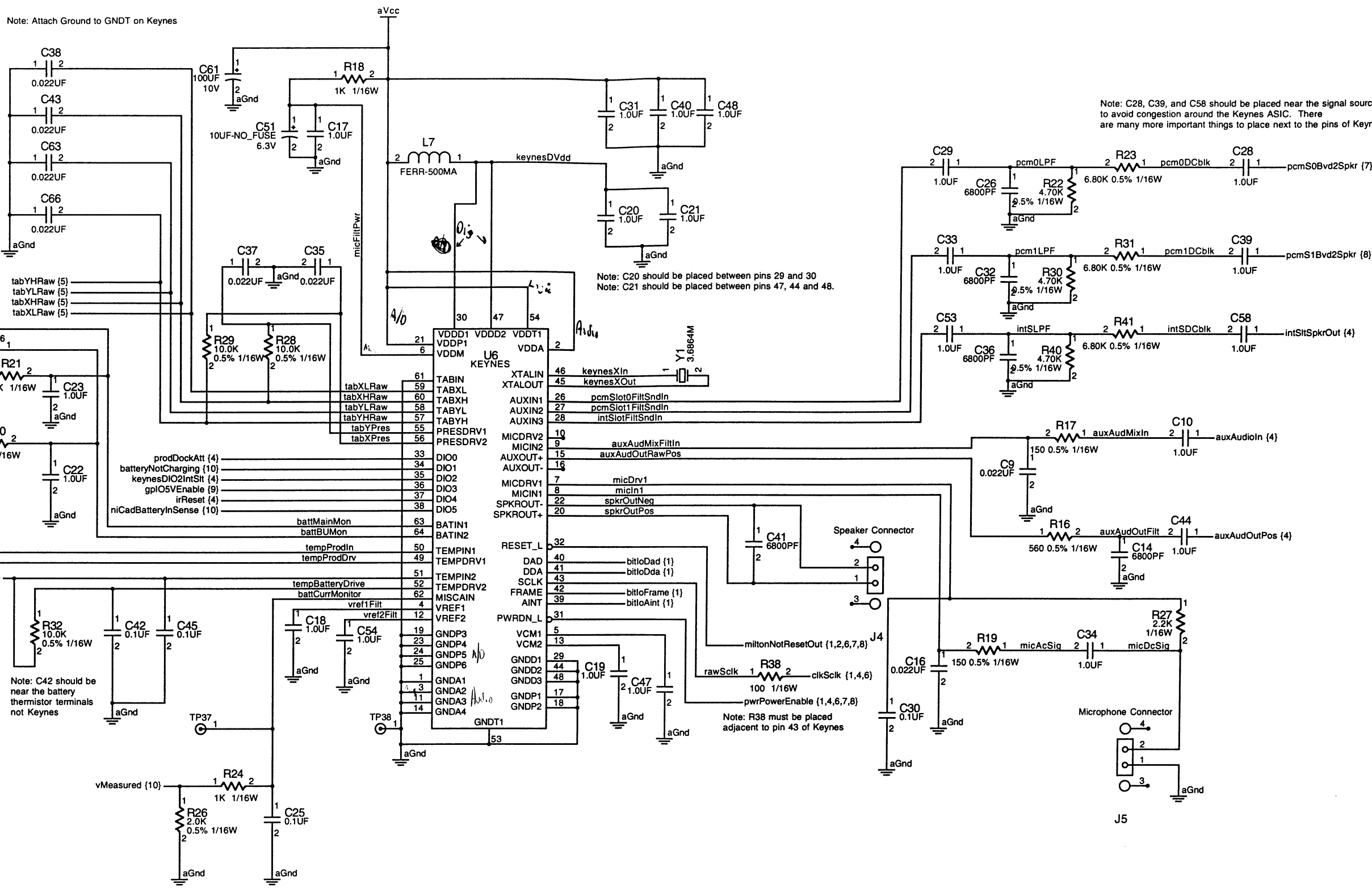
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Note: Attach Ground to GNDT on Keynes

Note: AD converter for absolute measurement must be calibrated. TP35-37 are used for this purpose. 6.125V ±1.0% must be applied to TP35 and TP36 separately. Each pin must then be measured and a calibration gain constant (relative to the ideal 7.0V must be created and stored. For TP36, 1.75V ±1.0% must be applied and then the same process must be followed. All voltages should be applied relative to TP38 (aGnd). Note: Power supply for voltage-above must be capable of supplying enough current (100mA) to prevent TP35 deviation of voltage due to main system voltages. Voltage should be measured AT EACH TP to prevent error due to wire losses between power supply and system. All three voltages may be applied simultaneously.

Note: C28, C39, and C58 should be placed near the signal source to avoid congestion around the Keynes ASIC. There are many more important things to place next to the pins of Keynes.

Note: C20 should be placed between pins 29 and 30  
Note: C21 should be placed between pins 47, 44 and 48.



Note: C55 does should be near RT1 not Keynes

Note: C42 should be near the battery thermistor terminals not Keynes

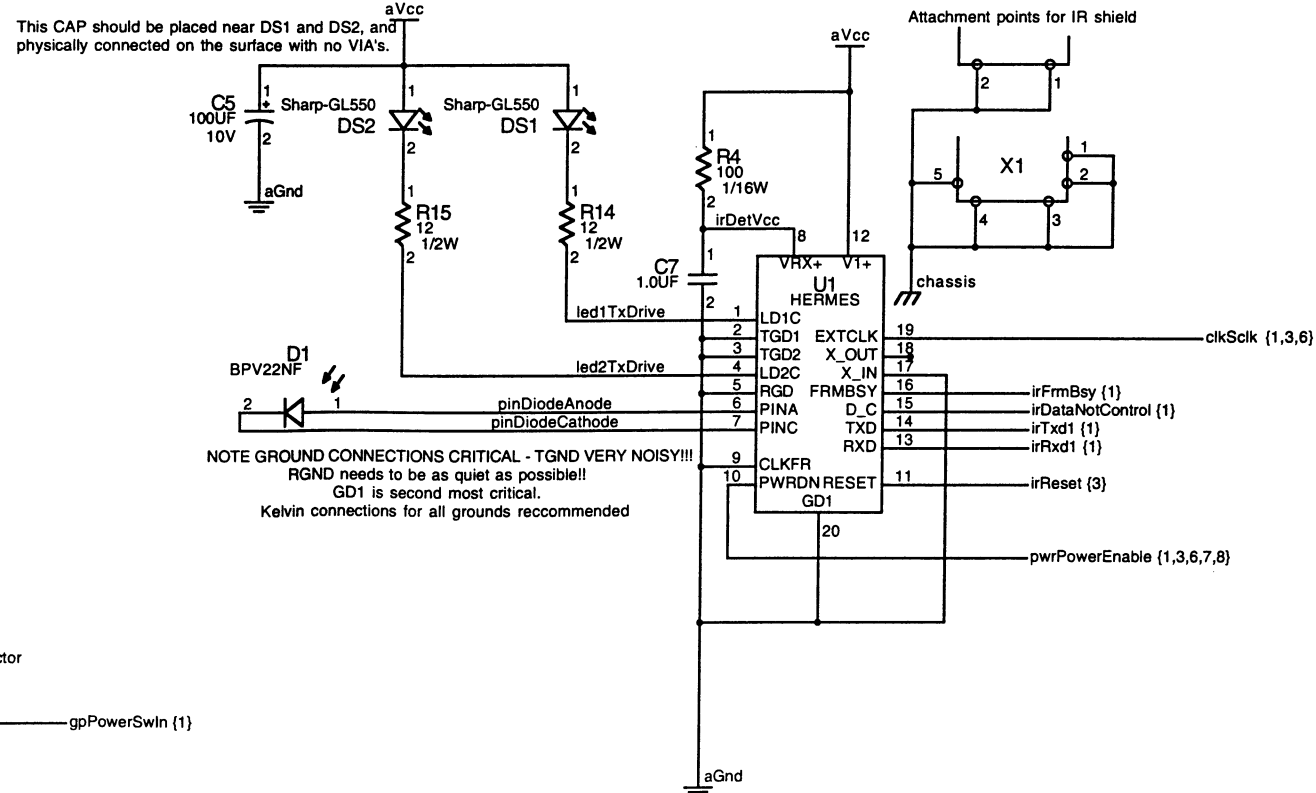
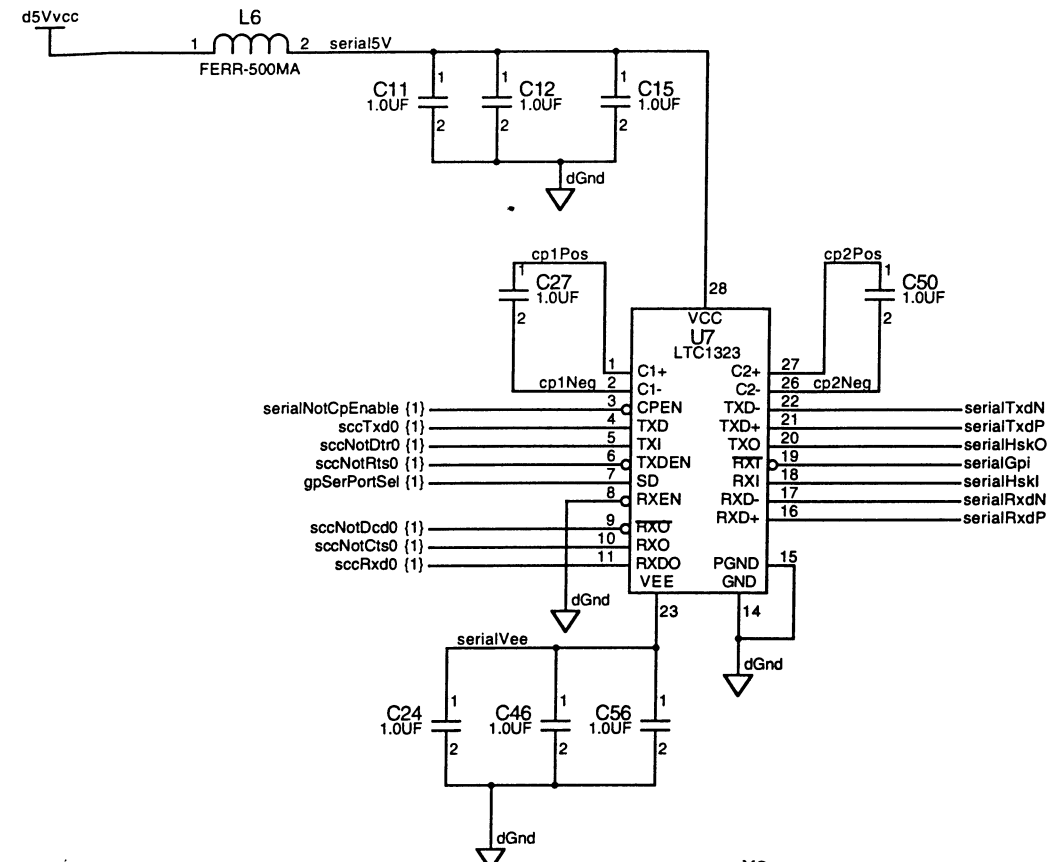
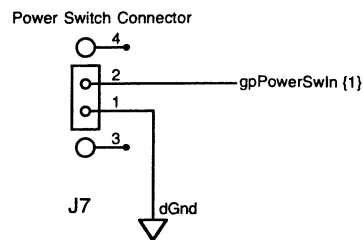
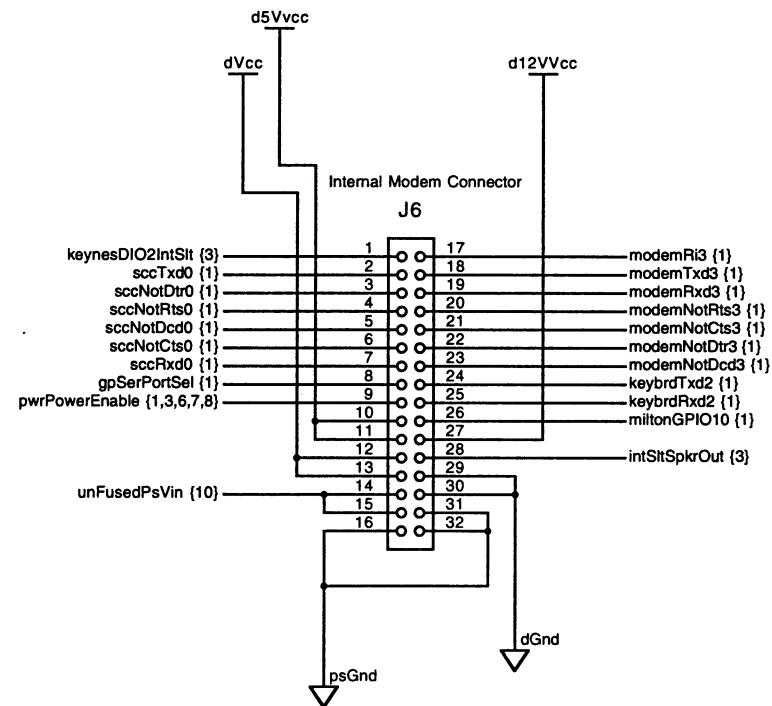
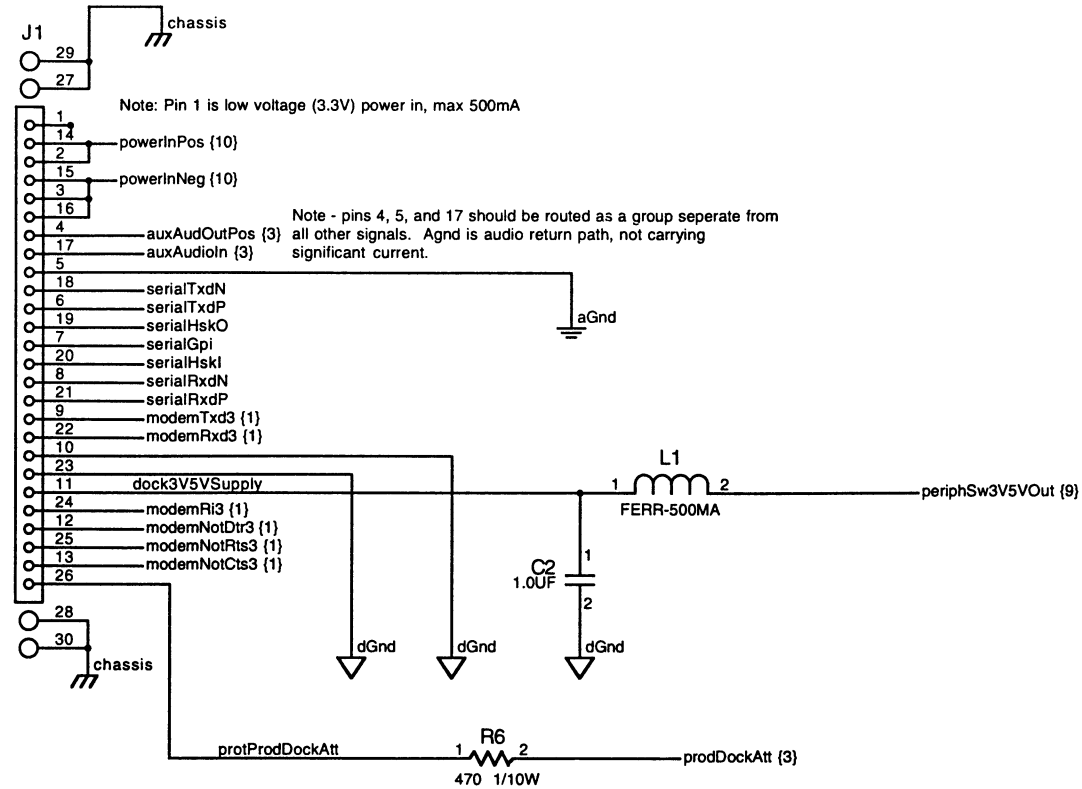
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Docking Connector



This CAP should be placed near DS1 and DS2, and physically connected on the surface with no VIA's.

NOTE GROUND CONNECTIONS CRITICAL - TGND VERY NOISY!!!  
 RGND needs to be as quiet as possible!!  
 GD1 is second most critical.  
 Kelvin connections for all grounds recommended

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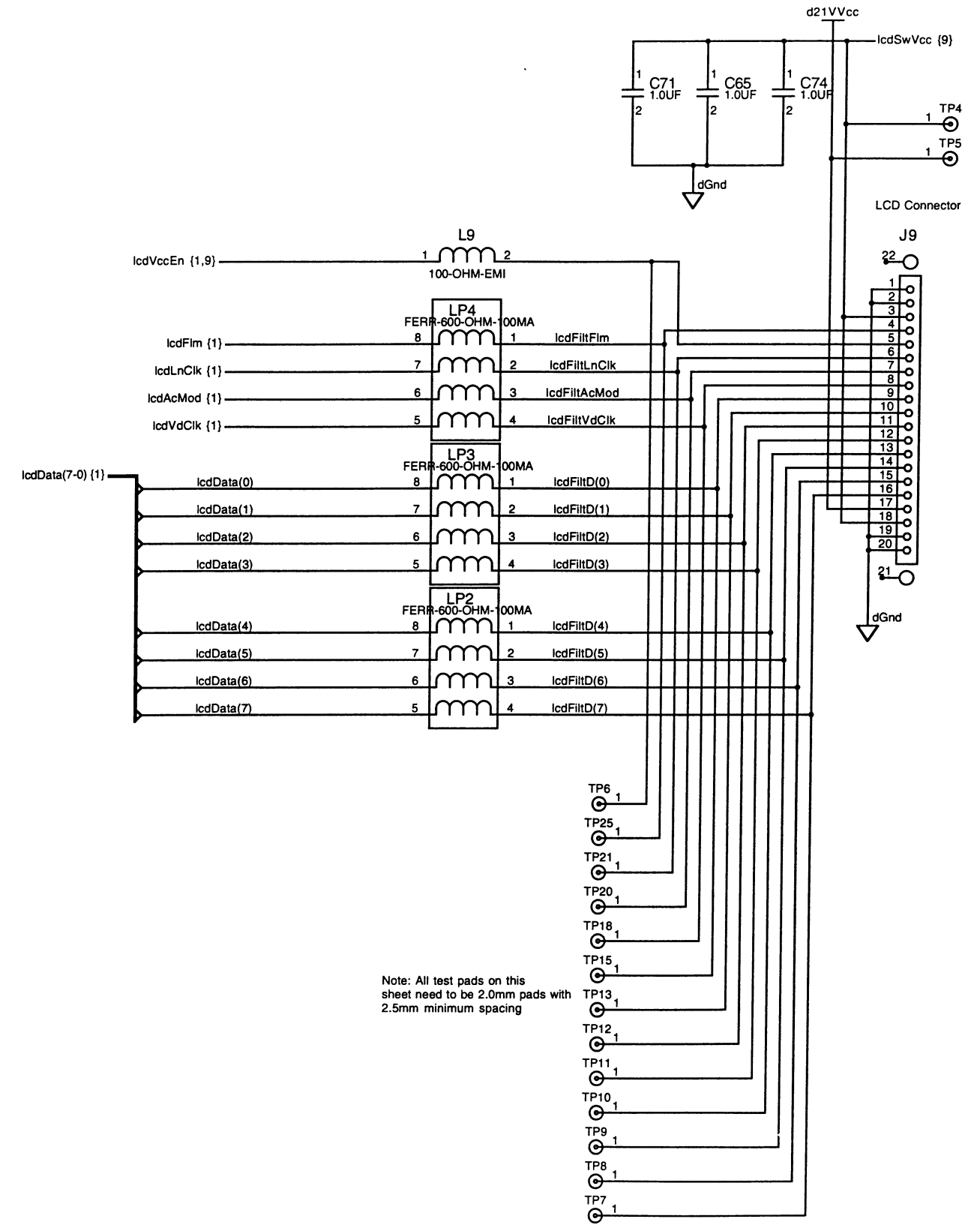
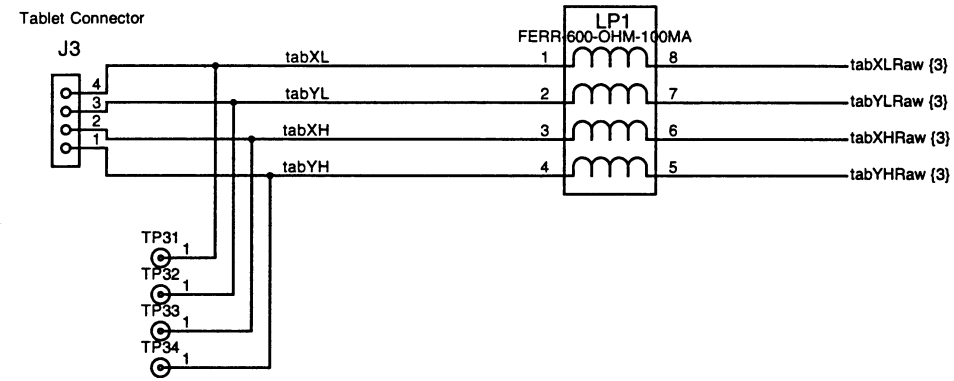
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D  
C  
B  
A

D  
C  
B  
A



Note: All test pads on this sheet need to be 2.0mm pads with 2.5mm minimum spacing

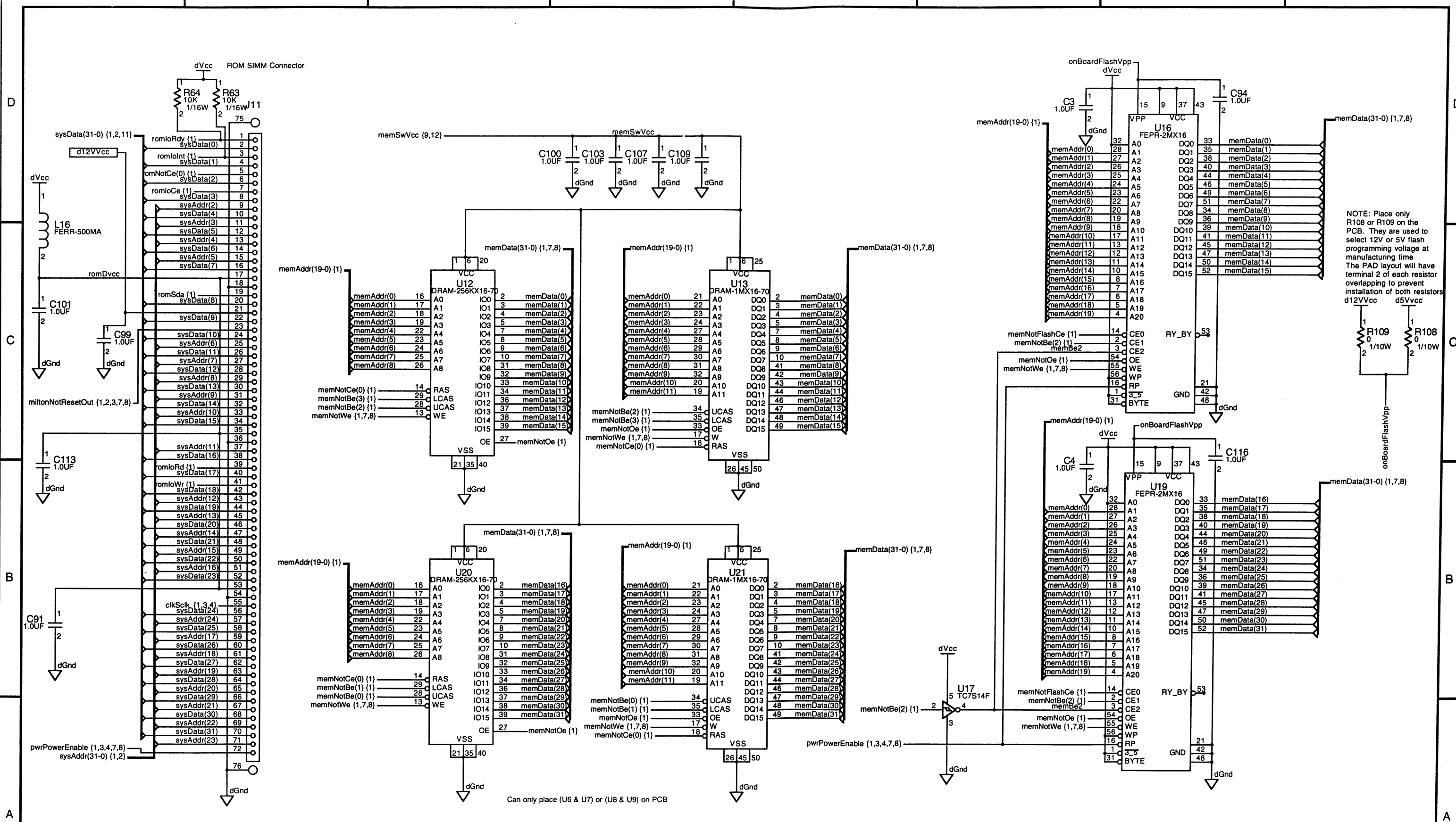
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8 7 6 5 4 3 2 1



NOTE: Place only R108 or R109 on the PCB. They are used to select 12V or 5V flash programming voltage at manufacturing time. The PAD layout will have terminal 2 of each resistor overlapping to prevent installation of both resistors d12Vcc d5Vcc

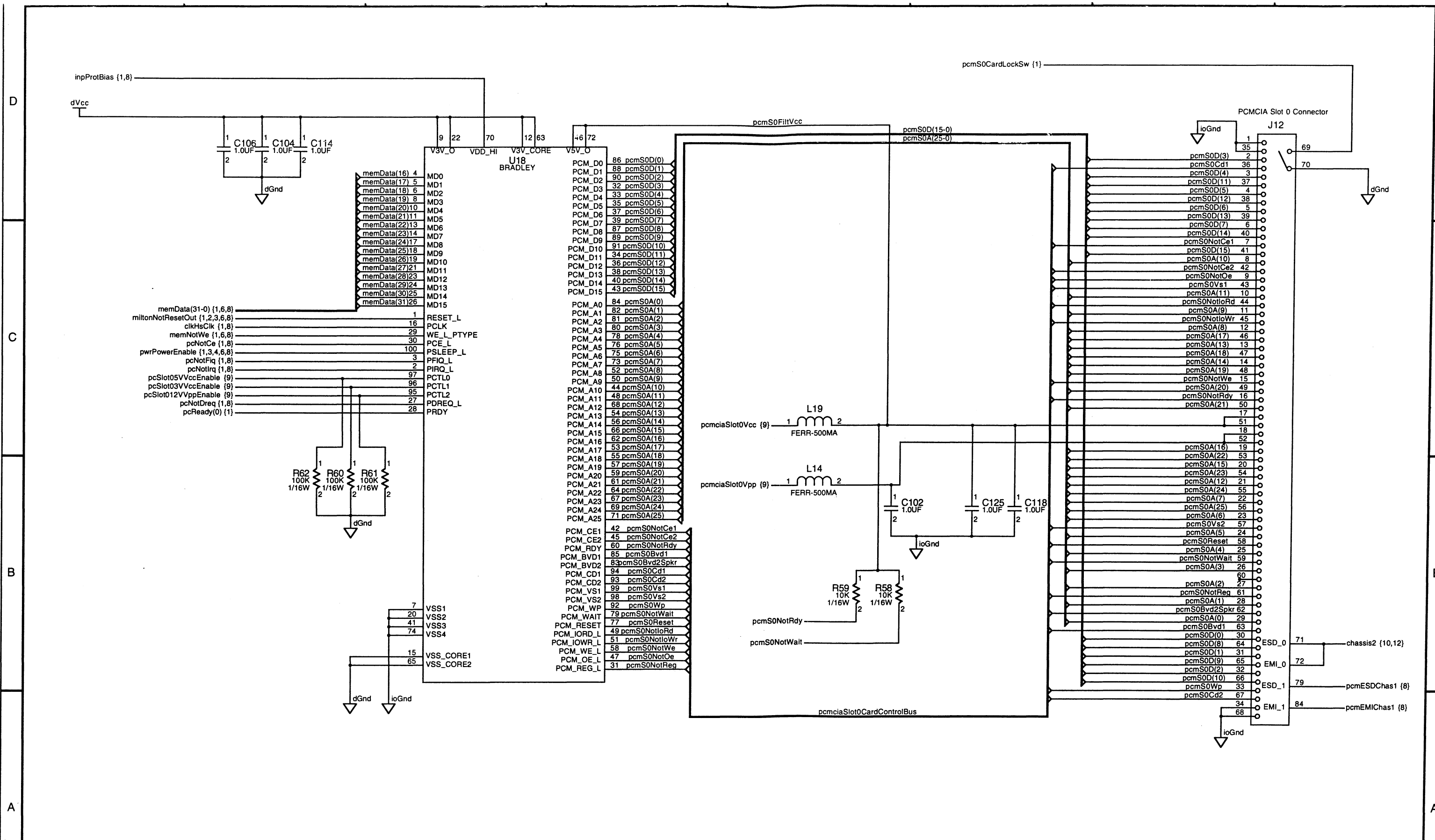
Can only place (U6 & U7) or (U8 & U9) on PCB

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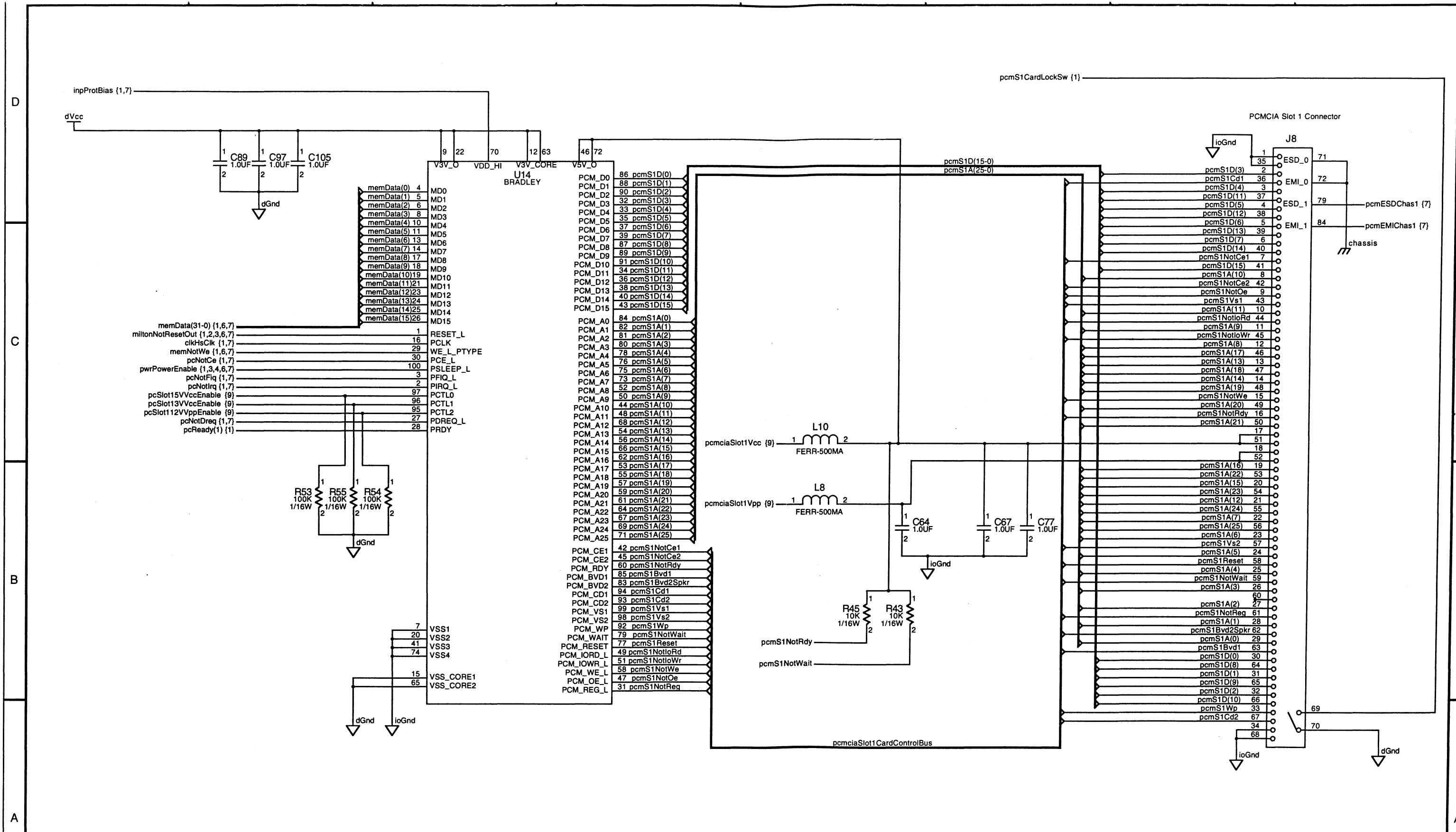


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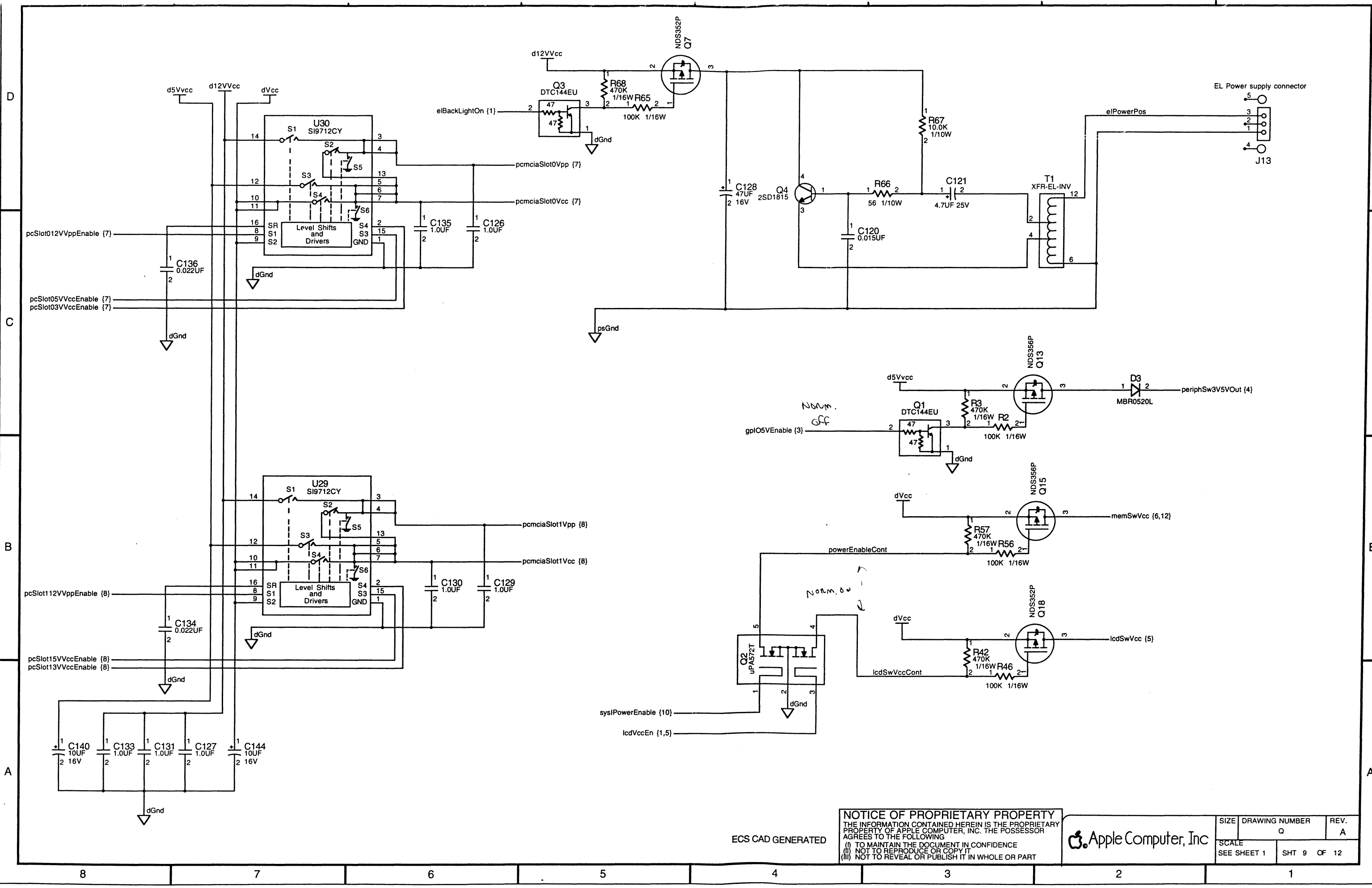


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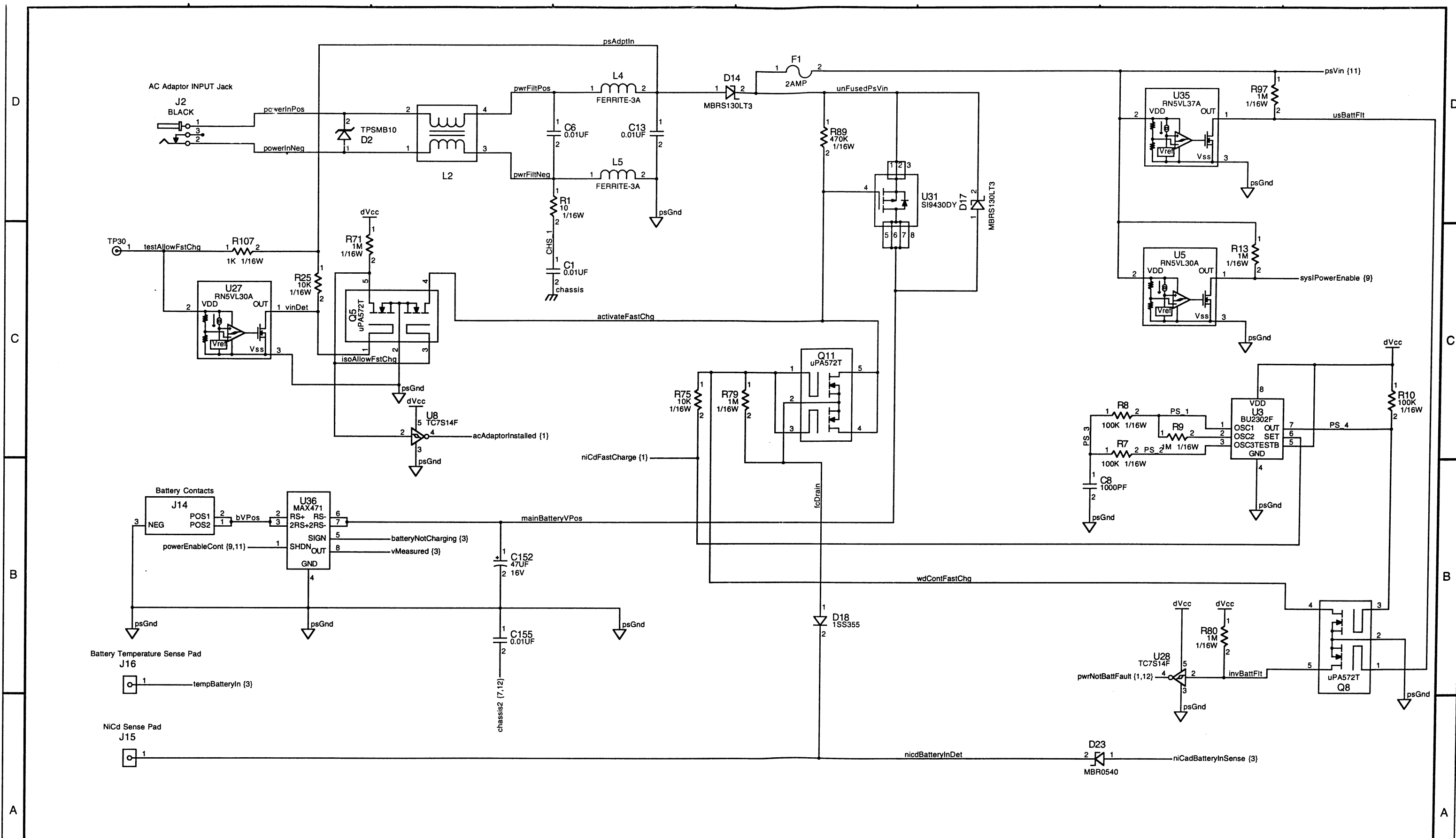




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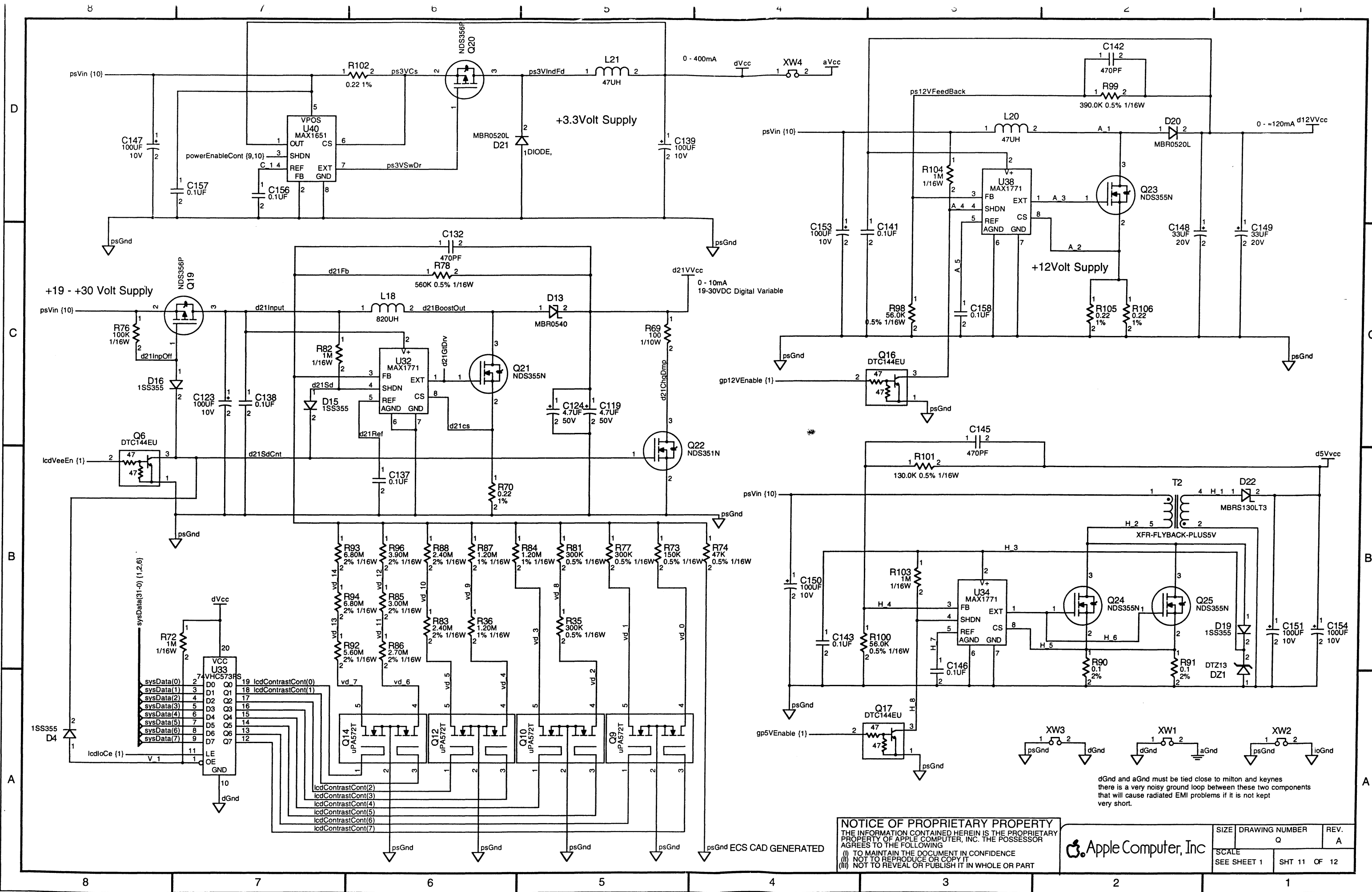
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8 7 6 5 4 3 2 1



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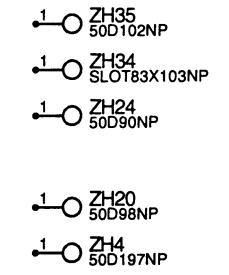
REFERENCE DESIGNATOR LOCATIONS

C1	10-C5	C109	6-D5	J5	3-B2	R30	3-C2	U7	4-C3	ZH34	12-D3
C2	4-C6	C113	6-B8	J6	4-B7	R31	3-C2	U9	1-A5	ZH35	12-D3
C3	6-D3	C114	7-D7	J7	4-A5	R32	3-B7	U10	2-B6		
C4	6-B3	C116	6-B2	J8	8-A1	R33	3-C7	U12	6-B6		
C5	4-B4	C118	7-B3	J9	5-C1	R34	1-C2	U13	6-B5		
C6	10-D5	C119	11-C5	J10	2-B3	R35	11-B5	U14	8-A6		
C7	4-B3	C120	9-C4	J11	6-A7	R36	11-B6	U16	6-C2		
C8	10-B3	C121	9-D3	J12	7-A1	R37	3-B8	U17	6-A3		
C9	3-C2	C123	11-C7	J13	9-D1	R38	3-B4	U18	7-B6		
C10	3-C2	C124	11-C5	J14	10-B8	R39	1-B2	U19	6-A2		
C11	4-D3	C125	7-B3	J15	10-A8	R40	3-C2	U20	6-A6		
C12	4-D3	C126	9-C6	J16	10-B8	R41	3-C2	U21	6-A5		
C13	10-D5	C127	9-A7			R42	9-A3	U27	10-C8		
C14	3-B2	C128	9-D4	L1	4-C6	R43	8-B4	U28	10-B2		
C15	4-D3	C129	9-B6	L2	10-D6	R44	2-B3	U29	9-B7		
C16	3-B3	C130	9-B6	L4	10-D5	R45	8-B4	U30	9-C7		
C17	3-D6	C131	9-A8	L5	10-D5	R46	9-A3	U31	10-C4		
C18	3-B6	C132	11-C6	L6	4-D4	R48	11-B8	U32	11-C6		
C19	3-B4	C133	9-A8	L7	3-D5	R49	1-B8	U33	11-A7		
C20	3-C4	C134	9-B8	L8	8-B4	R50	11-D2	U34	11-B3		
C21	3-C4	C135	9-C6	L9	5-C3	R52	1-B4	U35	10-D2		
C22	3-C7	C136	9-C8	L10	8-C4	R53	8-B7	U36	10-B7		
C23	3-C7	C137	11-B6	L11	2-D4	R54	8-B6	U38	11-C3		
C24	4-C3	C138	11-C7	L12	1-D1	R55	8-B7				
C25	3-A6	C139	11-D5	L14	7-B4	R56	9-B3				
C26	3-D2	C140	9-A8	L16	6-C8	R57	9-B3				
C27	4-D3	C141	11-C3	L18	11-C6	R58	7-B4				
C28	3-D1	C142	11-D2	L19	7-C4	R59	7-B4				
C29	3-D3	C143	11-B4	L20	11-D3	R60	7-B7				
C30	3-B3	C144	9-A7	L21	11-D5	R61	7-B6				
C31	3-D4	C145	11-C3			R62	7-B7				
C32	3-C2	C146	11-A3			R63	6-D7				
C33	3-C3	C147	11-D8			R64	6-D8				
C34	3-B2	C148	11-C2			R65	9-D5				
C35	3-C5	C149	11-C1			R66	9-D4				
C36	3-C2	C150	11-B4			R67	9-D3				
C37	3-C6	C151	11-B1			R68	9-D5				
C38	3-D7	C152	10-B6			R69	11-C5				
C39	3-C1	C153	11-C4			R70	11-B6				
C40	3-D4	C154	11-B1			R71	10-C7				
C41	3-B4	C155	10-B6			R72	11-B7				
C42	3-B6	C156	11-D7			R73	11-B8				
C43	3-D7	C157	11-D7			R74	11-B4				
C44	3-B2	C158	11-C3			R75	10-C5				
C45	3-B6					R76	11-C8				
C46	4-C3					R77	11-B5				
C47	3-B4					R78	11-C6				
C48	3-D4					R79	10-C4				
C49	3-B8					R80	10-B2				
C50	4-D2					R81	11-B5				
C51	3-D6					R82	11-C7				
C52	1-D3					R83	11-B6				
C53	3-C3					R84	11-B6				
C54	3-B5					R85	11-B6				
C55	3-B8					R86	11-B6				
C56	4-C3					R87	11-B6				
C57	1-B2					R88	11-B6				
C58	3-C1					R89	10-D4				
C60	1-D2					R90	11-A2				
C61	3-D6					R91	11-A2				
C63	3-D7					R92	11-B7				
C64	8-B4					R93	11-B7				
C65	5-D2					R94	11-B7				
C66	3-D7					R96	11-B6				
C67	8-B3					R97	10-D2				
C68	1-D2					R98	11-C3				
C69	1-D2					R99	11-D2				
C71	5-D2					R100	11-B3				
C74	5-D2					R101	11-B3				
C75	1-C3					R102	11-D7				
C77	8-B3					R103	11-B3				
C78	2-D6					R104	11-D3				
C79	2-D5					R105	11-C2				
C80	2-D5					R106	11-C2				
C81	2-D5					R107	10-C7				
C84	1-B1					R108	6-C1				
C85	1-B2					R109	6-C1				
C88	1-C1										
C89	8-D7										
C91	6-B8										
C94	6-D2										
C97	8-D7										
C99	6-C8										
C100	6-D5										
C101	6-C8										
C102	7-B4										
C103	6-D5										
C104	7-D7										
C105	8-D7										
C106	7-D7										
C107	6-D5										

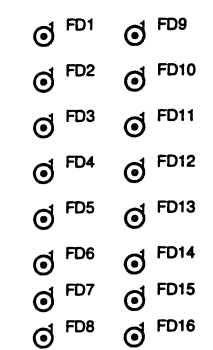
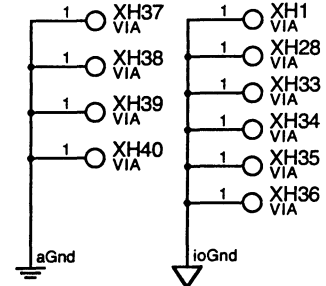
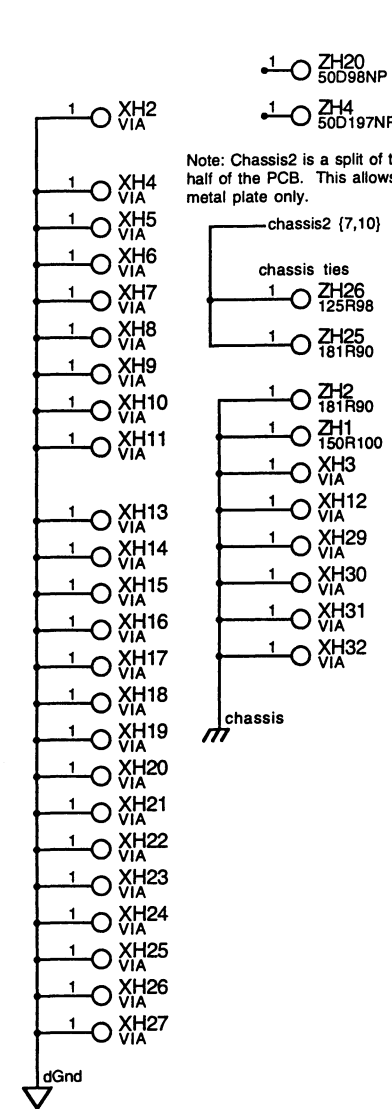
REFERENCE DESIGNATIONS

LAST USED	NOT USED
C158	C59
	C62
	C70
	C72-73
	C76
	C82-83
	C86-87
	C90
	C92-93
	C95-96
	C98
	C108
	C110-112
	C115
	C117
	C122
D23	D5-6
	D9-12
DS2	
DZ1	
F1	
FD16	
J16	
L21	L3
	L13
	L15
	L17
LP4	
Q25	
R109	R47
	R51
	R95
RT1	
S1	
T2	
TP38	
U40	U8
	U11
	U15
	U22-26
	U37
	U39
W10	W1-2
	W4
	W8
X2	
XH40	
XW4	
Y2	
ZH35	ZH3
	ZH5-19
	ZH21-23
	ZH27-33

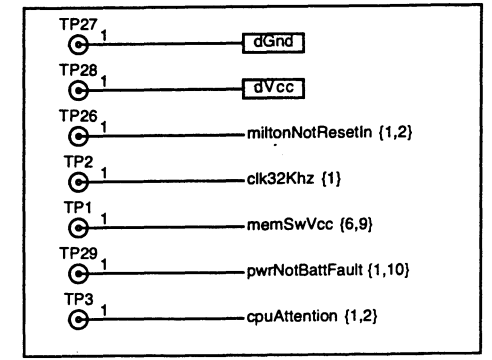
Tooling Holes, not plated



Note: Chassis2 is a split of the chassis net for the lower half of the PCB. This allows chassis connection through the metal plate only.



Special System Assembly Test Points



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SIZE	DRAWING NUMBER	REV.
	Q	A
SCALE	SEE SHEET 1 SHT 12 OF 12	